

FIG. 12 -- FULL SIZE FOIL PATTERN for memory board.

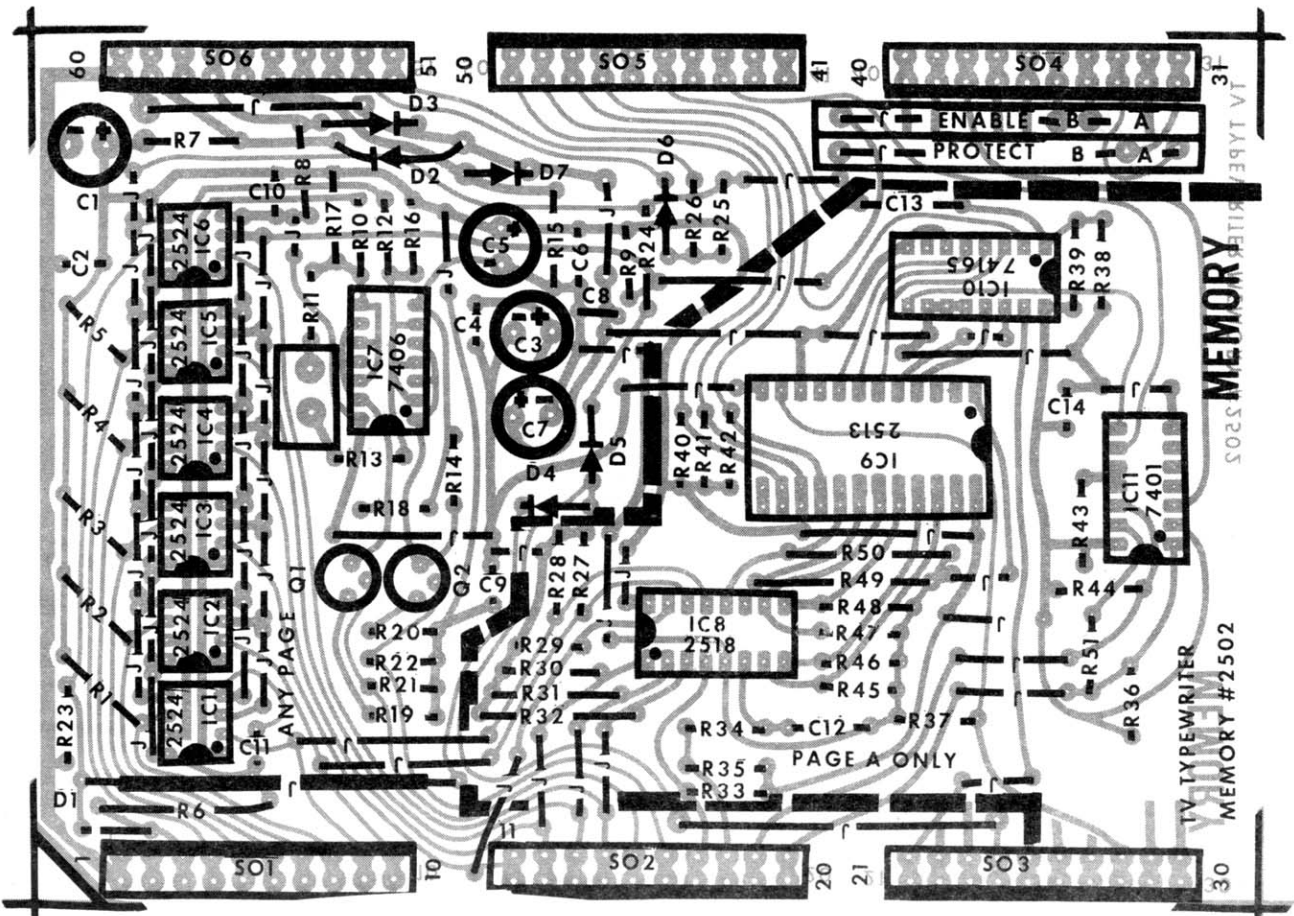


FIG. 13 -- PARTS PLACEMENT on memory circuit board.