

ABEL(tm) Version 1.13 - Doc
12 to 4 multiplexer
Equations for Module PLD81

Device IC1

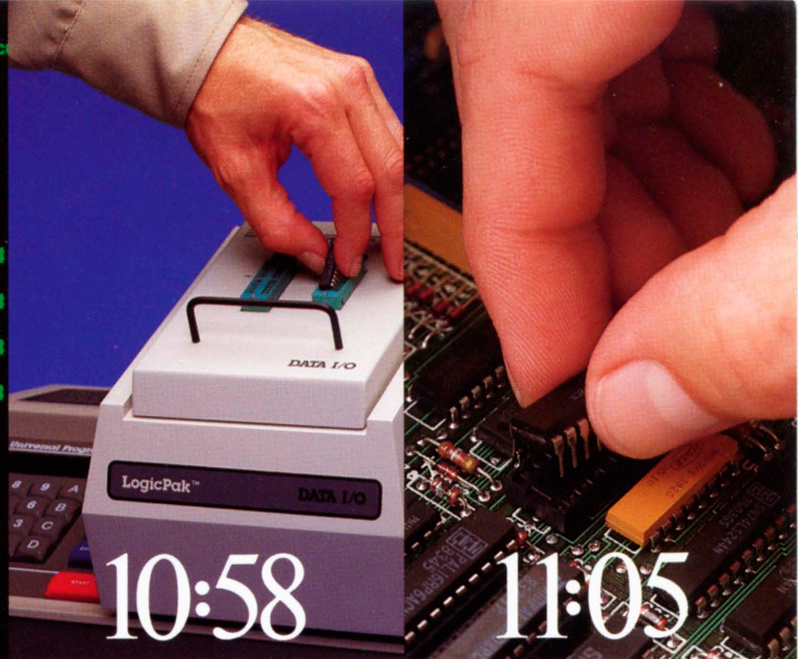
Reduced Equations:

$$Y3 = (A3 \& \uparrow S1 \& \uparrow S2 \&$$

$$Y2 = (A2 \& \uparrow S1 \& \uparrow S2 \&$$

$$Y1 = (A1 \& \uparrow S1 \& \uparrow S2 \&$$

$$Y8 = (A8 \& \uparrow S1 \& \uparrow S2 \&$$



8:05

9:37

10:58

11:05

CONCEPT TO SILICON IN 3 HOURS FLAT.

ALL THE BENEFITS OF CUSTOM DESIGN WITHOUT THE WAIT. Sometimes, the only way to optimize a design is by using custom parts. But some application-specific integrated circuits (ASICs), like gate arrays, can take weeks to develop.

Programmable Logic Devices (PLDs) may be the right solution for your application. They offer low risk and high design flexibility to reduce your development costs.

LOGIC CONCEPT TO FINAL DEVICE RIGHT AT YOUR DESK. Data I/O's Personal Silicon Foundry gives you all the tools you need to take logic from concept to programmed device in just hours. The Personal Silicon Foundry, based on an IBM® Personal Computer, automates time-consuming tasks such as design, logic reduction, test vector generation and verification. You can easily experiment to optimize your design and quickly test each iteration in an actual circuit.

FOUR WAYS TO EXPRESS YOUR DESIGN. With our FutureNet DASH™ schematic design package and ABEL™, the industry-standard logic



The Personal Silicon Foundry™

language, you can express your concept in any combination of schematics, truth tables, state diagrams or Boolean equations — whichever you prefer.

CREATE A FULLY TESTABLE DEVICE IN MINUTES. Because simulation is an integral part of the Personal Silicon Foundry, you can segment your logic circuit, develop test vectors and debug the design in one process. You can also automatically analyze design testability and generate device test vectors. The result: You easily produce a fully testable PLD.

SUPPORTS PLDS OF YOUR CHOICE. You have the flexibility to choose any PLD from virtually any manufacturer.

Select one of two Data I/O logic programmers to match your device requirements. Both give you full-functional testing and easy operation from the PC keyboard.

AUTOMATICALLY DOCUMENTS THE ENTIRE PROCESS. Throughout design, simulation, programming and testing, the Personal Silicon Foundry generates all necessary documentation. Your PLD is ready for the production line just hours after your concept is developed.

CONFIGURE YOUR OWN PERSONAL SILICON FOUNDRY. ASK US HOW. As the world leaders in PC-based CAE and PLD support tools, Data I/O and FutureNet* can help you tailor your own Personal Silicon Foundry. Call us today, toll free **1-800-426-1045** and ask for your free guide to PLDs and the Personal Silicon Foundry.

Capturing the power of PLDs is that simple. And fast.

*FutureNet is a Data I/O company.

YOUR GUIDE
TO THE
PERSONAL
SILICON FOUNDRY

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