Interoffice Correspondence

To: J. Bilbro   M. Moore   Date: **August 18, 1981**
      J. Fjellberg   G. Tannheimer
      C. Golm   B. To
      G. James   CC:

From: Wayne Paulson

Subject: <u>MINUTES OF PROGRAMMABLE LOGIC MEETING, 8/13/81</u>

The purpose of the meeting was to resolve the remaining open
questions surrounding the programmable logic program so that
it can be scheduled. The key points which Engineering raised
were:

1. What devices is this set of programming electronics expected
   to cover? It was resolved that this particular package was
   expected to program Bipolar fuse link logic devices similar
   in nature, although not necessarily identical to, those
   devices on the market today. There are however devices
   which are currently in design which will not be covered by
   this pack and will require a new design effort.

2. Engineering has purposely chosen to consider the programming
   electronics and the developmental software is two separate
   projects. The software was intended to development data to put
   into RAM, the hardware is expected to take that data and program
   a device with it. It was concluded that there is no problem with
   treating each section as a design effort, but some document should
   tie all of the efforts together.

3. The requirements for the proprietary software are very unclear to
   Engineering. The product proposal description of the proprietary
   software is too vague to be useful as a guideline to design the
   system. It was decided that a request would be made in writing
   to Vic with a copy to Sumner Averett requesting that he do a
   feasibility study of a generalized developmental language for
   programmable logic.

The points which Marketing raised were as follows:

1. The lack of a focal point in Engineering on the project makes
   coordination difficult. That problem will be addressed in early
   September.

2. Specifications have never been signed off for the logic support system.
   It was decided that new draft specifications would be completed, including

a section to tie all the various elements of the system together. During the week of August 17th a meeting to resolve any questions with the spec will be held. The target is to have full sign-off by August 21st.

3. There are no projects initiated on logic devices other than the Signetics IFL family and the MMI PALs. This is a remaining open action item.

4. It appears that information is not flowing in all the directions it should be. A copy of the priorities for the logic devices is attached, along with the system description of the logic support system to help alleviate this problem.

Wayne Paulson

/bam

Attachments