

Interoffice Correspondence

- | | |
|---|---|
| <input type="checkbox"/> For Your Comments | <input type="checkbox"/> For Your Information |
| <input type="checkbox"/> For Your Approval | <input type="checkbox"/> Note & Return |
| <input type="checkbox"/> Please Take Action | <input type="checkbox"/> Note & File |
| <input type="checkbox"/> Call Me | <input type="checkbox"/> For Your Signature |
| <input type="checkbox"/> See Me | <input type="checkbox"/> Please Advise Status |
| <input type="checkbox"/> Per Our Conversation | <input type="checkbox"/> Per Your Request |
| <input type="checkbox"/> Read, Initial & Return | <input type="checkbox"/> Please Advise
Appropriate Employees
In Your Department |

To: Wayne Paulson Charles Golm Date: April 28, 1982
George James Russ dePina
Mike Holley Bill To
CC:

From: Vic Belmondo

Subject: ABEL Task Force Meeting Action Items

1. Does PALASM as implemented meet customer needs? If not, prepare list of suggested changes. Assigned to Charlie and Wayne for completion 5-1.
2. Can Signetics and A.M.D. devices "fit" with current PALASM? List limitation, incompatibilities, etc. Completion 5-7, assigned to George and Bill.
3. Does ABEL, new or old definition, have a future? List disadvantages and advantages. Assigned to Russ and Wayne for completion 5-14.
4. Develop plan to get information and define future paths. Consider consultants, trips, meetings, etc. Assigned to Vic, Wayne and Mike for completion 5-14-82.

Please distribute results of action items when available to other team members on completion dates.

VEB:l c

VEB

MICROPROGRAMMING PROCESSOR CONTROL

DESIGN SPECIFICATION — PAL16R4

***** P00278 *****
 YOUR PAL DESIGN SPEC IS ACCEPTED AS MONOLITHIC MEMORIES BIT
 PATTERN NUMBER P00278 . YOUR PASSWORD IS P00001 .
 PLEASE CONFIRM THAT THESE EQUATIONS ARE CORRECT. MONOLITHIC
 MEMORIES WILL NOT PROCESS A PRODUCTION ORDER WITHOUT PROPER
 APPROVAL. USE BP NO. P00278 WHEN PLACING A PROGRAMMING ORDER.

CUSTOMER CONFIRMATION

I HAVE REVIEWED THIS PAL DESIGN SPECIFICATION AND UNDERSTAND
 THAT PALS ORDERED BY BIT PATTERN NUMBER P00278 WILL BE
 PROGRAMMED AS SPECIFIED BELOW.

CUSTOMER COMPANY NAME _____ CITY/STATE _____
 AUTHORIZED SIGNATURE _____ DATE _____
 NAME (PRINT) _____ TITLE _____

RETURN TO MMI 1165 E. AROQUES SUNNYVALE, CA 94086 ATTN: _____

PAL16R4
 P00001
 MICROPROGRAMMING PROCESSOR CONTROL
 SUNNYVALE CA
 CK A0 A1 A2 A3 I0 I1 I2 I3 GND
 /E B0 B1 B3 R2 R1 R0 B2 B3 VCC

PAL DESIGN SPECIFICATION
 S.K. 3/8/80

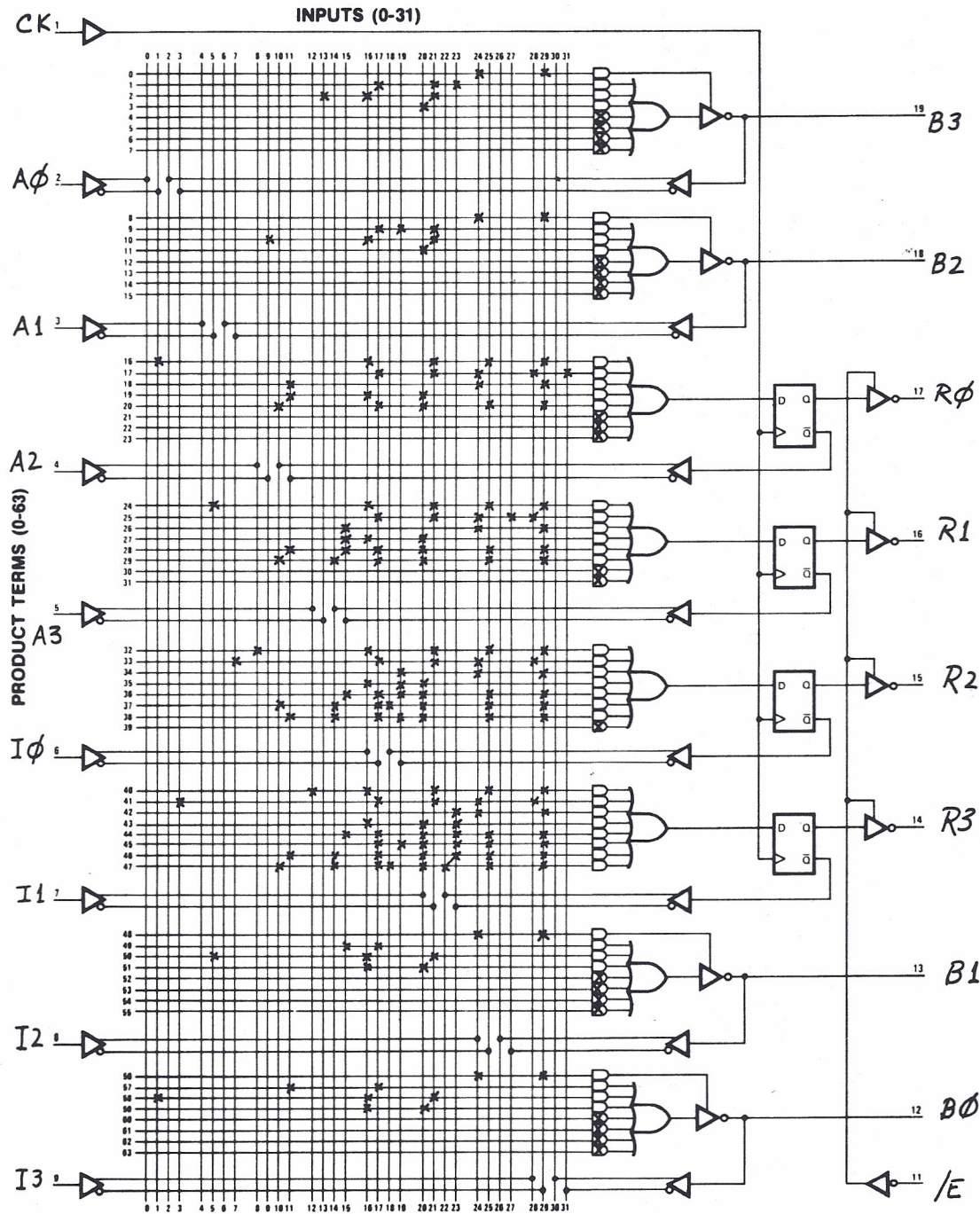
IF (/I3/I2) /B3 = /I1/I0/R3 + /I1/I0/A3 + I1
 IF (/I3/I2) /B2 = /I1/I0/R2 + /I1/I0/A2 + I1
 IF (/I3/I2) /B1 = /I0/R1 + /I1/I0/A1 + I1/I0
 IF (/I3/I2) /B0 = /I0/R0 + /I1/I0/A0 + I1/I0
 /R3 = /I3/I2/I1/I0/A3 + I3/I2/I1/I0/B3 +
 /I3/I2/R3 + I1/I0/R3 +
 /I3/I2/I1/I0/R3/R1 + /I3/I2/I1/I0/R3/R2 +
 /I3/I2/I1/I0/R3/R1/R0 +
 /I3/I2/I1/I0/R3/R2/R1/R0
 /R2 = /I3/I2/I1/I0/A2 + I3/I2/I1/I0/B2 +
 /I3/I2/R2 + I1/I0/R2 + /I3/I2/I1/I0/R2/R1 +
 /I3/I2/I1/I0/R2/R1/R0 + /I3/I2/I1/I0/R2/R1/R0
 /R1 = /I3/I2/I1/I0/A1 + I3/I2/I1/I0/B1 + /I3/I2/R1 +
 I1/I0/R1 + /I3/I2/I1/I0/R1/R0 +
 /I3/I2/I1/I0/R1/R0
 /R0 = /I3/I2/I1/I0/A0 + I3/I2/I1/I0/B0 +
 /I3/I2/R0 + I1/I0/R0 + /I3/I2/I1/I0/R0

DESCRIPTION:

THIS EXAMPLE ILLUSTRATES THE USE OF THE PAL AS A
 SEQUENTIAL AND COMBINATORIAL LOGIC ELEMENT.

Monolithic  Memories.

LOGIC DIAGRAM — PAL16R4



Monolithic **MM** Memories

FIELD PROGRAMMABLE LOGIC SEQUENCER

82S104 (O.C.)/82S105 (T.S.)

INTEGRATED FUSE LOGIC
SERIES 28

FPLS LOGIC DIAGRAM

