

REVISIONS

REV	DESCRIPTION	APPROVAL	DATE
--	ECN 200443-2		

THIS MODIFICATION ADDS THE CAPABILITY OF RUNNING CP/M TO THE CPU BOARD (PRINTED CIRCUIT BOARD P/N 101031), REV 0.2, 0.3, D, AND E.

PRODUCTION RELEASE
DATE RECEIVED MAY 26 1981

APPR.	DATE	PolyMorphic Systems 460 Ward Drive Santa Barbara Ca 93111 (805) 967-2351
APPR.		
APPR. <i>FEA</i>	<i>8-1-81</i>	TITLE <i>MJD 101031 - CPU</i> <i>CPU REV 0.2 THRU REV E</i>
CHECK <i>FEA</i>	<i>8-1-81</i>	
WRITTEN BY	NUMBER <i>130548</i>	REV SHEET OF <i>1 5</i>

THE FOLLOWING STEPS ADD THE OUTPUT BIT SWITCHED BY INPUT PORTS. THIS BIT (INVERTED) IS SENT OUT TO BUS PIN 16 THROUGH AN OPEN COLLECTOR BUFFER AND IS ALSO USED FOR THE ONBOARD ROM/RAM DISABLE LOGIC.

1. CUT TRACE FROM IC13-1 TO IC11-5.
2. CUT TRACE FROM IC17-12 TO IC11-6.
3. CUT TRACE FROM IC11-4 TO IC10-15 (1-3: FREE IC11).
4. CUT TRACE FROM IC12-10 TO IC19-2 (REMOVE 1 LS LOAD).
5. CUT TRACE FROM IC19-3 TO IC10-12.
6. CUT TRACE FROM IC13-7 TO IC12-3.
7. CUT TRACE FROM IC20-12 TO IC20-1.
8. CUT TRACE FROM IC12-6 TO IC11-11.
9. CUT TRACE FROM "H" TO GND.
10. JUMPER FROM IC38-5 TO IC10-15 (POC-).
11. JUMPER FROM IC36-9 TO IC11-5 (PS2-).
12. JUMPER FROM IC12-10 TO IC11-6 (DBIN-).
13. JUMPER FROM IC11-4 TO IC10-12.
14. JUMPER FROM IC36-7 TO IC12-3 (PS3-).
15. JUMPER FROM IC12-11 TO IC20-12 (DBIN+).
16. JUMPER FROM IC10-9 TO IC11-11 (CPM-).
17. JUMPER FROM "H" TO FEEDTHRU: (IC35-5).
18. JUMPER FROM "C" TO IC9-9 (CPM-).
19. JUMPER FROM IC9-8 TO P1-16.

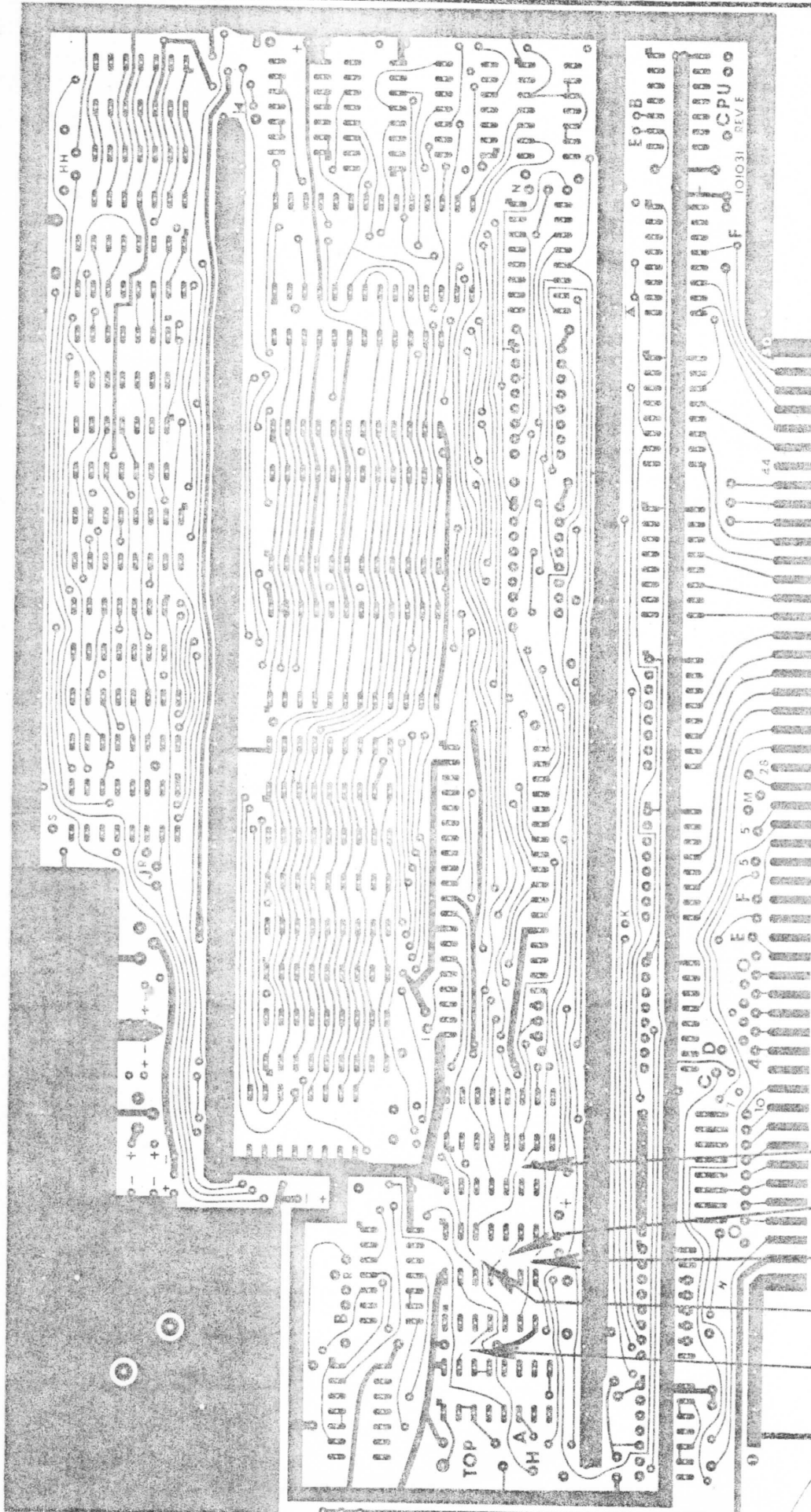
NOTE:

**SCRAPE SOLDERMASK OFF THE FEEDTHRU HOLE WITH A SHARP KNIFE BEFORE SOLDERING TO PAD.

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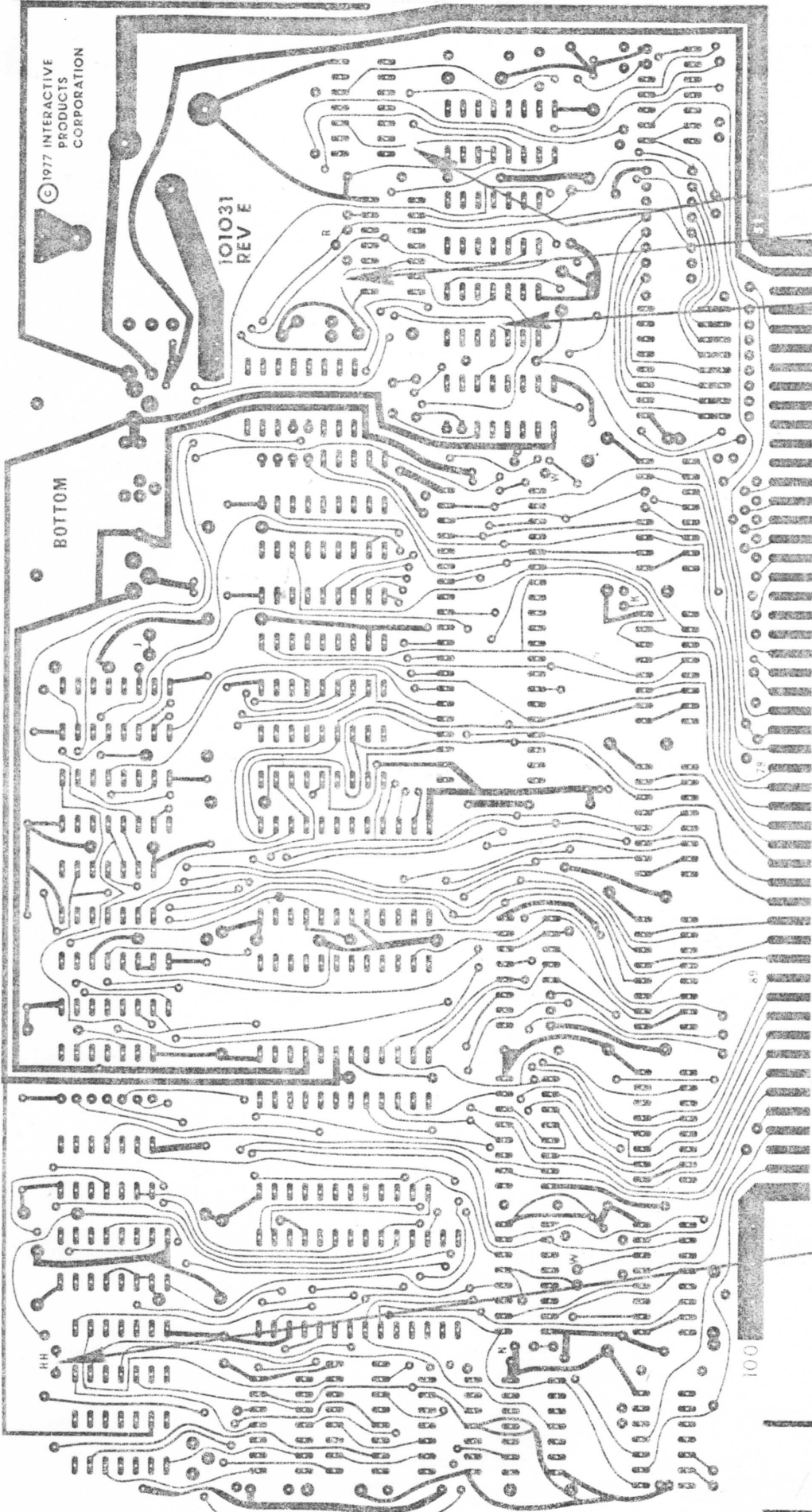
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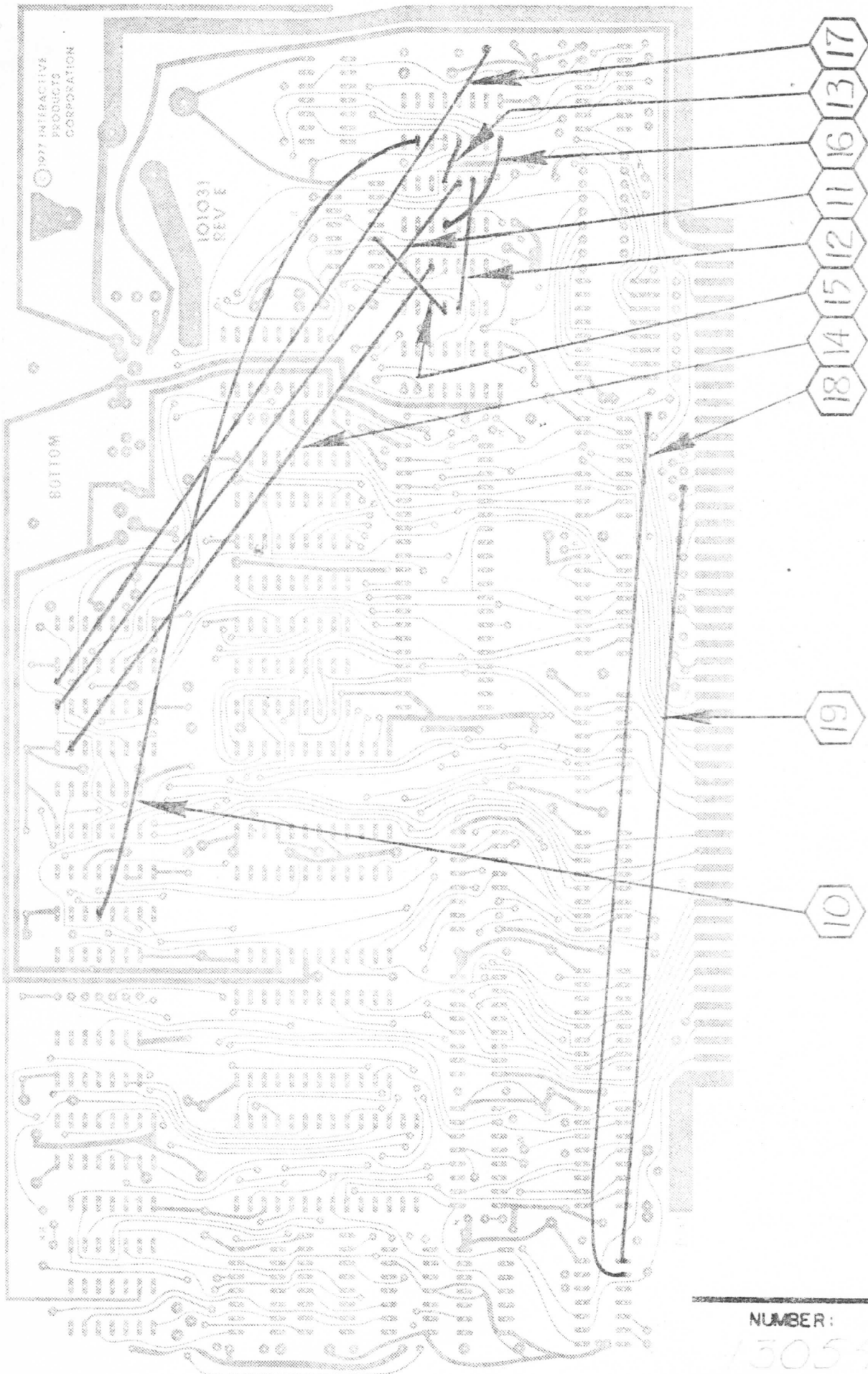
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