

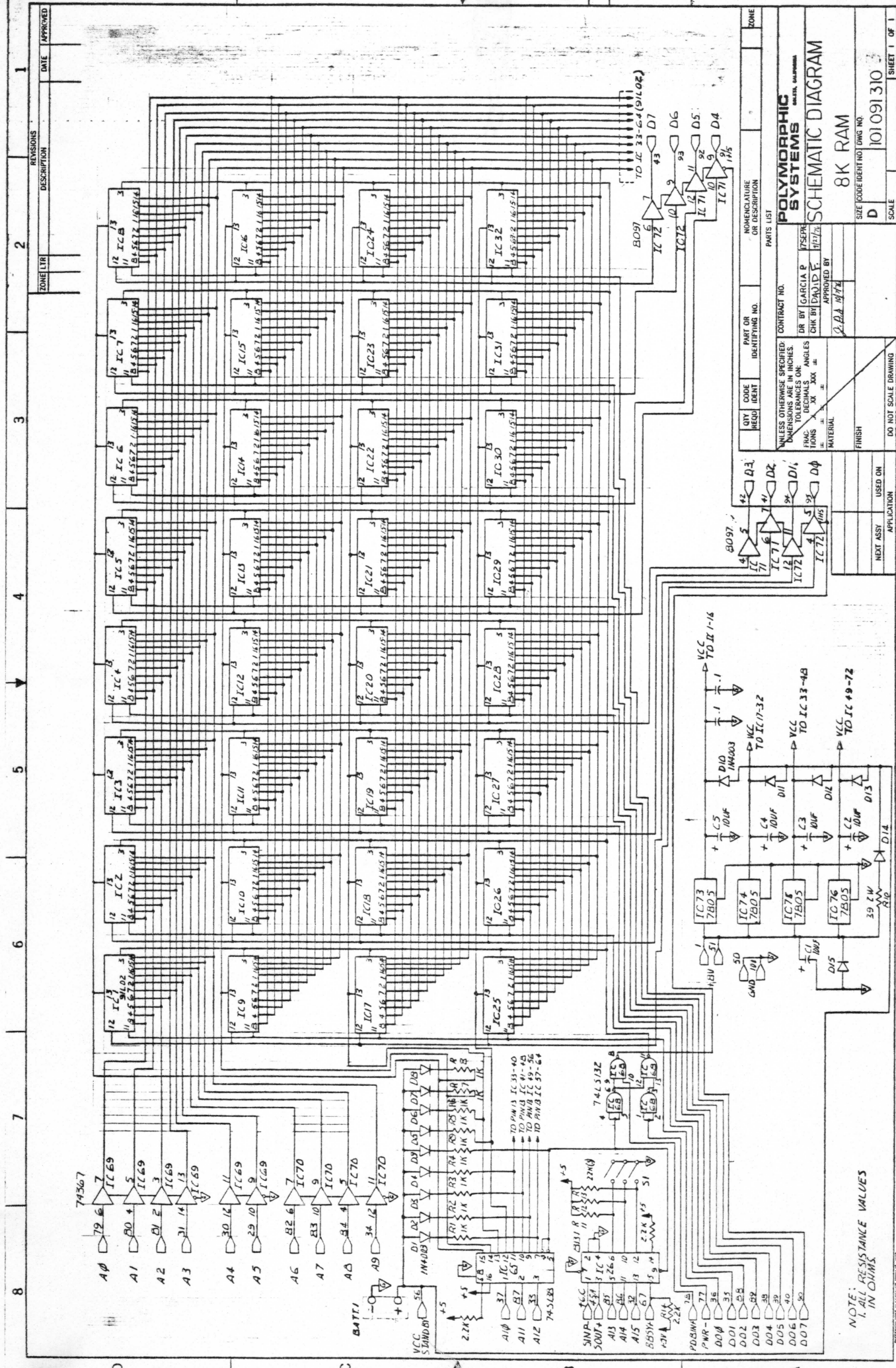
Polymorphic 8K Static RAM board

Three bits of the 4-position DIP switch determine the address of the board as shown below:

1	2	3	4
A13	A14	A15	Not Used

0 = Closed (typically up)

1= Open (typically down)



ZONE LTR	DESCRIPTION	DATE	APPROVED
1			
2			
3			
4			
5			
6			
7			
8			

REVISIONS

ZONE	
NOMENCLATURE OR DESCRIPTION	
PARTS LIST	
QTY	
REQD	
CODE	
IDENT	
IDENTIFYING NO.	
CONTRACT NO.	
OR BY	GARCIA P
CHK BY	DAVID E
APPROVED BY	
DATE	10/7/74
FINISH	
USED ON	
NDT ASSY	
APPLICATION	
DO NOT SCALE DRAWING	

POLYMORPHIC SYSTEMS
 7559 P
 1121/2
 APPROVED BY
 DATE 10/7/74

UNLESS OTHERWISE SPECIFIED:
 DIMENSIONS ARE IN INCHES
 DECIMALS .XX .XXX #
 FRACTIONS 1/32 1/16 3/32 #
 MATERIAL #

8K RAM
 SIZE (CODE IDENT NO) DWG NO.
 D 101091310

SCALE
 SHEET 1 OF 1

NOTE:
 1. ALL RESISTANCE VALUES
 IN OHMS

BK RAM POLYMORPHIC SYSTEMS

