

Z80 MACRO ASSEMBLER VERSION 2.1 MACRO-80

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MOSTEK MACRO-80 Z80 MACRO ASSEMBLER VERSION 2.1

MK78165

MOSTEK MACRO-80

Z80 MACRO ASSEMBLER

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MOSTEK MACRO-80

Z80 MACRO ASSEMBLER

VERSION 2.1

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MANUAL REVISION 1.5

SECTION 1

OVERVIEW AND OPERATION

1-1. INTRODUCTION.

1-2. The MOSTEK Z80 Macro Assembler (MACRO-80) is designed to run under FLP-80DOS Version 2.0 or above with 32K or more of RAM. MACRO-80 is the most powerful macro assembler in the microcomputer market. It features:

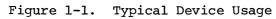
1.	optional arguments
2.	default arguments
3.	looping capability
4.	global/local macro labels
5.	nested/recursive expansions
6.	integer/boolean variables
7.	string manipulation
8.	conditional expansion based on symbol definition
9.	call by value facility
10	• expansion of code producing statements only

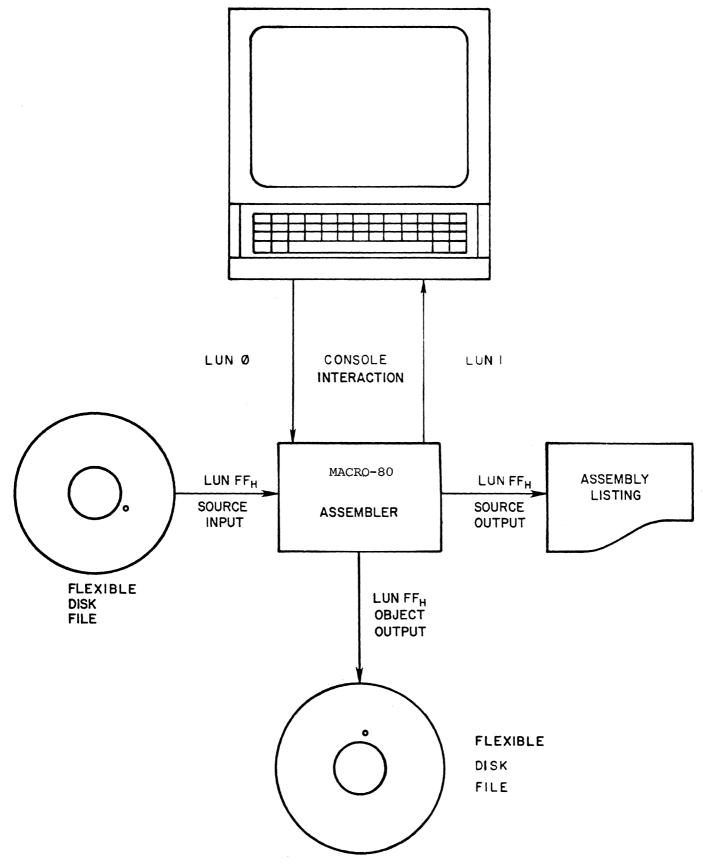
1-3. MACRO-80 is an advanced upgrade from the FLP-80DOS Assembler (ASM). In addition to its macro capabilities, it provides for nested conditional assembly, and it allows symbol lengths of any number of characters. It supports global symbols, relocatable programs, a symbol cross reference listing, and an unused symbol reference table.

1-4. Figure 1-1. shows the Assembler with typical device usage. The source module is read from a disk file; the object output is directed to a disk file; the assembly listing is directed to a line printer. User interaction is via the console device. Note that the Assembler can interact with any dataset.

02

e





1-5. REFERENCES.

AID-80F Operations Manual, MK78569 SYS-80F Operations Manual, MK78576 FLP-80DOS Operations Manual, MK78557

1-6. DEFINITIONS.

1-7. SOURCE MODULE - the user's source program. Each source module is assembled into one object module by the Assembler. The end of a source module is defined by an EOT character (ASCII 04) on input (standard end-of-file) or an END statement.

1-8. OBJECT MODULE - the object output of the Assembler for one source module. The object module contains linking information, address and relocating information, machine code, and checksum information for use by the FLP-80DOS Linker. The object module is in ASCII. A complete definition of the MOSTEK object format is given in Appendix B of the FLP-80DOS Operations Manual. The object module is typically output to a disk file with extension OBJ.

1-9. LOAD MODULE - the binary machine code of one complete program. The load module is defined in RAM as an executable program or on disk as a binary file (extension BIN). It is created by the Linker from one or more object modules.

1-10. LOCAL SYMBOL - a symbol in a source module which appears in the label field of a source statement.

1-11. INTERNAL SYMBOL - a symbol in a source (and object) module which is to be made known to all other modules which are linked with it by the Linker. An internal symbol is also called global, defined, public, or common. Internal symbols are defined by the GLOBAL pseudo-op. An internal symbol must appear in the label field of the same source module. Internal symbols are assumed to be addresses, not constants, and they will be relocated when linked by the Linker.

1-12. EXTERNAL SYMBOL - a symbol which is used in a source (and object) module but which is not a local symbol (does not appear in the label field of a statement). External symbols are defined by the GLOBAL pseudo-op. External symbols may not appear in an expression which uses operators. An external symbol is a reference to a symbol that exists and is defined as internal in another program module.

1-13. GLOBAL DEFINITION - both internal and external symbols are defined as GLOBAL in a source module. The Assembler determines which are internal and which are external.

1-14. POSITION INDEPENDENT - a program which can be placed anywhere in memory. It does not require relocating information in the object module.

1-15. ABSOLUTE - a program which has no relocating information in the object module. An absolute program which is not position independent can be loaded only in one place in memory in order to work properly.

1-16. RELOCATABLE - a program which has extra information in the object module which allows the Linker to place the program anywhere in memory.

1-17. LINKABLE - a program which has extra information in the object module which defines internal and external symbols. The Linker uses the information to connect, resolve, or link, external references to internal symbols.

1-18. CONVENTIONS USED IN THIS MANUAL.

1-19. All user input is underlined. Those items which must be entered exactly as shown are upper case. Those items which are variable are lower case. The symbol (CR) stands for carriage return.

1-20. USING THE ASSEMBLER.

1-21. The MACRO-80 Assembler is resident on a FLP-80DOS diskette. The user first prepares his source module using the FLP-80DOS Editor. Then the source file may be assembled via the following command:

SMACRO dataset S [TO dataset L [,dataset O]] (CR)

where dataset S = source input dataset dataset L = assembly listing output dataset (optiona dataset O = object output dataset (optional)

1-22. Dataset S is always a diskette file. Dataset L and dataset O are optional. If not given, dataset L defaults to the same disk unit and file name as dataset S, but the extension is LST. Dataset O, if not given, defaults to the same disk unit and file name as dataset L, but the extension is OBJ.

EXAMPLE

\$MACRO DK1:MYFILE TO CP:(CR)

- the user has selected to assemble file MYFILE on

disk unit 1. The listing is to be directed to the Centronics line printer device. The object will be directed to disk unit 1 on file MYFILE.OBJ.

1-23. ASSEMBLER OPTIONS

1-24. The Assembler allows the user to select the following options from the console when the Assembler outputs the message:

MOSTEK MACRO-80 ASSEMBLER V2.1. OPTIONS?

C - cross reference listing - prints a symbol cross reference table at the end of the assembly listing.

E - error exit - if any errors occur in pass 1 of the Assembler, they will be printed and pass 2 will not be done.

F - normal operation of pass 1 and pass 2 of the Assembler (default), switch off option E.

K - no listing - suppresses the assembly listing output. All errors will be output to the console device.

L - listing - the assembly listing will be output (default)

N - no object output - suppresses object output from the Assembler.

0 - object output - the object output will be produced (default).

Q - quit - return to Monitor.

R - redefine opcodes - allows normal $\ Z80$ opcodes to be redefined by macros (default off).

 ${\tt U}$ - unused symbols - a list of unused symbols will be printed at the start of the assembly listing.

V - switch off option U (default).

If no options are to be selected, the user enters a carriage return only.

EXAMPLE OPTIONS?NU(CR) ------ the user has selected no object output and an unused symbol listing.

1-25. ASSEMBLY LISTING OUTPUT

1-26. Figure 1-2. shows a sample Assembler listing output. The title (defined by the TITLE pseudo-op) is printed at the top of each page. The page number is in decimal notation. Three names appear in the second line at the top of each page. The first name is that of the source module; the second is the name of the object module; the third is that defined by the NAME pseudo-op. The key following the names is REL for a relocatable program and ABS for an absolute program.

1-27. Columns in the listing are automatically assigned by the Assembler. The LOC column defines the program address of the object code in hexadecimal. For relocatable programs, LOC is the relative offset from the start of the program. For absolute programs, LOC is the absolute address of the object code. The OBJ.CODE column defines the assembled Z80 opcode in hexadecimal. It is preceded by a guote (') if the statement contains a relocatable label. It is followed by a guote if the object code contains a relocatable address.

1-28. The STMT-NR heading defines two statement number columns. The column on the right defines a running statement number for all lines of the assembled program. The cross reference listing always refers to this number. The column on the left appears in programs with included files (INCLUDE pseudo-op) and/or macro expansions. Statement numbers are printed in decimal. The rest of each listing line is the source statement. If the line exceeds an 80 column width, then the source line is overflowed to the next line in the listing. The value of each equated symbol (EQU pseudo-op) is printed with an equal sign (=) next to it.

1-29. The number of lines printed per page of assembly listing is in address OBH of the Assembler. The number of characters per line of listing is in address OCH of the Assembler. Either of these values may be changed by the user. The default is 60 lines per page, 80 characters per line.

1-30. CROSS REFERENCE LISTING.

1-31. Figure 1-3. shows a cross reference listing, which is selected by option 'C'. The NAME column on the left hand side shows each symbol name used in the program in alphabetical order. The TYPE column indicates the type of the variable:

D	variable	defined by	DEFL	pseudo-op
E	external	variable		
I	internal	variable		

		MOSTEK MACRO-80 ASSEMBLER V2.0 PAGE 2 URCE-STMT PASS2 FIG1D2 FIG1D2 FIG1D2 REL
	1 2 SH 1 3 2 4 N 1 3 5 NL 4 6 5 7	MLOCAL L1,L2,L3,L4,L5,L6,L7 ;LOCAL MACRO LABEL DEFL #N-1 ;GET NUMBER OF BITS TO SHIFT
	6 8 7 9 8 10 L7 9 11 L6 10 12 L5 11 13 L4 12 14 L3 13 15 L2 14 16 L1 15 17 ;	MERROR ' N>7 OR N<1 ' MEXIT #KIND #REG ;SHIFT REGISTER NUMBER OF BITS #KIND #REG ;SPECIFIED BY #N PARAMETER #KIND #REG #KIND #REG ;THE TYPE OF SHIFT IS SHOWN #KIND #REG ;BY THE #KIND PARAMETER #KIND #REG
=0005 20 =0004 =0034	19 BB 20 1 21 2 22 N1 3 23 NL	SHIFT2 A BB SRL MLOCAL L1,L2,L3,L4,L5,L6,L7 ;LOCAL MACRO LABEL DEFL BB-1 ;GET NUMBER OF BITS TO SHIFT
DO A7 =FFFF	4 24 5 25	AND A ;RESET CARRY BIT FOR SHIFT MIF ('0004'<='0007').AND.('0004'>='0001') THE N L4
01 CB3F 03 CB3F 05 CB3F 07 CB3F	11 26 L4 12 27 L3 13 28 L2 14 29 L1 15 30	SRL A ;THE TYPE OF SHIFT IS SHOWN SRL A ;BY THE SPL PARAMETEP
09 =0002 =0032 09 A7 =FFFF	; 22 32 1 33 2 34 N1 3 35 NL 4 36 5 37	SHIFT2 A 'BB-2' RR MLOCAL L1,L2,L3,L4,L5,L6,L7 ;LOCAL MACRO LABEI DEFL BB-2-1 ;GET NUMBER OF BITS TO SHIFT DEFL '0002'[4,1] ;PREPARE FOR CONDITIONAL JUMH AND A ;RESET CARRY BIT FOR SHIFT MIF ('0002'<='0007').AND.('0002'>='0001') THH N L2
OA CB1F OC CB1F	13 38 L2 14 39 L1 15 40 ;	RR A RR A MEND
= 0 0 0 9 = 0 0 3 9	24 42 1 43 2 44 N1 3 45 NL	SHIFT2 L '2*BB' RL ;SHOULD GENERATE AN ERROR MLOCAL L1,L2,L3,L4,L5,L6,L7 ;LOCAL MACRO LABE DEFL 2*BB-1 ;GET NUMBER OF BITS TO SHIFT DEFL '0009'[4,1] ;PREPARE FOR CONDITIONAL JUM
) O E A 7 = 0 0 0 0	4 46 5 47 6 48	AND A ;RESET CARRY BIT FOR SHIFT MIF ('0009'<='0007').AND.('0009'>='0001') TH N L9 MERROR ' N>7 OR N<1 '
****ERR 5A ****	* * * * * * * * * *	*
	7 49	MEXIT
)OF	; 26 51	END

FIGURE 1			LE CROS				EK MA PASS2						-
	• • •						1 11 55 2	1101		0100	11010		
BB	C	005	19	22	34	44							
N1 I	DC	009	44	22* 47	23	25	25	34*	35	37	37	44*	45
NL I SHIFT2 M			45 2	23* 20	25 32	35* 42	37	45*	47				

М	macro name
U	undefined symbol
blank	absolute value, not global
۱.	relocatable value, not global
2	multiply defined variable

1-32. The VALUE column shows the 16-bit value of the symbol. The DEF column shows the statement number in which the symbol is defined. REFERENCES defines each statement number in which the symbol is used. A reference marked with an asterisk means the variable is used as a 'target operand' in the statement. For example:

LD	(NN),A		
SET	NBIT,B		
	- the references of	NN and NBIT are	marked by an
	asterisk (*) in the	cross reference	listing.

1-33. OBJECT OUTPUT.

1-34. The object output of the Assembler can be loaded by an Intel hexadecimal loader for non-linkable programs. Extra information is inserted into the object output for linkable and relocatable programs for using the MOSTEK Linker. For a complete discussion of the object format, see Appendix B in the FLP-80DOS Operations Manual.

1-35. ERROR MESSAGES.

1-36. Any error which is found is denoted in the assembly listing. A message is printed immediately after the statement which is in error. An asterisk is printed under the location in the statement where the error was detected. All the error codes for this Assembler are defined in Appendix A of this manual.

EXAMPLE

H2: LC A,B

1-37. Several errors abort the Assembler when they are encountered. Abort errors are output only to the console device and control is immediately returned to the Monitor. Abort errors may occur during pass 1 or pass 2.

1-38. ADVANCED OPERATIONS.

1-39. Several source modules may be assembled together to form one object module. The INCLUDE pseudo-op may be used several times in one module to properly sequence a set of source modules.

EXAMPLE

NAME MYFILE ;name of final object module INCLUDE FILE1 INCLUDE FILE2 INCLUDE FILE3 END - the object module named MYFILE will be built by the

assembly from FILE1 + FILE2 + FILE3.

1-40. SAMPLE ASSEMBLY SESSION

1-41. Assume that the file to be assembled is named PROG1. The diskette on which PROG1 exists is in disk unit 1 (DK1). The object output of the Assembler is to be directed to file PROG1.OBJ on disk unit 1. The assembly listing is to be directed to a line printer (LP:). A cross reference table is to be printed.

EXAMPLE

- user selects a printed cross reference table

_ _ _ _ _

• • \$ - indication that assembly is done and control is returned to the Monitor.

SECTION 2

ASSEMBLY LANGUAGE SYNTAX

2-1. INTRODUCTION.

2-2. An assembly language program (source module) consists of labels, opcodes, pseudo-ops, operands, and comments in a sequence which defines the user's program. The assembly language conventions for MACRO-80 are described below.

2-3. DELIMITERS.

2-4. Labels, opcodes, operands, and pseudo-ops must be separated from each other by one or more spaces or tab characters (ASCII 09). The operands must be separated from each other by commas. Operands in a macro call or macro definition statement may be separated from each other by one or more spaces or tab characters. The label may be separated from the opcode by a colon, only, if desired.

EXAMPLE

label	opcode	operands	comment	
LAB1	LD	А,В	;LOAD REGISTER A WI	TH B

2-5. LABELS.

2-6. A label may have any number of characters in it. The first six characters are decoded uniquely; any remaining characters are identified by a 'hash code'. This means that it is possible to use labels longer than 6 characters which appear different but are multiply defined by the Assembler. For example, 'ALABEL65' and 'ALABEL56' would be identified as the same label.

2-6A. The first character of a label must be alphabetic (A-Z). The remaining characters may be alphanumeric (A-Z, 0-9), question mark (?), or underline (_). Note that this is more restrictive than the FLP-80DOS ASM Assembler. A label may start in any column if immediately followed by a colon (:). It does not require a colon if started in column one.

EXAMPLE

```
allowed not allowed
```

LABEL1	1LAB4	(starts with a number)
HERE?	AD%DC	(contains illegal character)

2-7. OPCODES.

2-8. There are 74 generic opcodes (such as LD), 25 operand key words (such as A), and 693 legitimate combinations of opcodes and operands in the Z80 instruction set. The full set of these opcodes is documented in the 'Z80 CPU Technical Manual'. The MACRO-80 Assembler allows one other opcode which is not explicitly shown in the Technical Manual:

IN F,(C) ;SET CONDITION BITS ACCORDING TO THE CONTENTS ;OF THE PORT DEFINED BY THE C-REGISTER

2-9. PSEUDO-OPS.

2-10. Pseudo-ops are used to define assembly time parameters. Pseudo-ops appear like Z80 opcodes in the source module. Several pseudo-ops require a label. The following pseudo-ops are recognized by the Assembler:

- ORG nn origin sets the program counter to the value of the expression nn. Each origin statement in a program must be greater than the first origin of the program to assure proper linking.
- label EQU nn equate sets the value of the label to nn in the progr where nn is an expression; it can occur only once for any label.
- label DEFL nn define label sets the value of a label to nn in the program, where nn is an expression; it may be repeated in the program with different values for the same label. At any point in the program, the label assumes the last previously defined value. DEFL has certain other very useful properties associated with its use in macros. (See Section 3 of this manual).
- DEFM m,m,m... define message defines the contents of successive bytes of memory according to m. m is composed of a sequence of either strings of characters surrounded by quotes or constants, each separated by one comma. Strings and constants may be mixed. The maximum length of the message is 63 bytes. The number of bytes allocated to a constant depends on its value. For example, the constant OAF3H will

have 2 bytes allocated to it, and OEFH will have one byte allocated. Symbols and expressions are not allowed in operands in the DEFM statement. The delimiting quote characters are required on a character string. A quote may be placed in a message by a sequence of 2 quotes (''). Example: DEFM 5H,'TEXT1',20414E4420H,'TEXT2'

- DEFB n,n,n... define byte defines the contents of successive bytes starting at the current program counter address to be n, where n is any expression.
- DEFW nn,nn,nn... define word defines the contents of successive two-byte words to be the value of expressions nn. The least significant byte of each expression is located at the current program counter address. The most significant byte is located at the program counter address plus one.
- DEFS nn define storage reserves nn bytes of memory starting at the current program counter, where nn is an expression. When loaded, these bytes are not overwritten, i.e., they will contain what was previously in memory. This pseudo-op cannot be used at the start or end of a program to reserve storage.
- END nn end statement defines the last statement of a program. The END statement is not required. The expression nn is optional and represents the transfer address (starting execution address) of the program. Note that for binary files the transfer address must be the same as the starting address.
- GLOBAL symbol, symbol, ... define global symbol any symbol which is to be made known among several separately assembled modules must appear in this type of statement. The Assembler determines if the symbol is internal (defined as a label in the program), or external (used in the program but not defined as a label).
- NAME symbol module name This pseudo-op defines the name of the program (source and object). The name is placed in the heading of the assembly listing and is placed in the first record of the object module to identify it. This pseudo-op is designed primarily to facilitate future compiler design. The name of a module defaults to 6 blanks.
- PSECT op program section may appear only once at the start of a source module. This pseudo-op defines the program module attributes for the following operands:

REL - relocatable program (default) ABS - absolute program. No relocating information is generated in the object module. The module will be linked where it is origined.

- IF nn conditional assembly if the expression nn is or COND nn true (non-zero), the pseudo-op is ignored. If the expression is false (zero), the assembly of subsequent statements is disabled until an ENDIF statement is encountered. IF pseudo-ops can be nested to a level of 11.
- ENDIF end of conditional assembly re-enables or ENDC assembly of subsequent statements.
- INCLUDE dataset include source from another dataset allows source statements from another dataset to be
 included within the body of the given program.
 If a file name only is specified, then the file
 is searched for first on DKO:, then on DK1:.
 If the dataset cannot be opened properly, then
 assembly is aborted. The source module to be
 included must not end with an END pseudo-op
 (otherwise, assembly would be terminated). The
 source module must end with an EOT character
 (04H), which is true for all FLP-80DOS ASCII datasets.
 The INCLUDE pseudo-op cannot be nested, it
 cannot be followed by a comment on the same line,
 and it cannot appear in a macro definition.
- LIST nn list all assembled statements (default on), where nn is an expression. If nn = 0 then the listing is turned off. Otherwise it is turned on.
- ELIST nn list expanded statements from macro expansions if the expression nn = 0, then only the macro call statements will appear in the assembly listing. Otherwise, all expanded statements from macro calls will appear in the assembly listing (default on).
- CLIST nn list only code-producing statements from macro expansions - if the expression nn = 0, then only code-producing statements in the macro expansions will be listed. Otherwise all statements in each macro expansion will be listed in the assembly listing (default on).
- NLIST turn off assembly listing. This is provided for compatibility with the FLP-80DOS ASM.

EJECT - eject a page of the assembly listing.

TITLE s - print a title 's' at the top of each page of the listing. The title may be up to 32 characters in length.

2-11. OPERANDS.

2-12. There may be zero, one, or more operands in a statement depending upon the opcode or pseudo-op used. Operands in the Assembler may take the following forms:

2-13. GENERIC OPERAND. Table 2-1 summarizes the generic operands in the MACRO-80 Assembler.

2-14. CONSTANT. The constant must be in the range 0 thru OFFFFH. It may be in any of the following forms:

Decimal - this is the default mode of the Assembler. Any number may be denoted as decimal by following it with the letter 'D'. E.g., 35, 249D

Hexadecimal - must begin with a number (0-9) and end with the letter 'H'. E.g., OAF1H

- Octal must end with the letter 'Q' or 'O'. E.g. 3770, 2770
- Binary must end with the letter 'B'. E.g., 011011B

ASCII - letters enclosed in guote marks will be converted to their ASCII equivalent value. E.g., 'A' = 41H

2-16. LABEL. Labels cannot be defined by labels which have not yet appeared in the user program. This is an inherent limitation of a two pass assembler.

EXAMPLE	not allowed	allowed
	L EQU H	I EQU 7
	H EQU I	H EQU I
	I EQU 7	L EQU H

TABLE 2-1.

	MACRO-80 GENERIC OPERANDS
	A register (Accumulator) B register C register D register E register F register (flags) H register L register
AF' BC DE	AF register pair AF' register pair BC register pair DE register pair HL register pair
SP \$	Stack Pointer register Program Counter
I R	I register (interrupt vector MS byte) Refresh register
IX IY	IX index register IY index register
NC C PO	not zero zero not carry carry parity odd/not overflow parity even/overflow sign positive sign negative

2-17. EXPRESSION. MACRO-80 recognizes a wide range of expressions in the operand field of a statement. All expressions are evaulated left to right constrained by the hierarchies shown in Table 2-2. Parentheses may be used to ensure correct expression evaluation. The symbol '\$' is used to represent the value of the program counter of the current instruction. Note that enclosing an expression wholly in parentheses indicates a memory address. Integer two's complement arithmetic is used throughout. The negative (2's complement) of an expression or quantity may be formed by preceding it with a minus sign. The one's complement of an expression may be formed by preceding it with the '.NOT.' operator.

2-18. In doing relative addressing, the current value of the program counter may or may not be subtracted from the label, at the programmer's discretion:

JR	LOOP
JR	LOOP-\$
	-will both jump relative to the label 'LOOP'.

2-19. The allowed range of an expression depends on the context of its use. An error message will be generated if this range is exceeded during its evaluation. In general, the limits on the range of an expression are 0 thru OFFFFH. The range of a jump relative instruction (JR or DJNZ) is -126 bytes and +129 bytes. The Assembler monitors the number of items in an expression. If an expression is too long, an error message will be output. For relocatable programs the Assembler outputs relocation information in the object module for those addresses which are to be relocated by the Linker. Expressions are determined to be relocatable addresses or non-relocatable constants according to the rules shown in Table 2-3.

TABLE 2-2.

ALLOWED OPERATORS IN MACRO-80

OPERATOR	HIERARCHY	RELOCATE RULE	RANGE		
•RES.			\\ 		
•DEF•		1	operand must be a symbol		
unary + unary -	1	1			
**	1	2			
* /	2 2	2 2	operand 2 not = 0		
+ -	3 3	3 4			
.EQ. or = .LT. or < .GT. or > .LE. or <= or = .GE. or >= or = .NE. or <> or > .ULT. .UGT.	=> 4	5 5 5 5 5 5 5 5 5	string handling allowed		
• AND •	5	2			
•OR• •XOR• •MOD• •NOT•	6 6 6	2 2 2 1			
•SHR• •SHL•	6 6	2 2	operand 2 < 16 operand 2 < 16		
[m,n]			operand must be a string		

For relocate rules see Table 2-3.

TABLE 2-3.

RELOCATE RULES FOR OPERATORS

<pre><operand 1=""> op</operand></pre>	<operand 2=""></operand>	rand 2> Relocate rule 1 2 3 4 NOT * / + -		4		(rule number) (mnemonic)	
relocatable	relocatable	ERR	ERR	ERR	ABS	ABS	
relocatable	absolute	ABS	ERR	REL	REL	ABS	
absolute	relocatable	ERR	ERR	REL	ERR	ABS	
absolute	absolute	ABS	ABS	ABS	ABS	ABS	

where ABS denotes absolute result REL denotes relocatable result ERR denotes error condition.

The following table shows the rules for global symbols used in relocatable and absolute programs.

	relocatable nn = rel		absolute p nn = rel	-
GS EQU nn	REL	ERR	REL	REL
LS EQU nn	REL	ABS	REL	ABS

where

GS denotes a global symbol LS denotes a non-global symbol nn is an expression REL means relocatable result ABS means absolute result ERR denotes error condition

.RES. - reset overflow - appearance of this operator anywhere in an expression forces any overflow indication to be unconditionally reset.

.NOT. - one's complement.

** - exponentiation operator.

Relational operators (= > < etc.) can be used with character strings. This facility is useful when using macros to define a higher level language.

.ULT. - unsigned less than.

.UGT. - unsigned greater than.

•SHR• - shift first operand right by number of bits designated in second operand.

.SHL. - shift first operand left by number of bits designated by the second operand.

.DEF. - defined symbol operator - returns the value zero (false) if the symbol following the operator is not defined. Returns true (not zero) if the symbol is defined.

2-20. STRING EXPRESSIONS. The operator [,] extracts a substring from a given string. This is most useful in macros in which strings can be passed as arguments. Note that the Assembler does not support string variables. The general form of a string expression is:

string[m,n] or string[m]

where string is any character string enclosed by quotes,
 [and] are delimiters,
 m is an integer which represents the starting

column number, and n is an integer which represents the number of

columns to be accessed.

2-21. If the integer n is not present, then n is assumed to be equal to the remaining number of columns in the given string.

EXAMPLE

'ABCDEF'[3,2] is equivalent to 'CD' 'ABCDEF'[3] is equivalent to 'CDEF'

2-22. COMMENTS.

2-23. A comment is defined as any set of characters following a semicolon in a statement. A semicolon which appears in quotes in an operand is treated as an expression rather than a comment starter. Comments are ignored by the Assembler, but they are printed in the assembly listing. Comments can begin in any column. Note that the Assembler also treats as comments any statements with an asterisk (*) in column one.

2-24. ABSOLUTE MODULE RULES.

2-25. The pseudo-op 'PSECT ABS' defines a module to be absolute. The program will be loaded in the exact addresses at which it is assembled. This is useful for defining constants, a common block of global symbols, or a software driver whose position must be known. This method can be used to define a list of global constants as follows:

EXAMPLE

	PSECT	ABS	;ABSOLUTE	ASSEMBLY
	GLOBAL	A A		
AA	EQU	ОЕЗН		
	GLOBAL	ΑX		
AX	EQU	ОАГЗН		
	END			

2-26. RELOCATABLE MODULE RULES.

2-27. Programs default to relocatable if the 'PSECT ABS' statement is not used or if 'PSECT REL' is used.

2-28. Only those values which are 16-bit address values will be relocated. 16-bit constants will not be relocated.

EXAMPLE

AA	EQU	0A13H	;ABSOLUTE VALUE
	LD	A,(AA)	;AA NOT RELOCATED
AR	EQU	\$;RELOCATABLE VALUE
	LD	HL,(AR)	;AR WILL BE RELOCATED UPON LINKING

2-29. Relocatable quantities may not be used as 8-bit operands. This restriction exists because only 16-bit operands are relocated by the Linker.

EXAMPLE

LAB EQU \$;RELOCATABLE VALUE

19

DEFB	LAB	;NOT AL	LOWED	
LD	A,(IX+LA	B)	;NOT	ALLOWED
LD	A,(LAB)	;ALLOWE	D	
LD	HL,LAB	;ALLOWE	D	

2-30. Labels equated to labels which are constants will be treated as constants. Labels equated to labels which are relocatable addresses will be relocated.

EXAMPLE

B8	EQU	20H	;CONSTANT
C 8	EQU	B8	;CONSTANT
	LD	A,(C8)	;C8 WILL NOT BE RELOCATED
AR	EQU	\$;RELOCATABLE ADDRESS
BR	EQU	AR	;RELOCATABLE
	LD	A,(BR)	;BR WILL BE RELOCATED

2-31. External symbols in a relocatable program are marked relocatable, except for the first usage. The code for external symbols is actually a backward link list through the object code.

2-32. GLOBAL SYMBOL HANDLING.

2-33. A global symbol is a symbol which is known by more than one module. A global symbol has its value defined in one module. It can be used by that module and by any other module which is linked with it by the Linker. A global symbol is defined as such by the GLOBAL pseudo-op.

2-34. An internal symbol is one which is defined as global and also appears as a label in the same program. The symbol value is thus defined for all programs which use that symbol. An external symbol is one which is defined as global but does NOT appear as a label in the same program.

EXAMPLE

GLOBAL	SYM1	;DEF]	INE GLOBAD	L SYMBOL
CALL	SYM1			
•				
•				
• END				
END				
	- SIMI	is an	external	symbol

EXAMPLE

	GLOBAL	SYM1	;DEFINE	GLOBAL	SYMBOL
SYM1	EQU	\$			
	LD	A,(SYM1)			
	•				

٠

• END - SYM1 is an internal symbol. Its value is the address of the LD instruction.

2-35. If these two programs were assembled and then linked by the Linker, then all global symbol references from the first program would be 'resolved'. This means that each address in which an external symbol was used would be modified to the value of the corresponding internal symbol. The linked programs would be equivalent (using our example) to one program written as follows:

EXAMPLE

	CALL	SYM 1
	•	
	•	
	•	
SYM1	EQU	\$
	LD	A,(SYM1)
	•	
	•	
	•	
	END	

2-36. Global symbols are used to allow large programs to be broken up into smaller modules. The smaller modules are used to ease programming, facilitate changes, or allow programming by different members of the same team.

2-37. GLOBAL SYMBOL RULES.

2-38. An external symbol cannot appear in an expression which uses operators.

EXAMPLE

GLOBAL	SYM1	;EXTERNA	L SYN	1BOL
CALL	SYM 1	;OK		
LD	HL, (SYM	1+2)	;NOT	ALLOWED

2-39. An external symbol is always considered to be a 16-bit address. Therefore, an external symbol cannot appear in an instruction requiring an 8-bit operand.

EXAMPLE

GLOBAL	SYM1	;EXTERNAL SYMBOL
CALL	SYM 1	;OK
LD	A,SYM1	;NOT ALLOWED

2-40. An external symbol cannot appear in the operand field of an EQU or DEFL statement.

2-41. For a set of modules to be linked together, no duplication of internal symbol names is allowed. That is, an internal symbol can be defined only once in a set of modules to be linked together.

SECTION 3

MACRO CAPABILITY

3-1. INTRODUCTION.

3-2. MACRO-80 offers the most advanced macro handling capability in the microcomputer industry. Macros provide a means for the user to define his own opcodes or to redefine existing opcodes. A macro defines a body of text which will be inserted automatically into the source program at each occurrence of a macro call. Parameters associated with a macro provide a capability for making changes in the macro at each call. The following paragraphs describe how to use the macro facility.

3-3. MACRO DEFINITION.

3-4. The body of text to be used as a macro is given in the macro definition. Each definition begins with a MACRO pseudo-op and ends with an MEND pseudo-op. The general form is:

label	opcode	operands	comment
name:	MACRO	#p1,#p2,,#pn	;comments (optional)
	•	body of macro goes here	
label:	MEND		

3-5. The name is required, and it must obey all the usual rules for forming labels (recall that the colon is optional if the name starts in column one). If the name is a Z80 opcode (e.g., LD, EXX), then the 'R' option must be selected at the start of the Assembler to permit redefinition of opcodes by macros.

3-6. There can be any number of parameters from 0 to 99, each starting with the symbol '#'. The rest of the parameter name follows normal symbol rules. Parameter names are not entered into the symbol table. Parameters are separated from each other by single commas, or one or more blanks, or one or more tab characters.

3-7. The label on the MEND statement is optional, but if one is given it refers to the next program address upon expansion of the macro.

3-8. Each statement between the MACRO and MEND statements is entered into a temporary macro file. The only restriction on these statements is that they do not include another macro definition (nested definitions are not allowed) or an INCLUDE statement. They may include macro calls. The depth of nested calls is limited only by available memory space for buffering.

3-9. The statements of the macro body are not assembled at definition time, so they will not define labels, generate code, or cause errors. Exceptions are the Assembler commands such as LIST which are processed whenever they are encountered. Within the macro body text, the formal parameter names may occur anywhere that an expansion-time substitution is desired. This also applies to comments and quoted strings. However, no substitution of parameters is performed for comments defined by an asterisk in column one.

3-10. Macros must be defined before they are called. Once defined, a macro cannot be redefined within the same program. If a macro is called by another macro, then its definition must precede the calling macro's definition.

3-11. MACRO CALLS AND MACRO EXPANSION.

3-12. A macro is called by using its name as an opcode at any point after the definition. The general form is:

label	opcode	operands	comment
label	name	s1,s2,,sn	;comment (optional)

3-13. The label is optional and will be assigned to the current value of the program counter. The name must be a previously defined macro. There may be any number of argument strings s1 thru sn, separated by any number of blanks or tabs or single commas. The comma can be used as a place holder to pass null arguments to the macro expansion. All arguments are passed. If too few are passed, the remaining arguments assume the value of null (no characters in the argument string). If there are too many arguments, the extras may be accessed by the MNEXT pseudo-op (described below).

3-14. The position of each string in the list corresponds to the position of the macro parameter name it is to replace. Thus, the third string in a macro call statement will be substituted for each occurrence of the third parameter name.

3-15. Each string may be of any length and may contain any characters. Quotes around the string are optional; they are required if the string contains delimiters or the quote character itself. The quote character is represented by a sequence of two successive quote characters at the

inner level. The outer level of quotes, if present, will not occur in the substitution, i.e., they are stripped from the argument. The null string, represented by two successive quote characters, may be used in any parameter position.

3-16. After processing the macro call statement, the Assembler switches its input from the source file to the macro file. Each statement of the macro body is scanned for occurrences of parameter names. For each occurrence found, the corresponding argument string from the macro call statement is substituted. After substitution, the statement is assembled normally.

3-17. Default arguments may be specified in the parameter list by use of an equal sign (=). The call to the macro must specify comma place holders for each default argument to be substituted (otherwise the null argument will be substituted).

EXAMPLE

MAC1	MACRO	#A=DE, #B=HL, #C=BC				
	•					
	•					
	•					
	MEND					
	X 1 C 4	TURNATON UTAU NO IRGUNRUMO				
	MAC1	; EXPANSION WITH NO ARGUMENTS				
	•	;ALL ARGUMENTS WILL DEFAULT TO NULL				
	•					
	•					
	MEND					
	MAC1	,,, ;EXPANSION TO USE DEFAULT ARGUMENTS				
		;); ; ; EXPANSION TO USE DEFAULT ARGUMENTS ; DEFAULT ARGUMENTS WILL BE				
	•					
	•	; USED FOR PARAMETERS #A, #B, AND #C				
	MEND					

3-18. RECURSION.

3-19. Macros may include calls to other macros, including themselves. The definition statements of a macro which calls other macros must follow the definition statements of those macros. A macro which directly calls itself (or indirectly by calling a second macro which calls the first macro) is said to be recursive. Each recursive call causes a new expansion of the macro, possibly with different parameters. In order to prevent the macro from being called endlessly, conditional assembly can be used to inhibit a recursive call when certain conditions are met. A recursion of greater than 255 calls will generate an error.

3-20. SUBSTITUTION BY VALUE (% OPERATOR).

3-21. Symbol values can be expanded within a macro by preceding the symbol name with a percent sign (%). The symbol must appear as the label of a DEFL statement. The value of the symbol is expanded to 4 decimal digits when the macro is called.

3-22. The value of an argument may be substituted by value by using the DEFL statement and the % operator. In this case, some symbol is equated to the parameter via the DEFL pseudo-op. The value of the symbol is then expanded to four decimal digits by using the % operator. This facility can be used only within a macro.

The DEFL statement within a macro also has the characteristic that it can be expanded just like a macro parameter. The symbol defined by the DEFL pseudo-op can be preceded by a # sign elsewhere in the macro definition to expand its value as ASCII characters. See the example below.

EXAMPLE

MAC1 N1	MACRO DEFL	# N # N - 1				
NL	DEFL JP	"%N1"[4,1] L#NL	;GET ON	NE-DIGIT	ASCII	NUMBER
L1	• • •					
L2	• • •					
L3	• • •					
L4	MEND					
BB	EQU MAC1	4 BB	;EXPANS	SION		
N 1	DEFL	3				
NL	DEFL	'0003'[4,1]				
	JP	L3				
L1	• • •					
L2	• • •					
L3	• • •					
L4	MEND					

3-23. PREDEFINED ARGUMENTS.

3-14. The following predefined arguments are unique symbols and may be used anywhere in the macro definition.

%NEXP - expands to a four decimal digit representation of the number of the expansion of any macro. Thus, the first expansion of any macro

yields %NEXP = 0001, the second yields %NEXP = 0002, etc.

EXAMPLE

MAC1

MACRO DEFW MEND	%NEXP		
MAC1 DEFW MEND	0001	;1ST	EXPANSION
MAC1 DEFW MEND	0002	;2ND	EXPANSION

%NARG - expands to a four decimal digit representation of the number of aguments passed to the macro expansion.

EXAMPLE

MAC1	MACRO LD MEND	#A,#B,# A,%NARG	
	MAC1 LD MEND	1,2 A,0002	;EXPANSION

#PRM - expands to the last used argument. Note that the first parameter of the macro must be expanded explicitly before #PRM is used. Alternatively, the MNEXT pseudo-op can be used to access the first parameter. See the discussion of MNEXT, below.

EXAMPLE

MAC1	MACRO LD LD LD LD MEND	#A,#B HL,#A DE,#PRM BC,#B IY,#PRM	
	MAC1 LD LD LD LD MEND	SYM1,SYM2 HL,SYM1 DE,SYM1 BC,SYM2 IY,SYM2	;EXPANSION

%NPRM - expands to a two decimal digit representation of the position number of the last used argument. This shows the position of an argument in the argument list.

EXAMPLE

MAC1	MACRO	#A,#B
	LD	HL,#B

LD A,%NPRM MEND MAC1 SYM1,SYM2 ;EXPANSION LD HL,SYM2 LD A,02 MEND

%NCHAR - expands to a two decimal digit representation of the number of characters in the last used argument.

EXAMPLE

A
В
VPANSION
CDE

3-25. FORMATION OF LABELS WITHIN A MACRO EXPANSION.

3-26. There are three ways of forming unique labels within a macro expansion.

3-27. PREDEFINED ARGUMENT %NEXP. The current expansion number will be expanded as four decimal digits, which may be appended to a character or set of characters to form a unique label.

EX	A	М	Ρ	L	E
----	---	---	---	---	---

MAC1 L%NEXP	MACRO LD MEND	#A Hl,#A	
L0001	MAC1 LD MEND	SYM HL,SYM	;EXPANSION 1
L0002	MAC1 LD MEND	SYM2 Hl,SYM2	;EXPANSION 2

3-28. SUBSTITUTION OF PARAMETER. Unique labels may be formed by using a parameter as part of the label. A passed argument then defines a label or set of unique labels for the given expansion.

EXAMPLE

MAC1 L#A M#A	MACRO DEFM DEFB MEND	#A 'A MESSAGE' 9
LFST MFST	MAC1 DEFM DEFB MEND	FST ;EXPANSION 'A MESSAGE' 9
LSND MSND	MAC1 DEFM DEFB MEND	SND ;EXPANSION 2 'A MESSAGE' 9

3-29. DOT OPERATOR (.). Symbols in a macro definition may have a dot as the first character. The dot in every symbol will be replaced by the label specified in the macro call statement during macro expansion. Labels formed by the dot operator may also be used in MGOTO, MIF, and MNEXT statements.

EXAMPLE

MAC1 •L1	MACRO LD	HL,.L2	;MACRO DEFINITION
	•		
	•		
•L2 •LAB	•		
• 1110	MEND		
M 1	MAC1		;THE MACRO CALL
M1L1	LD	HL,M1L2	
	•		
	•		
M1L2			
M1LAB	MEND		

Note that the dot operator can be used with a parameter if the two items are separated by another character.

EXAMPLE

MAC1	MACRO LD	#A HL,.L#A	;MACRO	DEFINITION	
• L # A	• • •				

......

	MEND		
M4	MAC1 LD	25 ;MACRO CAL HL,M4L25	.L
M4L25	• • •		
	MEND		

3-30. LOCAL MACRO LABELS.

3-31. Local macro labels are allowed only in the MGOTO, MIF, and MNEXT statements. Local macro labels must follow normal symbol rules. They may not be formed by use of predefined arguments, substitution of parameters, or by use of the dot operator. Each local macro label will be in effect only during the current expansion of the current macro. They are in effect from the time of declaration via the MLOCAL pseudo-op through the MEND pseudo-op. They may not be redefined or respecified within one macro. Local declarations of the same symbol in nested or recursive macro calls are allowed. Local macro labels are not placed in the symbol table; they are used merely as pointers for the MGOTO, MIF, and MNEXT statements. A local macro label must be declared before it is used. The format for declaring local macro labels is:

MLOCAL mlabel1,mlabel2,...
- where mlabel1, mlabel2, etc., are labels which only
appear in the macro body. The MLOCAL statement may not
have a label on it.

EXAMPLE

MAC1	MACRO	#A,#B
	MLOCAL	L1,L2,L3
	MIF	'#A'='IF' THEN L1 ELSE L3
L1	MIF	'#B'='' THEN L2 ELSE L3
L2	MERROR	BAD IF STATEMENT
L3	MNOP	
	MEND	

3-32. MACRO RELATED PSEUDO-OPS.

3-33. In the following discussion, mlabel, mlabel1, and mlabel2 refer to local macro labels or labels formed by using the dot operator (.). The symbol nn refers to any valid expression. Brackets [] refer to optional parameters.

3-34. MNEXT nn [THEN mlabel1] [ELSE mlabel2]

- moves the argument pointer according to the expression nn in the argument list. A move to the left can be achieved by a negative value, to the right by a positive value. The argument may then be accessed by the #PRM predefined argument. If the argument pointer leaves the argument list and if the ELSE clause is present, then a jump to mlabel2 is performed. Otherwise the next statement in sequence is processed.

EXAMPLE

MAC1	MACRO	#A,#B	
	MLOCAL	L1,L2	
L1	MNEXT	1 ELSE	L2
	DEFB	#PRM	
	MGOTO	L 1	
L2	MEND		
	MAC1	1,2,3	;EXPANSION
	MLOCAL	L1,L2	
L1	MNEXT	1 ELSE	L2
	DEFB	1	
	MGOTO	L1	
L1	MNEXT	1 ELSE	L2
	DEFB	2	
	MGOTO	L1	
L1	MNEXT	1 ELSE	L2
	DEFB	3	
	MGOTO	L1	
L1	MNEXT	1 ELSE	L2
L2	MEND		

3-35. MGOTO mlabel

- continues the expansion at the specified macro label.

EXAMPLE

See the EXAMPLE for the MNEXT pseudo-op.

3-36. MIF nn THEN mlabel1 [ELSE mlabel2)

- if the expression nn evaluates to true (non-zero), then expansion is continued at the mlabel1 macro label. If the expression is false (equals zero) and the ELSE clause is present, expansion continues at the mlabel2 macro label. Otherwise expansion continues at the next statement in the macro.

EXAMPLE

MAC 1	MACRO	# A				
	MLOCAL	L1,L2				
	MIF	'#A'='THEN'	THEN	L1	ELSE	L2
L1	DEFM	"#A "				
L2	MEND					

L 1 L 2	MAC1 MLOCAL MIF DEFM MEND	THEN ;FIRST EXPANSION L1,L2 'THEN'='THEN' THEN L1 ELSE L2 'THEN'
L2	MAC1 MLOCAL MIF MEND	ELSE L1,L2 'ELSE'='THEN' THEN L1 ELSE L2

3-37. MNOP

- no operation is performed. This pseudo-op can be used to define a local macro label at this point in the macro body. This is useful because the local macro labels will not appear in the assembly listing if the CLIST 0 pseudo-op is used.

3-38. MEXIT

- terminates the current macro expansion.

EXAMPLE

MAC1	MACRO	# A
	MLOCAL	L1
	MIF	'#A'='THEN' THEN L1
	MEXIT	
L1	MNOP	
	LD	A,1
	MEND	
	MAC1	ELSE
	MLOCAL	L1
	MIF	'ELSE'='THEN' THEN L1
	MEXIT	

3-39. MERROR text

- prints the line of text like an error message with error number 5A called out.

EXAMPLE

MAC1	MACRO		
	MLOCAL	L1,L2,L3	
	MNEXT	1 ELSE L2	
L1	• • •		
	MGOTO	L3	
L2	MERROR	ARGUMENTS	REQUIRED
L3	MEND		
	X 1 0 4		
	MAC1		
	MLOCAL	L1,L2,L3	
	MNEXT	1 ELSE L2	

- 3-40. MEND
- marks the end of a macro.
- 3-41. MLOCAL label1, label2,...
- defines local macro labels.

SECTION 4

APPLICATIONS OF MACROS

4-1. INTRODUCTION.

4-2.

The MACRO-80 Assembler provides a powerful tool for microcomputer systems development. Five areas of applications are discussed below to show how the macro facility can be used to simplify program development:

- 1. Use of macros in implementing special-purpose languages.
- 2. Emulation of non-standard machine architectures.
- 3. Development of cross-assemblers.
- 4. Implementation of additional control structures.
- 5. Operating systems interface macros.

4-3. As macros are developed by a team of programmers, it is important to document each macro and its usage for each member of the team. The examples below should be studied for both their procedural content and the method of documenting them.

4-4. SPECIAL PURPOSE LANGUAGES.

4-5. A wide variety of microcomputer designs can be broadly classed as 'controller' designs. In these designs, the microcomputer is the controlling element in sequencing and decision-making as real-time events are sampled and directed. An example of this is a traffic control system. In this situation, it is useful to define a 'language' via macros which suits the particular application. After the macros are defined, an application programmer can use them as primitive language elements. If properly defined, the application language is easily programmed and can allow considerable machine independence. Further, the macros can incorporate debugging facilities to aid the application programmer.

4-6. In the traffic system defined here, the following hardware elements are present:

 central and corner traffic lights which display green, yellow, red, or are off completely.
 pushbutton switches for pedestrian crosswalks.
 road treadles for sensing the presence of an automobile at an intersection.
 a central controller box.

4-7. The central controller box contains a microprocessor connected through external logic to relays which control the lights and to latches which hold sensor input information. The controller also contains a time-of-day clock which counts hours from 0 through 23. The program which is run on the microprocessor is contained in PROM and is tailored to each intersection for traffic control.

4-8. We first define a set of macros to perform simple traffic-control functions via the system. These are shown in Figure 4-1. The system is configured such that the central traffic light is controlled by the microprocessor port number 0 (given by LIGHT). The time-of-day clock is read from port 3 (given by CLOCK). The north-south direction of the traffic light is controlled by the high order 4 bits of output port 0, and the east-west direction is controlled by the low order 4 bits of port 0. When either of these fields is set to 0, 1, 2, or 3, then the light in that direction is turned off or set to red, yellow, or green, respectively. Thus, the SETLITE macro sets the specified direction to the appropriate color.

4-9. The TIMER macro uses the cycle time of the microprocessor (one cycle = 400 nanoseconds) to construct an inline timing loop, based on the number of seconds delay requested.

4-10. Additional macros are provided for automobile treadles and pedestrian pushbuttons. For treadles (macro TREAD?) the sensors are attached to port 1 of the microprocessor (TRINP). The treadles require a 'reset' operation which is performed via port 1 (TROUT). At any intersection, the treadles are numbered clockwise from north from 0 through a maximum of 7. Each sensor and reset position of the treadle port corresponds to one bit position of port 1. Thus treadle #0 sensor is read from bit 0 of port 1 and reset via bit 0 of port 1. The TREAD? macro is used to sense the presence of a latched value for treadle #TR and, if on, the sensor is reset with control transferring to the label given by #IFTRUE.

4-11. Latched pedestrian pushbuttons are processed by the macro PUSH?. A latched pushbutton is sensed on input port 0 (CWINP) as a sequence of 1's and 0's in the least significant positions, corresponding to the switches at the intersection. Thus, if there are four pedestrian pushbuttons, bits 0, 1, 2, and 3 corresponds to these switches. A set bit in any of these positions indicates that a button has been pushed. All the crosswalk latches are reset whenever input port 0 is read.

4-12. Figure 4-2 shows a program written in the macros for controlling a rather simple intersection. Here, the lights are merely sequenced in proper fashion for traffic control.

4-13. Figure 4-3 shows a more complex intersection control program. In this case, heavy traffic normally occurs in an East-West direction. Light traffic from a residential section occurs in a North-South direction. Here, the lights favor traffic in the East-West direction until an automobile treadle or a pedestrian pushbutton is activated.

FIGURE 4-1 ; NLIST ; : MACRO LIBRARY FOR TRAFFIC CONTROL APPLICATION : ; THIS LIBRARY CONTAINS SEVERAL MACROS WHICH ; DEFINE A LANGUAGE FOR A TRAFFIC CONTROL APPLICATION. ; THE LANGUAGE IS DEFINED AS FOLLOWS: ; SETLITE DIR, COLOR - SET THE COLOR LIGHT IN THE DIRECTION SHOWN ; WHERE COLOR IS OFF, RED, YELLOW, OR GREEN AND ; DIRECTION IS 'NS' FOR NORTH-SOUTH OR 'EW' FOR ; EAST-WEST. ; ; TIMER SECONDS - DELAY THE NUMBER OF SECONDS SHOWN ; ; LOW, HIGH, LABEL ; CLOCK - TRANSFER CONTROL TO THE 'LABEL' IF ; THE CURRENT HOUR (0-23) IS BETWEEN 'LOW' ; AND 'HIGH'. ; ; ; RETRY LABEL - TRANSFER CONTROL TO 'LABEL'. ; ; ; TREAD? TR,LABEL - INTERROGATE TREADLE NUMBER 'TR' AND ; IF THE INPUT IS SET, RESET IT AND TRANSFER ; CONTROL TO 'LABEL'. ; ; PUSH? LABEL - CHECK IF ANY PUSHBUTTON HAS BEEN PUSHED. ; IF SO, TRANSFER CONTROL TO 'LABEL'. ; ; ; ; INPUT PORTS FOR LIGHT AND CLOCK LIGHT EQU 0 ;TRAFFIC LIGHT CONTROL ;24 HOUR CLOCK (0-23) CLOCK EQU 3 ; ; CONSTANTS FOR TRAFFIC LIGHT CONTROL ; ;NORTH-SOUTH BITS BITSNS EQU 4 BITSEW EQU 0 ;EAST-WEST BITS OFF EQU O RED EQU 1 YELLOW EQU 2 ;TURN LIGHT OFF ;RED LIGHT :YELLOW LIGHT GREEN EQU 3 ;GREEN LIGHT ; ; ; SET LIGHT IN DIRECTION #DIR (NS, EW) TO #COLOR (OFF, ; RED, YELLOW, GREEN) SETLITE MACRO #DIR,#COLOR A, #COLOR.SHL.BITS#DIR ;READY COLOR BITS LD ;OUTPUT TO LIGHT OUT (LIGHT),A

MEND

; ; TIMER FOR NUMBER OF SECONDS TO DELAY TIMER MACRO #SECOND LD BC,1000*#SECOND ;SECONDS TIMES MSECS L%NEXP PUSH BC ;SAVE IT LD B,191 ;MILLISECOND COUNTER K%NEXP K%NEXP DJNZ ;LOOP FOR 1 MSEC POP BC DEC BC ;DECREMENT MSEC COUNT LD A,B CHECK FOR END OF SECONDS OR С JR NZ,L%NEXP :LOOP FOR MORE ; ARRIVE HERE AFTER APPROXIMATE DELAY OF 'SECONDS' MEND ; ; ; CHECK CLOCK AND JUMP TO #IFTRUE IF TIME IS BETWEEN #LOW AND #HIGH CLOCK? MACRO #LOW, #HIGH, #IFTRUE MLOCAL L2 ΙN A,(CLOCK) ;READ CLOCK ; IF UPPER LIMIT NOT INPUT, DON'T CHECK IT MIF '#HIGH'='' THEN L2 #HIGH ;EQUAL OR GREATER? CP JR NC,F%NEXP ; IF SO, SKIP OUT L2 MNOP СР #LOW ;LESS THAN LOW VALUE? JP NC, #IFTRUE ; IF SO, EXIT TO LABEL F%NEXP MEND ; ; ; RETRY BY GOING TO '#LABEL' MACRO #LABEL RETRY JP #LABEL MEND ; ; ;TREADLE INPUT PORT TRINP EOU 1 EQU TROUT 1 ;TREADLE OUTPUT PORT ; ; CHECK IF TREADLE '#TR' HAS BEEN SENSED. IF SO, RESET ; AND EXIT TO LABEL '#IFTRUE'. TREAD? MACRO #TR, #IFTRUE ;CHECK FOR TREADLE SET ΙN A.(TRINP) AND 1.SHL.#TR ;CHECK FOR THIS TREADLE Z,F%NEXP JR ;IF NOT, SKIP OUT A,1.SHL.#TR ;ELSE RESET THE BIT LD ;TO CLEAR IT OUT (TROUT),A JP #IFTRUE ;EXIT VIA LABEL F%NEXP MEND ; ; CWINP EQU 0 ; PEDESTRIAN PUSHBUTTON PORT ; ; JUMP TO LABEL '#IFTRUE' IF ANY PUSHBUTTON PUSHED. ; READING THE PORT CLEARS ALL INPUT. PUSH? MACRO **#IFTRUE**

~

FIGURE 4-2 TRAFF LOC OBJ.CODE		ECTION MOSTEK MACRO-80 ASSEMBLER V2.0 PAGE SOURCE-SIMT PASS2 FIG4D2 FIG4D2 FIG4D2 REL
	1	TITLE FIGURE 4-2 TRAFFIC INTERSECTION
		; ; SIMPLE INTERSECTION EXAMPLE WHERE THE TRAFFI ; LIGHTS ARE MERELY SET AND RESET IN THE PROPE; ; SEQUENCE.
		INCLUDE THE MACRO LIBRARY IN THE ASSEMBLY
0000	9 129 138 10 139	
		; ; ; start of control
0000' 0004 0008	15 144 16 148 17 152	SETLITE EW,RED
		CHANGE LIGHTS
001A 0028 002C	23 183 24 187	TIMER 3 ;DELAY 3 SECONDS SETLITE NS,RED SETLITE EW,GREEN
0030	25 191	TIMER 15 ;DELAY 15 SECONDS ; ; CHANGE BACK
003E 0042 0050 0053	29 206 30 210 31 222 32 225	; SETLITE EW,YELLOW TIMER 3 ;3 SECONDS

JRE 4-3 COMPLEX INTERSECTION MOSTEK MACRO-80 ASSEMBLER V2.0 PAGE 1 OBJ.CODE STMT-NR SOURCE-STMT PASS2 FIG4D3 FIG4D3 FIG4D3 REL TITLE FIGURE 4-3 COMPLEX INTERSECTION ; =00043 CWCNT EOU 4 :4 CROSSWALK SWITCHES =0000 EQU O :NAME FOR TREADLE ZERO 4 LULLO 5 LULL1 EQU 1 =0001 :NAME FOR TREADLE ONE ; ; INCLUDE MACRO LIBRARY ; INCLUDE FIG4D1 0 9 FIGURE 4-1 ; 129 138 LISŤ 139 ELIST O :NO LIST EXPANSIONS 10 ; ; START OF PROGRAM FOR CONTROL =0000' 14 143 CYCLE ;ENTER HERE FOR EACH MAJOR CY CLE OF THE LIGHTS CLOCK? 2,5,NIGHT ;BETWEEN 2 AND 5 AM? 0 15 144 ; NOT BETWEEN 2 AND 5 AM, SO PROCESS ; EAST-WEST GETS MAJOR TRAFFIC FLOW SETLITE NS, RED ۱B 18 158)F SETLITE EW, GREEN 19 162 ; 167 SAMPLE =0013' 21 ; SAMPLE THE BUTTONS AND TREA DLES PUSH? SWITCH ;ANYONE THERE? 13 22 168 1 A I 23 174 TREAD? LULLO, SWITCH ; ANY CARS? TREAD? LULL1,SWITCH 183 27 24 25 192 CLOCK? 2,,NIGHT ;PAST 2AM? 34 26 202 RETRY SAMPLE ;NO, LOOP FOR ANOTHER SAMPLE 3 B ; ; =003E' 29 207 SWITCH ;SOMEONE IS WAITING, CHANGE T HE LIGHTS ЗE 30 208 SETLITE EW, YELLOW ;SLOW THEM DOWN 212 TIMER 3 ;3 SECONDS 42 31 SETLITE EW, RED ; STOP THEM 50 32 224 SETLITE NS, GREEN ; LET NORHT-SOUTH GO 54 33 228 ;FOR A WHILE 58 34 232 TIMER 23 =0066' 36 245 DONE? ; IS ALL THE TRAFFIC THROUGH ON NORHT-SOUTH? TREAD? LULLO, NOTDONE ; CHECK THE TREADLES 37 66 246 73 38 255 TREAD? LULL1,NOTDONE ; NEITHER TREADLE IS SET, CYCLE FOR ANOTHER LOOP 80 40 265 RETRY CYCLE ; ; =0083' 43 270 NOTDONE WAIT 5 SECONDS AND TRY AGAI 183 44 271 TIMER 5 191 45 283 RETRY DONE? ; ; 288 NIGHT =0094' 1 48 ;THIS IS NIGHTTIME, FLASH TH LIGHTS)94 49 289 SETLITE EW, OFF ; TURN OFF)98 293 SETLITE NS,OFF 50

FIGURE 4-3 COMPLEX INTERSECTION MOSTEK MACRO-80 ASSEMBLER V2.0 PAGE LOC OBJ.CODE STMT-NR SOURCE-STMT PASS2 FIG4D3 FIG4D3 FIG4D3 REL 009C 51 297 TIMER 1 ;WAIT WITH OFF OOAA 52 309 SETLITE EW, YELLOW ; CAUTION ON 53 313 SETLITE NS, RED ; STOP ON OOAE 54 00B2 317 TIMER 1 ;DELAY 00C0 55 329 RETRY CYCLE ;GO AROUND AGAIN 00C3 56 332 END

When the lights change to allow North-South flow, all traffic must be allowed to clear the lanes before a change to East-West can be done again. During early morning hours, the lights merely flash yellow in the East-West direction and red the in North-South direction. In the program shown, each major cycle of the traffic light enters as 'CYCLE' where the time of day is tested. If between 2 and 5AM, then control transfers to 'NIGHT' where the lights are merely flashed. Otherwise, the treadles and pedestrian pushbuttons are sampled until a change is required.

4-14. Macro-based languages of this sort can easily incorporate debugging facilities. In this example, a debugging flag (DEBUG) is set for use in the macro shown in Figure 4-4. The debug flag, when set, allows trace information to be output to the console device rather than code to activate the system. Here calls to MOSTEK's FLP-80DOS are shown to produce the trace output shown in Figure 4-5. After debugging is complete, the DEBUG flag can be reset and Assembly done once more for the final system. This idea can be extended to the other macros in the system to simulate operation of the system.

4-15. In this application of macros, a simple to use 'language' was developed for a specific use to ease programming and debugging of a final system employing the microprocessor.

4-16. MACHINE EMULATION.

4-17. A second application of macros is found in 'emulation' of a machine operation code set which is different from the given microprocessor. In this case, after the machine to be emulated is defined, a set of macros are written to emulate the opcodes. Each macro assumes the name of an opcode, and the macro body contains instructions which perform the same function as the opcode on the emulated machine. After the macros are defined, then a program can be written using these opcodes which expand to the given microprocessor instructions but which emulate the operation of the new machine.

4-18. In this example, a new machine is defined as an analog sensing and control element in a larger electronic environment. The new machine is based around a 16-bit word length and it is a 'stack machine', in which data can be loaded to the top of a 'stack' of data elements, automatically pushing existing elements deeper onto the stack. Arithmetic operations are performed on the topmost stack elements, automatically absorbing the stacked operands as the arithmetic is performed. The opcodes of the new machine are defined as follows:

SIZ n -reserves n 16-bit elements for the maximum size of the operand stack. This operation code must be provided at the beginning of the program.

LOC	OBJ.CODE	STMT-NR	MOSTEK MACRO-80 ASSEMBLER V2.0 PAGE 1 SOURCE-STMT PASS2 FIG4D4 FIG4D4 FIG4D4 REL
			; FIGURE 4-4 DEBUGGING MACRO
			; THIS MACRO DEFINITION IS THE SAME AS FIGURE 4- ; EXCEPT THAT A DEBUGGING FACILITY HAS BEEN ADDEI
		7	; DEFINITIONS FOR DEBUG PROCESSING
	=FFFF =0000		TRUE EQU OFFFFH ;TRUE VALUE FALSE EQU .NOT.TRUE ;FALSE VALUE
	=0000		DEBUG DEFL FALSE ; INITIALLY FALSE ;
			; ; INPUT/OUTPUT PORTS FOR TRAFFIC LIGHT CONTROL
			;
	=0000		LIGHT EQU O ;TRAFFIC LIGHT
	=0003	15	CLOCK EQU 3 ;24 HOUR CLOCK (0-23)
			; BIT POSITIONS FOR TRAFFIC LIGHT CONTROL
	=0004		BITSNS EQU 4 ;NORHT-SOUTH
	=0000	19	BITSEW EQU O ;EAST-WEST
			; ; CONSTANT VALUES FOR LIGHT CONTROL
	=0000	22	
	=0001		RED EQU 1
	=0002 =0003		YELLOW EQU 2 GREEN EQU 3
		2.5	; ;
			SET LIGHT MACRO WITH DEBUGGING INFO
		30	SETLITE MACRO #DIR,#COLOR
		1 31	MIF .NOT.DEBUG THEN L1
		3 33	; DEBUGGING, PRINT INFO ON CONSOLE LD HL,MS%NEXP
		4 34	LD E, 1
		5 35	GLOBAL PTXT
		6 36	CALL PTXT
		7 37 8 38	JR L%NEXP MS%NEXP DEFM '#DIR CHANGING TO #COLOR',ODH,OAH,3
			L%NEXP MEXIT
		10 40	L1 MNOP
		11 41 12 42 13 43	LD A,#COLOR.SHL.BITS#DIR ;READY COLOR OUT (LIGHT),A ;OUTPUT IT MEND

FIGURE	4-5.
SAMPLE	OUTPUT

CHANGING	ТΟ	GREEN
CHANGING	ТО	RED
CHANGING	ΤO	YELLOW
CHANGING	ΤO	RED
CHANGING	ΤO	GREEN
CHANGING	ΤO	YELLOW
CHANGING	ΤO	GREEN
CHANGING	ΤO	RED
	CHANGING CHANGING CHANGING CHANGING CHANGING CHANGING	CHANGING TO CHANGING TO CHANGING TO CHANGING TO CHANGING TO CHANGING TO CHANGING TO CHANGING TO

- RDM i -reads the analog signal from input port i (0, 1, 2, or 3) to the top of the stack, automatically pushing the stack down.
- WRM i -writes the digital value from the top of the stack to the D-A output port given by i (0, 1, 2, or 3). The value at the top of the stack is removed.
- DUP -duplicates the item at the top of the stack.
- SUM -the top two elements of the stack are added, both operands are removed from the stack, and the resulting sum is placed on the top of the stack.
- LSR n -performs a logical shift of the topmost stack element to the right by n bits (1, 2, ..., 15), replacing the original operand by the shifted result. Note that LSR n performs a division of the topmost stack value by the divisor 2 to the nth power.
- JMP a -branches directly to the program address given by the label a.

4-19. Each of these opcodes can be emulated by using macros to define them in terms of the given microprocessor instructions. The complete definition of the macros is shown in Figure 4-6.

4-20. The SIZ macro sets the program origin (hence, it must be the first opcode used in a program), and the stack area is reserved. Double bytes of storage are reserved since a 16-bit word size is assumed.

4-21. In the following macros, the stack top is assumed to be in the HL register pair. Each operation which pushes the stack of the emulated machine causes the element in the HL register pair to be pushed onto the memory area designated as STACK.

4-22. The DUP opcode simply pushes the HL register pair to the memory stack. In the case of the SUM opcode, it is assumed that the programmer has loaded two values to the stack to be summed. Thus, the HL register pair contains the most recently loaded value, and the memory stack contains the next-to-most recently stacked value. The POP DE operation loads the second operand into the DE register pair, ready for adding to HL. The result goes into the HL register pair because the top of the stack of the emulated machine is located in the HL register pair.

4-23. The LSR macro generates a loop which shifts the HL register pair right the specified number of times.

4-24. The RDM and WRM opcodes are implemented by 'memory mapped' I/O

FIGURE 4-6 ; NLIST ; ***** STACK MACHINE OPCODE MACRO LIBRARY ; ; ; SET THE PROGRAM ORIGIN AND CREATE A STACK ; #SIZE SIZ MACRO ORG 0 ;SET STACK POINTER LD SP,STACK STACK ;GET PAST STACK JP 2*#SIZE ;SET UP STACK AREA DEFS STACK MEND ; ; ; DUPLICATE TOP OF STACK ; DUP MACRO PUSH ΗL MEND ; ; ; ADD THE TOP TWO STACK ELEMENTS ; SUM MACRO DE ;TOP OF STACK TO DE POP ADD HL, DE ; ADD AND PUT INTO HL MEND ; ; ; LOGICAL SHIFT RIGHT BY #LEN LSR MACRO #LEN B,#LEN ;COUNT OF SHIFTS LD L%NEXP XOR A ;RESET CARRY RR Н ;ROTATE H INTO CARRY ;ROTATE L WITH CARRY RR L L%NEXP ;LOOP FOR TOTAL COUNT DJNZ MEND ; ; ; JUMP TO A LABEL ; JMP MACRO # A JP # A MEND ; ; ;DEFINITION OF ADC INPUTS AND DAC OUTPUTS VIA ; MEMORY MAPPED I/O ; ADCO EQU 1080H ;A-D CONVERTER O ADC1 EQU 1082H ;A-D CONVERTER 1 ADC2 ;A-D CONVERTER 2 EQU 1084H ADC 3 EQU 1086H ;A-D CONVERTER 3 ; ;D-A CONVERTER O DACO EQU 1090H 1092H DAC1 EQU ;D-A CONVERTER 1

1094H;D-ACONVERTOR21096H;D-ACONVERTER3 DAC2 EQU DAC3 EQU ; ; ; READ A-D CONVERTER NUMBER #NUM ; RDM MACRO # N U M PUSH CLEAR THE STACK ΗL HL, (ADC#NUM) ;READ VIA MEMORY MAP LD MEND ; ; ; WRITE D-A CONVERTER NUMBER #NUM ; #NUM WRM MACRO LD (DAC#NUM), HL ; WRITE VIA MEMORY MAP POP HL ;RESTORE STACK MEND ; END OF MACRO LIBRARY ****** LIST

operations. That is, locations 1080H through 1087H are intercepted external to the given microprocessor and treated as external read operations. Thus a load of HL from 1080H and 1081H is treated as a read from A-D device 0, rather than from RAM. This applies also to devices ADC1, ADC2, and ADC3. Similarly, the D-A output values are written to locations 1090H through 1097H for devices DAC0 through DAC3.

4-25. Figure 4-7 shows a sample program written for the emulated machine. In this case, the machine is connected to four temperature sensors via ADCO through ADC3. The program continuously reads the four input values and computes their average value by summing and dividing by four. The average value is sent to DACO where it is used to set environmental controls.

4-26. The program begins by reserving 20 elements for the stack, which are more than enough. The program then cycles through 'LOOP', where the values are read and processed. The four RDM operations read the four temperature sensors, placing their data values on the top of the stack. The three SUM operations which follow perform pairwise addition of the temperature values, producing a single sum at the top of the stack. To obtain the average value, the LSR opcode is applied to perform a division by 4. The resulting average is then sent to DACO using the WRM opcode. Control then transfers back to 'LOOP' and the operation is repeated.

4-27. As in the previous example, debugging statements could be added to the macro to perform an emulation without the ADC and DAC hardware. These statements could take the form of additional macros used to print out values as the program is executed.

4-28. DEVELOPMENT OF CROSS-ASSEMBLERS.

4-29. Macros can be written to assemble another microprocessor's instruction set. The resultant object code may be used directly or may have to be translated to a different format by a utility program. Each opcode of the new machine is used as a macro name. Parameters are used if the opcode uses operands. The macro can decode the operands to produce the correct machine code. If any of the new machine's opcodes are the same as the Z80 opcodes, then the 'R' option must be used when the Assembler is executed.

4-30. Consider a portion of the 3870 microcomputer instruction set given in Figure 4-8. The corresponding macros to produce the correct object code are shown. Note that in this implementation, programs formed by the resultant cross-assembler must be non-linkable. This restriction exists because of the way in which the FLP-80DOS Linker processes external reference addresses. That is, such addresses are produced by the MACRO-80 Assembler with least significant byte first,

FIGURE 4-7 A-D LOC OBJ.CODE			ROGRAM MOSTEK MACRO-80 . SOURCE-STMT PASS2 FIG4D7	
		1	TITLE FIGURE 4-7	A-D AVERAGING PROGRAM
		;		CH ARE READ FROM A-D CONVI S
		;	; D-A CONVERTER O, THEN D	RESULTING VALUE TO THE LOOP FOR MORE.
		;	; ; INCLUDE MACRO LIBRARY ;	
0000		9	INCLUDE FIG4D6 ; FIGURE 4-6	
	82 10	91 92	LIST ELIST O	;NO LIST EXPANSIONS
0000 002E'	12 13	94 100 I	; SIZ 20 LOOP RDM 0	;RESERVE 20 LEVELS FOR ST ;READ ADCO
0032 0036 003A	15	104 108 112	RDM 1 RDM 2 RDM 3	;READ ADC1 ;READ ADC2 ;READ ADC3
UUSA	10	;	; ; ALL FOUR VALUES ARE STA	
003E 0040 0042	21	; 119 123 127	; SUM SUM SUM	;ADC3+ADC2 ;(ADC3+ADC2)+ADC1 ;((ADC3+ADC2)+ADC1)+ADC0
0044	25	; 133	; ; SUM IS AT TOP OF STACK LSR 2	, DIVIDE BY 4 ;SHIFT RIGHT BY 2 = DIVIDE 4
004D 0051 0054	27	140 144 147	WRM O JMP LOOP END	;WRITE RESULT TO DACO ;REPEAT THE PROCESS

3870 CROSS ASSEMBLER MACROS

THESE MACROS ARE EXAMPLES WHICH COULD BE EXTENDED TO PRODUCE A 3870 CROSS ASSEMBLER RUNNING UNDER MACRO-80.

REGISTER DEFINITION

5 EOU 0 C H EQU ODH) EQU 0 E H ; ;)CI #ADDR ;LOAD DATA COUNTER MACRO DEFB 2AH, (#ADDR.SHR.8).AND.OFFH, #ADDR.AND.OFFH MEND ; AS MACRO ;ADD TO SCRATCHPAD # R MLOCAL LERR MIF #R.UGT.OEH THEN LERR DEFB OCOH.OR.#R MEXIT MERROR *** OUT OF RANGE *** LERR MEND ; SL. MACRO # N ;SHIFT LEFT MLOCAL L1,L2,L3 MIF #N=4 THEN L1 ELSE L2 CHECK RANGE OF OPERAND L1 MNOP DEFB 15H MEXIT L2 MIF #N=1 THEN L3 *** OUT OF RANGE *** MERROR MNOP L3 DEFB 13H MEND ; LI MACRO #OP ;LOAD IMMEDIATE DEFB 20H DEFB #OP.AND.OFFH MEND ; LISL MACRO # A LERR MLOCAL MIF #A.UGT.7 THEN LERR DEFB 68H.OR.#A MEXIT *** OUT OF RANGE *** LERR MERROR MEND ; BR7 #AA MACRO MLOCAL LERR DEFB 8FH MIF (#AA-\$>128).OR.(#AA-\$<0) THEN LERR ;CHECK RANGE DEFB #AA-\$ MEXIT *** OUT OF RANGE *** LERR MERROR MEND

;	
BF	MACRO #T,#AA
	MLOCAL LERR
	MIF #T.UGT.OFH THEN LERR ;CHECK RANGE
	DEFB 90H.OR.#T
A%NEXP	EQU #AA-\$
	MIF (A%NEXP>128).OR.A%NEXP<0) THEN LERR ;CHECK RANGE
	DEFB A%NEXP
	MEXIT
LERR	MERROR *** OUT OF RANGE ***
	MEND

while the 3870 requires most significant byte first. Note also that cross-assemblers developed under MACRO-80 must follow the Z80 conventions for forming constants and expressions.

4-31. PROGRAM CONTROL STRUCTURES.

4-32. Macros can be used to provide program-control statements which resemble those found in many high-level languages. Figure 4-9 shows a set of macros which define a simple language for performing 16-bit integer operations. The following paragraphs describe each type of statement allowed in a program written around these macros.

4-33. LET var1 = var2 or LET var1 = var2 <op> var3

The LET statement allows a variable to be set equal to another variable or to the result of an operation performed on two variables. The allowed operations are addition $(\langle op \rangle = +)$, subtraction (-), multiplication (*), and division (/). The blanks between the operands are required.

4-34. TEST var1 <relop> var2 THEN label1 ELSE label2

The TEST statement allows two variables to be compared as being equal (=), less than (<) or greater than (>). If the result is true, then a branch is made to label1. Otherwise a branch is made to label2. The ELSE-clause is optional. If it is not present and a false condition is encountered, then the next statement in sequence will be processed.

4-35. DCL var1 INIT n

The DCL statement declares variables used in the program. Note that all variables must be declared. The initial value n is optional and defaults to zero.

4-36. DO var1 = var2 TO var3

The DO statement, together with the ENDDO statement, allows writing of loops. The value of var1 is initially set to var2. Each pass through the loop increments var1 until it equals the value of var3. DO loops may be nested, but the program stack must always be balanced between the DO and ENDDO statements.

4-37. ENDDO

This signals the end of a DO loop.

4-38. READ var1, var2,...

This statement reads and converts to binary sequences of two

FIGURE 4-9 ; NLIST ; PROGRAM CONTROL STRUCTURES VIA MACROS ; ; ; PRINT message PRINT MACRO # A GLOBAL PTXT E,CHNL+1 LD ;CHANNEL NBR HL,MS%NEXP LD CALL PTXT L%NEXP JR MS%NEXP DEFM **'#A',ODH,OAH,3H** L%NEXP MEND ; ***** ; ; LET var1 = var2 <op> var3 ******* MACRO #A #B #C #D #E LET MLOCAL L1,L2,L3,L4,L5,LS,LERR MIF '#B'='=' THEN L1 ELSE LERR ;SYNTAX CHECK L1 MNOP HL,(#C) ;GET VAR2 LD MIF '#D'='' THEN LS ; IF NO OPERATOR, DO ASSIGNMENT DE,(#E) ;GET VAR3 LD '#D'='+' THEN L2 MIF ;CHECK OPERATOR '#D'='-' THEN L3 MIF '#D'='*' THEN L4 MIF MIF '#D'='/' THEN L5 MERROR ***** ILLEGAL OPERATOR ***** MEXIT ; L2 MNOP ADD HL,DE MGOTO LS L3 MNOP OR А SBC HL,DE MGOTO LS L4 MNOP ; MULTIPLY BY SEVERAL ADDITIONS ;CHECK FOR MULT BY ZERO LD A,D OR Ε JR NZ, I%NEXP HL,0 ; IF SO, ZERO RESULT LD JP K%NEXP I%NEXP DEC DE ;CHECK FOR MULT BY ONE LD A,D OR E JR Z,K%NEXP ;YES, JUST PUT IN VALUE LD BC, (#C) ;GET VAR2 HL,BC L%NEXP ADD DΕ DEC LD A,D ;CHECK FOR END

ζN EX P	OR JR	E NZ,L%NEXP
	MGOTO	LS
ERR	MERROR MEXIT	**** BAD SYNTAX ****
5	MNOP LD	A,D ;CHECK FOR DIVIDE BY ZERO
ZNFYD	JR	E NZ,C%NEXP '*** OVERFLOW ERROR' Z%NEXP BC,0 ;RESULT
%NEXT	OR SBC INC JR	A ;RESET CARRY HL,DE ;SUBTRACT UNTIL DONE
c	L D L D	L,C ;PUT INTO HL H,B
	MNOP LD MEND	(#A),HL ;SAVE IN VAR1
; ; * * * * * *	*****	****
;	-	> var2 THEN label1 [ELSE label2]
rest	MACRO	**************************************
L1	MNOP LD LD OR	HL,(#A) ;GET VAR1 DE,(#C) ;GET VAR2 A
L2	SBC MIF JP MGOTO	HL,DE ;SUBTRACT FOR COMPARE '#B'='=' THEN L2 ELSE L3 ;CHECK OPERATOR Z,#E ;IF EQUAL (TRUE), DO JUMP LCONT
L3 L4	MIF MNOP	'#B'='<' THEN L4 ELSE L5
L5	JP MGOTO MIF	C,#E ;IF LESS THAN, JUMP LCONT '#B'='>' THEN L6 ELSE LERR
L6	MNOP JR JP MGOTO	Z,L%NEXP ;IF EQUAL TO THEN FALSE NC,#E ;IF GREATER THAN, JUMP LCONT
; LERR	MERROR MEXIT	**** BAD SYNTAX ****
; LCONT L%NEXP	MNOP	
L7	MNOP	'#F'='ELSE' THEN L7 ELSE L8 ;CHECK FOR IF CLAUSE
	JP	#G ;JUMP TO FALSE LABEL

MEXIT MNOP L8 MEND ; ********************* ; DCL var INIT n **** #A #B #C DCL MACRO MLOCAL L1,L2,L3 '#B'='INIT' THEN L1 ELSE L2 MIF '#C'='' THEN L2 L1 MIF #A # C ;DECLARE VARIABLE DEFW MEXIT L2 MNOP # A DEFW O ;DEFAULT TO ZERO MEND ; *********** ; ; DO var1 = var2 TO var3 ****** #A #B #C #D #E DO MACRO MLOCAL L1, L2, LERR MIF '#B'='=' THEN L1 ELSE LERR ;SYNTAX CHECK '#D'='TO' THEN L2 MIF L1 ***** BAD SYNTAX ***** LERR MERROR MEXIT ; L2 MNOP LD HL,(#C) ;GET VAR2 LD DE,(#E) ;GET VAR3 IX,L%NEXP ;GET LOOP BACK LABEL LD L%NEXP LD (#A),HL ;SET VAR1 PUSH ΗL ;PUSH VALUES ONTO STACK PUSH DE PUSH IX MEND ; ; ; ENDDO ****** ENDDO MACRO POP IX ;LOOP ADDRESS POP DE ;FINAL VALUE ΗL ;CURRENT VALUE POP ΗL INC ;INCREMENT VAR1 PUSH ΗL A ;CHECK IT ЭR SBC HL,DE POP ΗL Z,KK%NEXP ;LAST TIME THRU NC,L%NEXP ;IF DONE, SKIP OUT JR JR KK%NEXP JP (IX) ;ELSE LOOP L%NEXP MEND

READ var1, var2,... # A ₹EAD MACRO MLOCAL L1,L2 ; #A FIRST TIME USAGE OF PARAMETER GLOBAL ECHO, ASBIN L.D E,CHNL [1 MNOP CALL ECHO ;READ A CHARACTER LD A,D ; PREPARE TO CONVERT CALL ASBIN ; CONVERT AND OFH RLCA RLCA RLCA RLCA PUSH ΑF CALL ECHO ;GET NEXT ONE LD A,D CALL ASBIN AND OFH LD L,A ;SAVE IT POP ΑF OR L LD L,A LD Η,Ο ;SAVE RESULT LD (#PRM),HL LN%NEXP CALL ECHO ;GET NEXT INPUT CHAR LD ;CHECK CHARACTER A,D CP ODH ;CARRIAGE RETURN? ;YES, SKIP OUT JP Z,P%NEXP ',' СР ; COMMA? NZ,LN%NEXP ;NO, LOOP FOR ANOTHER JR MNEXT 1 THEN L1 ELSE L2 ;CHECK FOR MORE ARGS L2 MNOP P%NEXP CALL CRLF MEND ; *** ; ; WRITE var1, var2,... ; ************* WRITE MACRO #A,#B ; #A FIRST TIME USAGE OF PARAMETER MLOCAL L1 GLOBAL PTXT, CRLF, PADDO LD E,CHNL+1 ;OUTPUT CHANNEL L1 MNOP ;OUTPUT MESSAGE LD HL,MS#PRM CALL PTXT LD HL,(#PRM) CALL PADDO ;WRITE OUT IN HEX L#PRM JR '#PRM = ' MS#PRM DEFM

DEFB 3 L#PRM MNEXT 1 THEN L1 CALL CRLF MEND ; ; ; GOTO label ; **** GOTO MACRO # A # A JP MEND ; ***************** ; ; EXIT ; MACRO EXIT GLOBAL JTASK LD A,1 JP JTASK MEND ***************** ; END OF MACRO LIBRARY ****** LIST

hexadecimal characters, placing them into the variables var1, var2, etc.

4-39. WRITE var1,var2,...

This statement writes each variable in the list in the form 'name = value', where name is the name of the variable and value is its value in four hexadecimal digits.

4-40. PRINT 'message'

This macro prints a message of any length on the console.

4-41. GOTO label

This macro transfers control to the specified label.

4-42. EXIT

This macro transfers control back to the FLP-80DOS Monitor.

4-43. Figure 4-10 shows two simple programs which demonstrate use of these macros. The first program calculates n numbers in a Fibonacci series where n is a number input from the console keyboard. The second program generates n x n combinations of addition, subtraction, multiplication, and division, where n is read from the console keyboard. Figure 4-11 shows sample output from the programs.

4-44. OPERATING SYSTEM INTERFACE.

4-45. The fifth area where macros are useful is in providing systematic and simplified mechanisms for access to operating system functions. These macros can allow easy use of the operating system's I/O facilities, service routines, and system support routines.

4-47. In this example, a set of macros are shown which provide access to FLP-80DOS I/O facilities. Use of these macros can eliminate a large portion of the drudgery of assembly language programming. Furthermore, the macros reduce programming errors and provide for some checking of parameters associated with the operating system calls. It is assumed in this discussion that the user is acquainted with Section 9 of the FLP-80DOS manual (IOCS).

4-47. Figure 4-12 shows a file which has definitions of each IOCS related parameter. This file is included in programs which use IOCS to provide a set of standard symbols for use in the macros and in the program itself. (The file is called IODEF).

4-48. The set of macros shown in Figure 4-13 allows a simplified

FIGURE 4-10. LOC OBJ.CODE	STMT-NR		MOSTEK MACRO-80 ASSEMBLER V2.0 PAGE SOURCE-STMT PASS2 FIG410 FIG410 FIG410 REL			
	1		TITLE FIGURE 4-10.			
			; ; SAMPLE	E USAG	GE OF CONTROL	L STRUCTURES
			; : INCLUD	DE MAG	CRO DEFINITIO)NS
0000		7	;		JDE FIG4D9	
0000			; FI	GURE		
	269	276	;	LIST		
= 0 0 0 0	9	278	CHNL	EQU	0	
			•			UP TO N FIBONACCI NUMBH HE CONSOLE KEYBOARD
0000		283	,		r 'ENTER 2 HI	EX DIGITS'
0000 1E01	2	284 285		LD		;CHANNEL NBR
0002 210A00' 0005 CDFFFF		286 287		LD CALL	HL,MS0001 PTXT	
0008 1815 000A'454E5445		288 289		JR DEFM	LOOO1 'ENTER 2 HE	K DIGITS',ODH,OAH,3H
52203220	0	200	1150001			r bigits yobnyonnyon
48455820 44494749						
54530D0A 03						
=001F'		290 291	L0001	MEND		
001F	15	292		READ		
		293	; N FIRS	ST TIM	AL L1,L2 1e usage of 1	
001F 1E00		295 296			AL ECHO,ASBI) E,CHNL	N
	5	297 298		MNOP		DEAD & CHADACTER
0021 CDFFFF 0024 7A	7	299		LD	ECHO A,D	;READ A CHARACTFR ;PREPARE TO CONVERT
0025 CDFFFF 0028 E60F		300 301		CALL AND	ASBIN OFH	;CONVERT
002A 07	10	302		RLCA		
002B 07 002C 07		303 304		RLCA RLCA		
002D 07 002E F5		305 306		RLCA PUSH	AF	
002F CD2200'	15	307		CALL	ECHO	;GET NEXT ONE
0032 7A 0033 CD2600'	17	308 309		LD CALL	A,D ASBIN	
0036 E60F 0038 6F		310 311		A N D L D	OFH L,A	;SAVE IT
0039 F1	20	312		POP	AF	
003A B5 003B 6F	22	313 314		OR LD	L L,A	
003C 2600 003E 22EB00'		315 316		LD LD	H,O (N),HL	;SAVE RESULT
0041'CD3000' 0044 7A	25		LN0002		ECHO	;GET NEXT INPUT CHAR ;CHECK CHARACTER
0045 FEOD		318		СР	A,D ODH	CARRIAGE RETURN?
					,	

3E 4-10. MOSTEK MACRO-80 ASSEMBLER V2.0 PAGE 2 OBJ.CODE STMT-NR SOURCE-STMT PASS2 FIG410 FIG410 FIG410 REL CA4E00' 28 320 JP Z, P0002 ;YES, SKIP OUT ;COMMA? FE2C 29 321 CP ۰,۰ NZ,LN0002 20F3 30 322 JR ;NO, LOOP FOR ANOTHER 31 323 MNEXT 1 THEN L1 ELSE L2 ;CHECK FOR MORE AR 32 324 L2 MNOP 33 325 P0002 =004E' CDFFFF 34 326 CALL CRLF 35 327 MEND 16 328 LET COUNT = ONE 1 329 MLOCAL L1,L2,L3,L4,L5,LS,LERR 2 330 '='='=' THEN L1 ELSE LERR ;SYNTAX CHECK =FFFFMIF 3 331 L1 MNOP ;GET VAR2 2ADF00' 4 332 LD HL,(ONE) MIF ''='' THEN LS ; IF NO OPERATOR, DO ASSIGNM =FFFF 5 333 ENT 57 334 LS MNOP +'22E900' 58 335 Z0003 LD (COUNT),HL ;SAVE IN VAR1 59 336 MEND 7 17 337 LET A = ONE1 338 MLOCAL L1,L2,L3,L4,L5,LS,LERR 2 339 '='='=' THEN L1 ELSE LERR ;SYNTAX CHECK MIF =FFFF 3 340 L1 MNOP 7 2ADF00' 4 341 LD ;GET VAR2 HL, (ONE) 5 ''='' THEN LS ; IF NO OPERATOR, DO ASSIGNM =FFFF342 MIF ENT 57 343 LS MNOP A'22E300' 58 344 Z0004 LD (A),HL ;SAVE IN VAR1 59 345 MEND D 18 346 LET B = TWOMLOCAL L1,L2,L3,L4,L5,LS,LERR 1 347 '='='=' THEN L1 ELSE LERR ;SYNTAX CHECK =FFFF2 348 MIF 3 349 L1 MNOP D 2AE100' 4 LD ;GET VAR2 350 HL,(TWO) 5 ''='' THEN LS ; IF NO OPERATOR, DO ASSIGNM =FFFF351 MIF ENT 57 352 LS MNOP 0'22E500' 58 353 Z0005 LD (B),HL ;SAVE IN VAR1 59 354 MEND 33 19 355 WRITE A,B ; A FIRST TIME USAGE OF PARAMETER MLOCAL L1 2 357 3 358 GLOBAL PTXT, CRLF, PADDO 53 1E01 4 359 ;OUTPUT CHANNEL LD E,CHNL+1 5 360 L1 MNOP 55 217300' 6 361 LD ;OUTPUT MESSAGE HL,MSA 58 CD0600' 7 362 CALL PTXT 5B 2AE300' 8 363 LD HL,(A) 6E CDFFFF 9 364 CALL PADDO ;WRITE OUT IN HEX 71 1805 10 365 JR LA 73'41203D20 11 366 MSA DEFM 'A = '77 03 12 367 DEFB 3 =0078' 13 368 LA 14 369 MNEXT 1 THEN L1 5 370 L1 MNOP 78 218600' ;OUTPUT MESSAGE 6 371 LD HL,MSB 7B CD6900' 7 372 CALL PTXT 7E 2AE500' 8 373 LD $HL_{(B)}$

FIGURE 4-10. LOC OBJ.CODE	STMT-NR SOURCE-	MOSTEK MACRO-80 ASSEMBLER V2.0 PAGE 3 STMT PASS2 FIG410 FIG410 FIG410 REL
0081 CD6F00' 0084 1805 0086'42203D20 008A 03 =008B' 008B CD4F00'	9 374 10 375 11 376 MSB 12 377 13 378 LB 14 379 15 380 16 381	CALL PADDO ;WRITE OUT IN HEX JR LB DEFM 'B = ' DEFB 3 MNEXT 1 THEN L1 CALL CRLF MEND
008E' =FFFF 008E 2AE300'	; 21 383 LAB1 1 384 2 385 3 386 L1 4 387	LET C = A + B MLOCAL L1,L2,L3,L4,L5,LS,LERR MIF '='='=' THEN L1 ELSE LERR ;SYNTAX CH MNOP LD HL,(A) ;GET VAR2
=0000 0091 ED5BE500' =FFFF 0095 19		MIF '+'='' THEN LS ;IF NO OPERATOR, DO A MENT LD DE,(B) ;GET VAR3 MIF '+'='+' THEN L2 ;CHECK OPERATOR MNOP ADD HL,DE MGOTO LS
0096 '22E700 ' 0099 =FFFF	57 394 LS 58 395 Z0007 59 396 22 397 1 398 2 399	MNOP LD (C),HL ;SAVE IN VAR1 MEND TEST COUNT > N THEN DONE MLOCAL L1,L2,L3,L4,L5,L6,L7,L8,LERR,LCONT MIF 'THEN'='THEN' THEN L1 ELSE LERR ;SYN'
0099 2AE900' 009C ED5BEB00' 00A0 B7 00A1 ED52	3 400 L1 4 401 5 402 6 403 7 404	HECK MNOP LD HL,(COUNT) ;GET VAR1 LD DE,(N) ;GET VAR2 OR A SBC HL,DE ;SUBTRACT FOR COMPARE
=0000 =0000 =FFFF 00A3 2803 00A5 D2DA00'	16 408 L6 17 409 18 410	MIF '>'='>' THEN L6 ELSE LERR MNOP JR Z,LOOO8 ;IF EQUAL TO THEN FALSE JP NC,DONE ;IF GREATER THAN, JUMP
= 0 0 A 8 ' = 0 0 0 0	24 412 LCONT 25 413 L0008	MIF ''='ELSE' THEN L7 ELSE L8 ;CHEC LAUSE
0088	31 416 23 417	MEND WRITE C ST TIME USAGE OF PARAMETER MLOCAL L1 GLOBAL PTXT,CRLF,PADDO
	4 421 5 422 L1 6 423 7 424	LD E,CHNL+1 ;OUTPUT CHANNEL

JRE 4-10. MOSTEK MACRO-80 ASSEMBLER V2.0 PAGE Ц OBJ.CODE STMT-NR SOURCE-STMT PASS2 FIG410 FIG410 FIG410 REL 3'43203D20 428 MSC DEFM 'C = '11 2 03 12 429 DEFB 3 =00BD' 13 430 LC 14 431 MNEXT 1 THEN L1 15) CD8C00' 432 CALL CRLF 16 433 MEND 0 24 434 LET COUNT = COUNT + ONE 1 435 MLOCAL L1, L2, L3, L4, L5, LS, LERR = FFFF2 436 MIF '='='=' THEN L1 ELSE LERR ; SYNTAX CHECK 437 L1 MNOP 3 0 2AE900' 438 HL, (COUNT) ;GET VAR2 4 LD 5 '+'='' THEN LS ; IF NO OPERATOR, DO ASSIGN 439 MIF =0000 MENT 3 ED5BDF00' 6 440 LD DE, (ONE) ;GET VAR3 '+'='+' THEN L2 ;CHECK OPERATOR 441 MIF =FFFF7 14 442 L2 MNOP 7 19 15 ADD HL,DE 443 15 444 MGOTO LS 445 LS 57 MNOP 18'22E900' 58 446 Z0010 LD (COUNT), HL ; SAVE IN VAR1 59 MEND 447 **:**B 25 448 LET A = B1 449 MLOCAL L1, L2, L3, L4, L5, LS, LERR 2 '='='=' THEN L1 ELSE LERR ;SYNTAX CHECK =FFFF450 MIF 451 L1 MNOP 3 'B 2AE500' 4 452 LD ;GET VAR2 HL,(B)5 ''='' THEN LS ; IF NO OPERATOR, DO ASSIGNM =FFFF453 MIF ENT 57 454 LS MNOP CE'22E300' 455 Z0011 LD 58 (A),HL ;SAVE IN VAR1 59 456 MEND 21 457 26 LET B = C458 1 MLOCAL L1, L2, L3, L4, L5, LS, LERR 2 =FFFF459 MIF '='='=' THEN L1 ELSE LERR ; SYNTAX CHECK 460 L1 MNOP 3 D1 2AE700' 4 461 LD $HL_{(C)}$;GET VAR2 MIF ''='' THEN LS ; IF NO OPERATOR, DO ASSIGNM =FFFF5 462 ENT 57 463 LS MNOP D4'22E500' 58 464 Z0012 LD (B),HL ;SAVE IN VAR1 59 465 MEND D7 27 466 GOTO LAB1 D7 C38E00' 467 1 JP LAB1 2 468 MEND : DA" 29 470 DONE EXIT GLOBAL JTASK 1 471 472 DA 3E01 2 LD A, 1 DC C3FFFF 3 473 JTASK JP 4 474 MEND ;)DF 31 476 DCL ONE INIT 1 1 477 MLOCAL L1, L2, L3 'INIT'='INIT' THEN L1 ELSE L2 2 478 =FFFFMIF '1'='' THEN L2 479 L1 =0000 3 MTF)DF '0100 4 480 ONE DEFW 1 ;DECLARE VARIABLE 5 481 MEXIT

FIGURE 4-10. LOC OBJ.CODE	STMT-NR S	MOSTEK MACRO-80 ASSEMBLER V2.0 PAGE SOURCE-STMT PASS2 FIG410 FIG410 FIG410 REL
00E1 =FFFF =0000	32 482 1 483 2 484 3 485 L	MIF 'INIT'='INIT' THEN L1 ELSE L2
00E1'0200	4 486 T 5 487	WO DEFW 2 ;DECLARE VARIABLE MEXIT
00E3 =0000	33 488 1 489 2 490	DCL A MLOCAL L1,L2,L3 MIF ''='INIT' THEN L1 ELSE L2
00E3'0000	6 491 L 7 492 A 8 493	A DEFW O ;DEFAULT TO ZERO MEND
00E5 =0000	34 494 1 495 2 496	DCL B MLOCAL L1,L2,L3 MIF ''='INIT' THEN L1 ELSE L2
00E5'0000	6 497 L 7 498 B 8 499	
00E7 =0000	35 500 1 501 2 502	DCL C MLOCAL L1,L2,L3 MIF ''='INIT' THEN L1 ELSE L2
00E7'0000	5 503 L 7 504 C 8 505	2 MNOP
00E9 =0000	36 506 1 507 2 508	DCL COUNT MLOCAL L1,L2,L3 MIF ''='INIT' THEN L1 ELSE L2
00E9'0000	6 509 L 7 510 C 8 511	2 MNOP
00EB =0000	37 512 1 513 2 514	DCL N MLOCAL L1,L2,L3 MIF ''='INIT' THEN L1 ELSE L2
00EB'0000	6 515 L 7 516 N 8 517	L2 MNOP

RE 4-10. OBJ.CODE	STMT-	NR SOURCE-	MOSTEK MACRO-80 AS STMT PASS2 FIG410 B	SSEMBLER V2.0 PAGE 6 FIG410 FIG410 REL
		; ADDIT	ION, SUBTRACTION, N	N BY N CALCULATIONS FOR MULTIPLICATION, AND DIVISION HE CONSOLE KEYBOARD.
)') 1E01	1 5 2 5	25 26	PRINT 'ENTER TWO H GLOBAL PTXT LD E,CHNL+1	
2 1F700 ' 2 CDAE00 ' 5 1817	45	27 28 29	LD HL,MS0022 CALL PTXT JR L0022	
7 • 454E5445 52205457 4F204845 58204449 47495453 0D0A03	65	30 MS0022	DEFM 'ENTER TWO HE	EX DIGITS',ODH,OAH,3H
=010E'		31 L0022 32	MEND	
E	45 5	33 34	READ N MLOCAL L1,L2	
		; N FIR	ST TIME USAGE OF PA	ARAMETER
E 1E00		36 37	GLOBAL ECHO, ASBIN LD E, CHNL	
	55	38 L1	MNOP	
0 CD4200' 3 7A		39 40		READ A CHARACTER
4 CD3400'	8 5	41	CALL ASBIN ;	CONVERT
7 E60F 9 07		42 43	AND CFH RLCA	
A 07		44	RLCA	
B 07		45	RLCA	
IC 07 ID F5		46 47	RLCA PUSH AF	
IE CD1101'	15 5	48	CALL ECHO ;	GET NEXT ONE
21 7A 22 CD1501'		49 50	LD A,D CALL ASBIN	
25 E60F		51	AND OFH	
27 6F		52		SAVE IT
28 F1 29 B5		53 54	POP AF OR L	
2A 6F	22 5	55	LD L,A	
2B 2600 2D 22EB00'		56 57	LD H,O LD (N),HL ;	SAVE RESULT
30'CD1F01'	25 5	58 LN0023	CALL ECHO ;	GET NEXT INPUT CHAR
33 7A 34 FEOD		59 60		CHECK CHARACTER CARRIAGE RETURN?
36 CA3D01'		61	JP Z,P0023 ;	YES, SKIP OUT
39 FE2C		62	CP ',' ;	COMMA?
3B 20F3		63 64		NO, LOOP FOR ANOTHER SE L2 ;CHECK FOR MORE ;
	32 50	65 L2	MNOP	
=013D' 3D CDBE00'		66 P0023 67	CALL CRLF	
	35 56	68	MEND	
40		69 70	TEST N = ZERO THEN MLOCAL $L1, L2, L3, L4$	LOOP ,L5,L6,L7,L8,LERR,LCONT

$\begin{array}{cccccccccccccccccccccccccccccccccccc$	FIGURE 4-10. LOC OBJ.CODE	STMT-NR SOU	MOSTEK MACRO-80 ASSEMBLER V2.0 PAGE 7 URCE-STMT PASS2 FIG410 FIG410 FIG410 REL
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	=FFFF	2 571	
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	0143 ED5B6502' 0147 B7	4 573 5 574 6 575	MNOP LD HL,(N) ;GET VAR1 LD DE,(ZERO) ;GET VAR2 OR A
=0000 26 582 MIF ''='ELSE' THEN L7 ELSE L8 ;CHI LAUSE 30 583 L8 MNOP 31 584 MEND 014D 47 585 D0 I = ONE TO N 1 586 MLOCAL L1,12,LERR =FFFF 2 587 MIF '='='=' THEN L1 ELSE LERR ;SYN =FFFF 3 588 L1 MIF 'D'='TO'THEN L2 014D 2ADF00' 8 590 LD HL,(ONE) ;GET VAR2 0150 ED5BEB00' 9 591 LD DE,(N) ;GET VAR3 0154 DD215801' 10 592 LD IX,L0025 ;GET LOOP BACK LABEL 0158 226702' 11 593 L0025 LD (I),HL ;SET VAR1 015B E5 12 594 PUSH HL ;PUSH VALUES ONTO STACK 015C D5 13 595 PUSH DE 015D DDE5 14 596 PUSH DE 015F 48 598 D0 J = ONE TO N 1 599 MLOCAL L1,L2,LERR =FFFF 3 601 L1 MIF 'TO'='TO' THEN L1 015F 200 MIF '='='=' THEN L1 ELSE LERR ;SYN: =FFFF 3 601 L1 MIF 'TO'='TO' THEN L2 015C 205 13 603 LD HL,(ONE) ;GET VAR2 0162 ED5BEB00' 9 604 LD DE,(N) ;GET VAR3 0166 D2215A01' 10 605 LD IX,L0025 ;GET LOOP BACK LABEL 0166 AD226902' 11 606 L0026 LD UJ,HL ;SET VAR1 0166 D225A01' 10 605 LD IX,L0025 ;GET VAR2 0166 D226402' 11 606 L0026 LD UJ,HL ;SET VAR3 0166 D215A01' 10 605 LD IX,L0025 ;GET VAR3 0166 D226402' 11 606 L0026 LD (J),HL ;SET VAR3 0166 D25 13 608 PUSH HL ;PUSH VALUES ONTO STACK 016F DDE5 14 609 PUSH IX 0167 DDE5 14 609 PUSH X 0167 DDE5 14 609 PUSH X =FFFF 2 613 MIF '='='=' THEN L1 ELSE LERR ;SYNTAX CHE =FFFF 2 613 MIF '='='=' THEN L1 ELSE LERR ;SYNTAX CHE 0171 2A6702' 4 615 LD HL,(I) ;GET VAR2	=FFFF	8 577 9 578 L2 10 579	MIF '='='=' THEN L2 ELSE L3 ;CHECK OPER JP Z,LOOP ;IF EQUAL (TRUE), DO JUI MGOTO LCONT
31 584 MEND 014D 47 585 DO I = ONE TO N 1 586 MLOCAL L1,L2,LERR =FFFF 2 587 MIF '='='' THEN L1 ELSE LERR ;SYN =FFFF 3 588 L1 MIF 'TO'='TO' THEN L2 014D 2ADF00' 8 590 LD HL,(ONE) ;GET VAR2 0150 EDSBEB00' 9 591 LD DE,(N) ;GET VAR2 0150 EDSBEB00' 9 591 LD DE,(N) ;GET VAR2 0154 DD215801' 10 592 LD IX,L0025 ;GET LOOP BACK LABEL 0158 E5 12 594 PUSH HL ;PUSH VALUES ONTO STACK 0155 13 595 PUSH BE ;015D DDE5 14 599 015F 48 598 DO J = ONE TO N ;5YN' ;5YN' ;5YN' =FFFF 3 601 L1 MIF '='='=' THEN L1 ELSE LERR ;SYN' ;SYN' =FFFF 3 601 L1 MIF '='=' THEN L1 ELSE LERR ;SYN' ;SYN' =FFFF 3 601 L1 MIF 'IO'='TO' THEN L2			MIF ''='ELSE' THEN L7 ELSE L8 ;CHE
=FFFF 2 587 MIF '='='' THEN L1 ELSE LERR ;SYN =FFFF 3 588 L1 MIF 'TO'='TO' THEN L2 7 589 L2 MNOP 014D 2ADF00' 8 590 LD HL,(ONE) ;GET VAR2 0150 ED5BEB00' 9 591 LD DE,(N) ;GET VAR3 0154 DD215801' 10 592 LD IX,L0025 ;GET LOOP BACK LABEL 0158 226702' 11 593 L0025 LD (1),HL ;SET VAR1 0158 226702' 13 595 PUSH HL ;PUSH VALUES ONTO STACK 0150 DDE5 14 596 PUSH IX 0151 DDE5 14 597 MEND 0151 DDE5 14 597 MEND 0151 DDE5 14 597 MEND 0152 ADF00' 8 601 L1 MIF '='='=' THEN L1 ELSE LERR ;SYN' =FFFF 3 601 L1 MIF 'TO'='TO' THEN L2 SYN' 0152 ADF00' 8 603 LD HL,(ONE) ;GET VAR2 0155 2ADF00' 8 603 LD HL,(ONE) ;GET VAR3 0166 DD216A01' 10 605 LD <td>014D</td> <td>31 584 47 585</td> <td>MEND DO I = ONE TO N</td>	014D	31 584 47 585	MEND DO I = ONE TO N
0150 ED5BEB00' 9 591 LD DE,(N) ;GET VAR3 0154 DD215801' 10 592 LD IX,L0025 ;GET LOOP BACK LABEL 0158 '226702' 11 593 L0025 LD (I),HL ;SET VAR1 0155 E5 12 594 PUSH HL ;PUSH VALUES ONTO STACK 0150 DDE5 14 596 PUSH HL ;PUSH VALUES ONTO STACK 0157 DDE5 14 596 PUSH JX 0158 '226702' 11 599 MLOCAL L1,L2,LERR 0150 DDE5 14 596 DO J = ONE TO N 0157 '48 598 DO J = ONE TO N 1 599 MLOCAL L1,L2,LERR =FFFF '2 600 MIF '='='=' THEN L1 ELSE LERR ;SYN' =FFFF '3 601 L1 MIF 'TO' TO' THEN L2 0157 2ADF00' 8 603 LD HL,(ONE) ;GET VAR2 0162 ED5BEB00' 9 604 LD DE,(N) ;GET VAR3 0164 DD216A01' 10 605 LD IX,L0026 ;GET LOOP BACK LABEL 0166 DD216A01' 10 605 LD IX,L0026 ;GET VAR1 0166 DD216A01' 10 605 LD IX,L0026 ;GET VAR1 0166 DD216A01' 10 605 LD IX,L0026 ;GET VAR1 0166 DD5 13 608 PUSH HL ;PUSH VALUES ONTO STACK 0167 DDE5 14 609 PUSH	=FFFF	2 587 3 588 L1 7 589 L2	MIF '='='' THEN L1 ELSE LERR ;SYN MIF 'TO'='TO' THEN L2 MNOP
015C D5 13 595 PUSH DE 015D DDE5 14 596 PUSH IX 15 597 MEND 015F 48 598 DO J = ONE TO N 15 597 MEND 015F 48 598 DO J = ONE TO N 1 599 MLOCAL L1,L2,LERR =FFFF 2 600 MIF '='='' THEN L1 ELSE LERR ;SYN' =FFFF 3 601 L1 MIF 'TO'='TO' THEN L2 7 602 L2 MNOP 015F 2ADF00' 8 603 LD 0162 ED5BEB00' 9 604 LD DE,(N) 0164 D226902' 11 606 L0026 LD IX,L0026 0166 D216A01' 10 605 LD IX,L0026 ;GET VAR3 0166 D5 12 607 PUSH HL ;PUSH VALUES ONTO STACK 016E D5 13 608 PUSH IX ;PUSH VALUES ONTO STACK 016F DDE5 14 609 PUSH IX ; 0171 49 611 LET ADD = I + J 1 <	0150 ED5BEB00' 0154 DD215801'	9 591 10 592	LD DE,(N) ;GET VAR3 LD IX,L0025 ;GET LOOP BACK LABEL
1 599 MLOCAL L1,L2,LERR =FFFF 2 600 MIF '='='=' THEN L1 ELSE LERR ;SYN' =FFFF 3 601 L1 MIF 'TO'='TO' THEN L2 7 602 L2 MNOP 015F 2ADF00' 8 603 LD HL,(ONE) ;GET VAR2 0162 ED5BEB00' 9 604 LD DE,(N) ;GET VAR3 0166 DD216A01' 10 605 LD IX,L0026 ;GET LOOP BACK LABEL 016A '226902' 11 606 L0026 LD (J),HL ;SET VAR1 016D E5 12 607 PUSH HL ;PUSH VALUES ONTO STACK 016E D5 13 608 PUSH DE 016F DDE5 14 609 PUSH IX 15 610 MEND 0171 49 611 LET ADD = I + J =FFFF 2 613 MIF '='='='' THEN L1 ELSE LERR ;SYNTAX CHE 3 614 L1 MNOP 0171 2A6702' 4 615 LD HL,(I) ;GET VAR2	015C D5	13 595 14 596	PUSH DE PUSH IX
7 602 L2 MNOP 015F 2ADF00' 8 603 LD HL,(ONE) ;GET VAR2 0162 ED5BEB00' 9 604 LD DE,(N) ;GET VAR3 0166 DD216A01' 10 605 LD IX,L0026 ;GET LOOP BACK LABEL 016A '226902' 11 606 L0026 LD (J),HL ;SET VAR1 016D E5 12 607 PUSH HL ;PUSH VALUES ONTO STACK 016F D5 13 608 PUSH DE 016F DDE5 14 609 PUSH IX 15 610 MEND 0171 49 611 LET ADD = I + J 1 612 MLOCAL L1,L2,L3,L4,L5,LS,LERR =FFFF 2 613 MIF '='='=' THEN L1 ELSE LERR ;SYNTAX CHE 3 614 L1 MNOP ;GET VAR2	= F F F F	1 599 2 600	MLOCAL L1,L2,LERR MIF '='='' THEN L1 ELSE LERR ;SYN:
016A'226902' 11 606 L0026 LD (J),HL ;SET VAR1 016D E5 12 607 PUSH HL ;PUSH VALUES ONTO STACK 016E D5 13 608 PUSH DE 016F DDE5 14 609 PUSH IX 15 610 MEND 0171 49 611 LET ADD = I + J 1 612 MLOCAL L1,L2,L3,L4,L5,LS,LERR =FFFF 2 613 MIF '='='=' THEN L1 ELSE LERR ;SYNTAX CHE 3 614 L1 MNOP 0171 2A6702' 4 615 LD HL,(I) ;GET VAR2	015F 2ADF00' 0162 ED5BEB00'	7 602 L2 8 603 9 604	MNOP LD HL,(ONE) ;GET VAR2 LD DE,(N) ;GET VAR3
0171 49 611 LET ADD = I + J 1 612 MLOCAL L1,L2,L3,L4,L5,LS,LERR =FFFF 2 613 MIF '='='=' THEN L1 ELSE LERR ;SYNTAX CHE 3 614 L1 MNOP 0171 2A6702' 4 615 LD HL,(I) ;GET VAR2	016A'226902' 016D E5 016E D5	11 606 L00 12 607 13 608 14 609	026 LD (J),HL ;SET VAR1 PUSH HL ;PUSH VALUES ONTO STACK PUSH DE
0171 2A6702' 4 615 LD HL,(I) ;GET VAR2		49 611 1 612 2 613	LET ADD = I + J MLOCAL L1,L2,L3,L4,L5,LS,LERR MIF '='='=' THEN L1 ELSE LERR ;SYNTAX CHE
MENT	=0000	4 615 5 616	LD HL,(I) ;GET VAR2 MIF '+'='' THEN LS ;IF NO OPERATOR, DO AS MENT
0174 ED5B6902' 6 617 LD DE,(J) ;GET VAR3 =FFFF 7 618 MIF '+'='+' THEN L2 ;CHECK OPERATOR 14 619 L2 MNOP 0178 19 15 620	= F F F F	7 618 14 619 L2	MIF '+'='+' THEN L2 ;CHECK OPERATOR MNOP
16 621 MGOTO LS 57 622 LS MNOP 0179'226B02' 58 623 Z0027 LD (ADD),HL ;SAVE IN VAR1 59 624 MEND		16 621 57 622 LS 58 623 ZOC	MGOTO LS MNOP 027 LD (ADD),HL ;SAVE IN VAR1

MOSTEK MACRO-80 ASSEMBLER V2.0 PAGE IRE 4-10. 8 OBJ.CODE STMT-NR SOURCE-STMT PASS2 FIG410 FIG410 FIG410 REL 2 50 LET SUB = I - J625 626 MLOCAL L1, L2, L3, L4, L5, LS, LERR 1 2 '='='=' THEN L1 ELSE LERR ;SYNTAX CHECK =FFFF627 MIF 3 628 L1 MNOP C 2A6702' ;GET VAR2 Ц 629 LD $HL_{(I)}$ '-'='' THEN LS ; IF NO OPERATOR, DO ASSIGN 5 630 MIF =0000 MENT ;GET VAR3 F ED5B6902' 6 631 LD DE_{J} '-'='+' THEN L2 ;CHECK OPERATOR =0000 7 632 MIF '-'='-' THEN L3 =FFFF8 633 MIF 17 634 L3 MNOP 3 B7 18 635 OR A HL,DE 4 ED52 19 636 SBC 20 637 MGOTO LS 57 MNOP 638 LS 6'226D02' 58 639 Z0028 LD (SUB),HL ;SAVE IN VAR1 59 640 MEND LET MUL = I + J9 51 641 642 1 MLOCAL L1, L2, L3, L4, L5, LS, LERR 2 643 MIF '='='=' THEN L1 ELSE LERR ;SYNTAX CHECK =FFFF 644 L1 MNOP 3 9 2A6702' 4 645 LD ;GET VAR2 $HL_{(I)}$ 5 '*'='' THEN LS ; IF NO OPERATOR, DO ASSIGN MIF =0000 646 MENT C ED5B6902' 6 647 LD DE_{J} ;GET VAR3 =0000 7 648 MIF '*'='+' THEN L2 ;CHECK OPERATOR '*'='-' THEN L3 =0000 8 649 MIF '*'='*' THEN L4 9 =FFFF650 MIF 651 L4 MNOP ; MULTIPLY BY SEVERAL ADDITION 21 S }O 7A 22 652 LD ;CHECK FOR MULT BY ZERO A,D 91 B3 E 23 653 OR NZ, I0029 € 2006 24 654 JR 25 655 LD HL,O ; IF SO, ZERO RESULT 34 210000 97 C3A901' 26 656 JP K0029 **JA'1**B 27 657 I0029 DEC DE ;CHECK FOR MULT BY ONE 9B 7A 28 658 LD A,D 9C B3 29 Е 659 OR 9D 280A 30 660 JR Z,K0029 ;YES, JUST PUT IN VALUE 9F ED4B6702' ;GET VAR2 31 661 LD $BC_{(I)}$ A3'09 32 662 L0029 ADD HL,BC 33 A4 1B 663 DEC DE A5 7A 34 664 LD A,D ;CHECK FOR END A6 B3 35 665 OR Ε JR NZ,L0029 A7 20FA 36 666 =01A9'37 667 K0029 38 668 MGOTO LS 57 669 LS MNOP 58 A9'226F02' 670 Z0029 LD ;SAVE IN VAR1 (MUL),HL 59 671 MEND AC 52 672 LET DIV = I / J 1 673 MLOCAL L1, L2, L3, L4, L5, LS, LERR 2 '='='=' THEN L1 ELSE LERR ;SYNTAX CHECK =FFFF674 MIF MNOP 3 675 L1 ;GET VAR2 AC 2A6702' 4 676 LD $HL_{(I)}$ '/'='' THEN LS ; IF NO OPERATOR, DO ASSIG =0000 5 677 MIF MENT

01AF ED5B6902' 6 678 LD DE.(J) ;GET VAR3 =0000 7 679 MIF '/'='' THEN L2;CHECK OPERATOR =0000 9 680 MIF '/'='' THEN L4;CHECK OPERATOR =0000 9 681 MIF '/'='' THEN L4;CHECK OPERATOR =0000 9 681 MIF '/'='' THEN L4;CHECK OPERATOR =0000 9 681 MIF '/'='' THEN L4;CHECK OPERATOR =0183 683 L5 MNOP CHECK FOR DIVIDE BY ZE 01B5 44 684 LD A,D ;CHECK FOR DIVIDE BY ZE 01B5 1016 687 GEDADATOR FEROR' 01B5 101 2 689 LD E_CHNI+1 ;CHENK PEROR' 01B5 101 2 689 JR CO031 01B6 CD101' 4 691 CALL PXT OVERFLOW ERROR',ODH,OAH,3H 4520200A - 70 GR 564 JR Z0030 ID 01D6 180C 696 JR Z0030 ID ERCATU PROF',ODH,OAH,3H	FIGURE 4-10. LOC OBJ.CODE	STMT-NR			ASSEMBLER V2.0 PAGE FIG410 FIG410 REL
0187 1E01 2 689 LD E,CHNL+1 ;CHANNEL NER 0182 CDF300' 4 691 CALL PTXT 0185 CDF300' 4 691 CALL PTXT 0187 12031 DEC DEC DEC 0161':2A2A2A2 6 693 MS0031 DEFM '*** OVERFLOW ERROR', ODH, OAH, 3H 4F564552 464C4F57 20455252 4F520D0A 03 03 =0106' 7 694 L0031 ERROR', ODH, OAH, 3H 03 =0106' 7 694 L0031 ERROR', ODH, OAH, 3H 03 =0106' 7 694 L0031 ERSULT 0108 010000 49 697 C0030 LD EC,0 ; RESULT 0108 010000 49 697 C0030 LD EC,0 ; RESULT 0108 0107 00 RA ; RESULT NUNTIL DENE 01010 37 705 INC NL ERROR', CORRECT THE RESULT 0111 JER NC, D0030 ; LOP UNTIL NEGATIVE <td>=0000 =0000 =0000 =FFFF 01B3 7A 01B4 B3 01B5 2021</td> <td>7 679 8 680 9 681 10 682 43 683 44 684 45 685 46 686 47 687</td> <td>M M M L5 L5 L S L S L S P P</td> <td>MIF '/'='+' THEN MIF '/'='-' THEN MIF '/'='*' THEN MIF '/'='/' THEN MNOP LD A,D DR E JR NZ,C0030 PRINT '*** OVERFI</td> <td>N L2 ;CHECK OPERATOR N L3 N L4 N L5 ;CHECK FOR DIVIDE BY ZE</td>	=0000 =0000 =0000 =FFFF 01B3 7A 01B4 B3 01B5 2021	7 679 8 680 9 681 10 682 43 683 44 684 45 685 46 686 47 687	M M M L5 L5 L S L S L S P P	MIF '/'='+' THEN MIF '/'='-' THEN MIF '/'='*' THEN MIF '/'='/' THEN MNOP LD A,D DR E JR NZ,C0030 PRINT '*** OVERFI	N L2 ;CHECK OPERATOR N L3 N L4 N L5 ;CHECK FOR DIVIDE BY ZE
8 695 MEND 01D6 180C 48 696 JR Z0030 01D8'10000 49 697 C0030 LD BC.0 ;RESULT 01D8'B7 50 698 D0030 OR A ;RESET CARRY 01DC ED52 51 699 SEC HL,DE ;SUBTRACT UNTIL DONE 01DF 30FA 53 701 JR NC,D0030 ;LOOP UNTIL NEGATIVE 01E1 08 54 702 DEC BC ;CORRECT THE RESULT 01E2 69 55 703 LD L,C ;PUT INTO HL 01E3 60 56 704 LD H,B 57 705 LS MNOP MINTE ADD,SUB,MUL,DIV 01E7 53 708 WRITE ADD,SUB,MUL,DIV 710 MLOCAL L1 ; 3 01E7 1E01 4 712 LD F.CHNL+1 01E7 1E01 714 LD HL,MSADD ;OUTPUT MESSAGE 01E7 1E01<	01B9 21C101' 01BC CDF300' 01BF 1815 01C1'2A2A2A20 4F564552 464C4F57 20455252 4F520D0A 03	2 689 3 690 4 691 5 692 6 693	I I C J	LD E,CHNL+1 LD HL,MSOO31 CALL PTXT JR LOO31	
01D6 180C 48 696 JR Z0030 01D8'010000 49 697 C0030 LD BC,0 ;RESUT 01D8'010000 49 697 C0030 LD BC,0 ;RESET CARRY 01DC ED52 51 699 SEC HL,DE ;SUBTRACT UNTIL DONE 01DF 30FA 53 700 INC BC ;CORRECT THE RESULT 01E1 08 54 702 DEC BC ;CORRECT THE RESULT 01E2 69 55 703 LD L,C ;PUT INTO HL 01E3 60 56 704 LD H,B 57 705 LS MNOP 01E4'227102' 58 706 Z0030 LD (DIV),HL ;SAVE IN VAR1 59 707 MEND MART 2004000 (DIV) 01E7 53 708 WRITE ADD,SUB,MUL,DIV ; ADD FIRST TIME USAGE OF PARAMETER 01E7 1E01 4 712 LD H,MSADD ;OUTPUT MESSAGE 01E7 1E01 5 713 <l1< td=""> MNOP <t< td=""><td></td><td></td><td></td><td></td><td></td></t<></l1<>					
01E1 0B 54 702 DEC BC ;CORRECT THE RESULT 01E2 69 55 703 LD L,C ;FUT INTO HL 01E3 60 56 704 LD H,B 57 705 LS MNOP 01E4'227102' 58 706 Z0030 LD (DIV),HL ;SAVE IN VAR1 59 707 MEND 01E7 53 708 WRITE ADD,SUB,MUL,DIV ; ADD FIRST TIME USAGE OF PARAMETER 2 710 MLOCAL L1 3 711 GLOBAL PTXT,CRLF,PADDO 01E7 01E7 1E01 4 712 LD E,CHNL+1 ;OUTPUT CHANNEL 5 713 <l1< td=""> MNOP MNOP ;OUTPUT MESSAGE 01E7 01E9 21F701' 6 714 LD HL,MSADD ;OUTPUT MESSAGE 01E7 20B001' 7 715 CALL PTXT 01FF 10 718 JR IADD 01F7 41404420 3 721 LADD ;WRITE OUT IN HEX 3D20 01F7 10 718 JR LADD ;WRITE OUT IN HEX</l1<>	01D8'010000 01DB'B7 01DC ED52 01DE 03	48 696 49 697 50 698 51 699 52 700	J C0030 I D0030 O S I	JR Z0030 LD BC,0 DR A SBC HL,DE LNC BC	;RESET CARRY ;SUBTRACT UNTIL DONE
01E4'227102' 58 706 20030 LD (DIV),HL ;SAVE IN VAR1 59 707 MEND 01E7 53 708 WRITE ADD,SUB,MUL,DIV ; ADD FIRST TIME USAGE OF PARAMETER 2 710 MLOCAL L1 3 711 GLOBAL PTXT,CRLF,PADDO 01E7 1E01 4 712 1E9 21F701' 6 714 LD 01E9 21F701' 6 714 LD 01E9 21F701' 6 714 LD HL,MSADD 01E7 2A6B02' 8 716 LD HL,(ADD) 01E7 2A6B02' 8 716 LD HL,(ADD) 01F2 CDB400' 9 717 CALL PADD ;wRITE OUT IN HEX 01F5 1807 10 718 JR LADD 01F7 '41444420 11 719 MSADD DEFM 'ADD = ' 3D20 12 720 DEFB 3	01E1 OB 01E2 69	54 702 55 703 56 704	D I L	DEC BC LD L,C LD H,B	;CORRECT THE RESULT
01E7 53 708 WRITE ADD,SUB,MUL,DIV ; ADD FIRST TIME USAGE OF PARAMETER 2 710 MLOCAL L1 3 711 GLOBAL PTXT,CRLF,PADDO 01E7 1E01 4 712 LD E,CHNL+1 ;OUTPUT CHANNEL 5 713 L1 MNOP ;OUTPUT MESSAGE 01E9 21F701' 6 714 LD HL,MSADD ;OUTPUT MESSAGE 01E7 20B001' 7 715 CALL PTXT 01E7 20B001' 7 716 LD HL,MSADD ;OUTPUT MESSAGE 01E7 20B400' 9 717 CALL PTXT OUT IN HEX 01F2 20B400' 9 717 CALL PADDO ;WRITE OUT IN HEX 01F5 1807 10 718 JR LADD 01F7 '41444420 11 719 MSADD DEFM 'ADD = ' 3D20 12 720 DEFB 3	01E4'227102'	58 706	Z0030 L	LD (DIV),HL	;SAVE IN VAR1
2 710 MLOCAL L1 3 711 GLOBAL PTXT, CRLF, PADDO 01E7 1E01 4 712 LD E, CHNL+1 ;OUTPUT CHANNEL 5 713 L1 MNOP ;OUTPUT MESSAGE 01E9 21F701' 6 714 LD HL, MSADD ;OUTPUT MESSAGE 01EC CDBD01' 7 715 CALL PTXT 01EF 2A6B02' 8 716 LD HL, (ADD) 01F2 CDB400' 9 717 CALL PADDO ;WRITE OUT IN HEX 01F5 1807 10 718 JR LADD 01F5 1807 10 718 JR LADD 01F7 '41444420 11 719 MSADD DEFM 'ADD = ' 3D20 3D20 0 DEFB 3	01E7		W	WRITE ADD, SUB, MUI	
01E7 1E01 4 712 LD E,CHNL+1 ;OUTPUT CHANNEL 5 713 L1 MNOP ;OUTPUT CHANNEL 01E9 21F701' 6 714 LD HL,MSADD ;OUTPUT MESSAGE 01EC CDBD01' 7 715 CALL PTXT ;OUTPUT MESSAGE 01E7 2A6B02' 8 716 LD HL,(ADD) 01F2 CDB400' 9 717 CALL PADDO ;WRITE OUT IN HEX 01F5 1807 10 718 JR LADD 01F7 '41444420 11 719 MSADD DEFM 'ADD = ' 3D20 3D20		2 710			F PARAMETER
01EC CDBD01' 7 715 CALL PTXT 01EF 2A6B02' 8 716 LD HL,(ADD) 01F2 CDB400' 9 717 CALL PADDO ;WRITE OUT IN HEX 01F5 1807 10 718 JR LADD 01F7'41444420 11 719 MSADD DEFM 'ADD = ' 3D20	01E7 1E01	4 712	L	LD E, CHNL+1	
01F2 CDB400' 9 717 CALL PADDO ;WRITE OUT IN HEX 01F5 1807 10 718 JR LADD ;WRITE OUT IN HEX 01F7 '41444420 11 719 MSADD DEFM 'ADD = ' ;WRITE OUT IN HEX 01F7 '41444420 11 719 MSADD DEFM 'ADD = ' ;WRITE OUT IN HEX 01F0 03 12 720 DEFB 3 ;URITE OUT IN HEX 01FE 01FE' 13 721 LADD ;URITE OUT IN HEX 14 722 MNEXT 1 THEN L1 ;UTPUT MESSAGE 01FE 210C02' 6 724 LD HL,MSSUB ;OUTPUT MESSAGE 0201 CDED01' 7 725 CALL PTXT ;UD HL,(SUB)	01EC CDBD01'	7 715	С	CALL PTXT	;OUTPUT MESSAGE
01FD 03 12 720 DEFB 3 =01FE' 13 721 LADD 14 722 MNEXT 1 THEN L1 5 723 L1 MNOP 01FE 210C02' 6 724 LD HL,MSSUB 0201 CDED01' 7 725 CALL PTXT 0204 2A6D02' 8 726 LD HL,(SUB)	01F2 CDB400' 01F5 1807	9 717 10 718	C J	CALL PADDO JR LADD	;WRITE OUT IN HEX
14 722 MNEXT 1 THEN L1 5 723 L1 MNOP 01FE 210C02' 6 724 LD HL,MSSUB ;OUTPUT MESSAGE 0201 CDED01' 7 725 CALL PTXT 0204 2A6D02' 8 726 LD HL,(SUB)	01FD 03			DEFB 3	
01FE 210C02' 6 724 LD HL,MSSUB ;OUTPUT MESSAGE 0201 CDED01' 7 725 CALL PTXT ;OUTPUT MESSAGE 0204 2A6D02' 8 726 LD HL,(SUB)	=01FE'	14 722	М		
0207 CDF301' 9 727 CALL PADDO ;WRITE OUT IN HEX 020A 1807 10 728 JR LSUB	0201 CDED01' 0204 2A6D02' 0207 CDF301'	6 724 7 725 8 726 9 727	L C L C	LD HL,MSSUB CALL PTXT LD HL,(SUB) CALL PADDO	;OUTPUT MESSAGE ;WRITE OUT IN HEX

IGURE 4-10. DC OBJ.CODE	STMT-NR	SOURCE-		ASSEMBLER V2.0 PAGE 10 FIG410 FIG410 REL
20C'53554220 3D20	11 729	MSSUB	DEFM 'SUB = '	
212 03 =0213'	12 730 13 731 14 732	LSUB	DEFB 3 MNEXT 1 THEN L1	
213 212102' 216 CD0202'	6 734 7 735		MNOP LD HL,MSMUL CALL PTXT	;OUTPUT MESSAGE
219 2A6F02' 21C CD0802' 21F 1807 221'4D554C20	8 736 9 737 10 738 11 739		LD HL,(MUL) CALL PADDO JR LMUL DEFM 'MUL = '	;WRITE OUT IN HEX
3D20 227 03 =0228 '	12 740 13 741	LMUL	DEFB 3	
228 213602	6 744	L1	MNEXT 1 THEN L1 MNOP LD HL,MSDIV	;OUTPUT MESSAGE
)22B CD1702')22E 2A7102')231 CD1D02')234 1807	7 745 8 746 9 747 10 748		CALL PTXT LD HL,(DIV) CALL PADDO JR LDIV	;WRITE OUT IN HEX
)236'44495620 3D20)23C 03	11 74912 750	MSDIV	DEFM 'DIV = ' DEFB 3	
=023D')23D CD3E01'		LDIV	MNEXT 1 THEN L1 CALL CRLF	
0240 0240 DDE1 0242 D1 0243 E1 0244 23 0245 E5 0246 B7	16 754 54 755 1 756 2 757 3 758 4 759 5 760 6 761		MEND ENDDO POP IX POP DE POP HL INC HL PUSH HL OR A	;LOOP ADDRESS ;FINAL VALUE ;CURRENT VALUE ;INCREMENT VAR 1
0247 ED52 0249 E1 024A 2802 024C 3002 024E'DDE9 =0250'	7 762 8 763 9 764 10 765 11 766		SBC HL,DE POP HL JR Z,KK0033	;CHECK IT ;LAST TIME THRU ;IF DONE, SKIP OUT ;ELSE LOOP
0250 0250 DDE1 0252 D1 0253 E1 0254 23 0255 E5 0256 B7 0257 ED52 0259 E1 025A 2802 025C 3002 025E'DDE9 =0260'	$\begin{array}{cccccccccccccccccccccccccccccccccccc$		MEND ENDDO POP IX POP DE POP HL INC HL PUSH HL OR A SBC HL,DE POP HL JR Z,KK0034 JR NC,L0034 JP (IX)	;LOOP ADDRESS ;FINAL VALUE ;CURRENT VALUE ;INCREMENT VAR1 ;CHECK IT ;LAST TIME THRU ;IF DONE, SKIP OUT ;ELSE LOOP
	13 782		MEND	

FIGURE 4-10. LOC OBJ.CODE	STMT-NR	SOURCE-	MOSTEK MACRO-80 ASSEMBLER V2.0 PAGE 11 STMT PASS2 FIG410 FIG410 FIG410 REL
0260	56 783		EXIT
	1 784		GLOBAL JTASK
0260 3E01	2 785		LD A,1
0262 C3DD00'	3 786 4 787		JP JTASK MEND
	4 /8/		H E N D
0265	58 789	;	DCL ZERO
	1 790		MLOCAL L1,L2,L3
=0000	2 791		MIF ''='INIT' THEN L1 ELSE L2
		L2	MNOP
0265'0000		ZERO	DEFW O ;DEFAULT TO ZERO
	8 794		MEND
0267	59 795 1 796		DCL I
=0000	2 797		MLOCAL L1,L2,L3 MIF ''='INIT' THEN L1 ELSE L2
-0000	6 798		MNOP
0267'0000	7 799		DEFW O ;DEFAULT TO ZERO
	8 800		MEND
0269	60 801		DCL J
	1 802		MLOCAL L1,L2,L3
=0000	2 803		MIF ''='INIT' THEN L1 ELSE L2
00000000	6 804		
0269'0000	7 805 8 806		DEFW O ;DEFAULT TO ZERO MEND
026B	61 807		DCL ADD
0200	1 808		MLOCAL L1,L2,L3
=0000	2 809		MIF ''='INIT' THEN L1 ELSE L2
		L2	MNOP
026B'0000		ADD	DEFW O ;DEFAULT TO ZERO
	8 812		MEND
026D	62 813		DCL SUB
=0000	1 814 2 815		MLOCAL L1,L2,L3 MIF ''='INIT' THEN L1 ELSE L2
-0005		L2	MNOP
026D'0000		SUB	DEFW O ;DEFAULT TO ZERO
	8 818		MEND
026F	63 819		DCL MUL
	1 820		MLOCAL L1, L2, L3
=0000	2 821		MIF ''='INIT' THEN L1 ELSE L2
026510000		L2 MUL	
026F'0000	7 823 8 824		DEFW O ;DEFAULT TO ZERO MEND
0271	64 825		DCL DIV
	1 826		MLOCAL L1,L2,L3
=0000	2 827		MIF ''='INIT' THEN L1 ELSE L2
	6 828	L2	MNOP
0271'0000		DIV	DEFW O ;DEFAULT TO ZERO
	8 830		MEND
0273	65 831		END

FIGURE 4-11. SAMPLE RUNS

FIBONACCI SERIES:

ENTER 2 HEX DIGITS 07 **A** = 0001 B = 0002 C = 0003 C = 0005 C = 0008 C = 000D C = 0015 C = 0022 C = 0037

COMBINATIONS:

ENTH	ER	TWO H	IEX I	DIC	SITS						
04											
ADD	=	0002	SUB	=	0000	MUL	=	0001	DIV	=	0001
ADD	=	0003	SUB	=	FFFF	MUL	=	0002	DIV	=	0000
ADD	=	0004	SUB	=	FFFE	MUL	=	0003	DIV	=	0000
ADD	Ξ	0005	SUB	=	FFFD	MUL	=	0004	DIV	=	0000
ADD	Ξ	0003	SUB	Ξ	0001	MUL	=	0002	DIV	=	0002
ADD	Ξ	0004	SUB	=	0000	MUL	=	0004	DIV	Ξ	0001
ADD	Ξ	0005	SUB	=	FFFF	MUL	Ξ	0006	DIV	=	0000
ADD	Ξ	0006	SUB	=	FFFE	MUL	=	0008	DIV	Ξ	0000
ADD	=	0004	SUB	=	0002	MUL	=	0003	DIV	=	0003
ADD	=	0005	SUB	Ξ	0001	MUL	=	0006	DIV	Ξ	0001
ADD	Ξ	0006	SUB	=	0000	MUL	=	0009	DIV	Ξ	0001
ADD	=	0007	SUB	=	FFFF	MUL	=	000C	DIV	=	0000
ADD	=	0005	SUB	=	0003	MUL	=	0004	DIV	Ξ	0004
ADD	Ξ	0006	SUB	=	0002	MUL	=	8000	DIV	Ξ	0002
ADD	Ξ	0007	SUB	=	0001	MUL	=	000C	DIV	=	0001
ADD	=	8000	SUB	=	0000	MUL	=	0010	DIV	Ξ	0001

•	IGURE 4-12.
NLIST	
; THESE DEFINITION ; IOCS-BASED PROGR ; THE USER, BUT BE ; AND ROUTINES INC	NS ARE FOR THE CONVENIENCE OF THE USER WRITING RAMS. THESE DEFINITIONS MAY BE CHANGED TO SUIT EWARE OF POSSIBLE CONFLICT WITH SYSTEM PROGRAMS CLUDING THIS FILE. THE USER MAY ALSO ADD ADDITIONAL PECIALLY IN THE ERROR CODE SECTION (ERRC)
; THIS FILE IS GEN ; INCLUDE IO	NERALLY USED AS AN INCLUDED FILE: DDEF
; I/O SYSTEM DEFIN	NITIONS
; VECTOR DISPLACEM	1ENTS
LUNITEQUODVCEEQU1UNITEQU2FNAMEQU4FEXTEQU10VERSEQU13USEREQU14RQSTEQU14RQSTEQU15FMATEQU16;HADDREQU17ERRAEQU19CFLGSEQU21SFLGSEQU23;PBFFREQU24UBFFREQU25USIZEEQU27;NRECEQU29	; DEFM 1 BYTE ; DEFM 6 BYTE ; DEFM 3 BYTE 3 ; DEFB 1 BYTE 4 ; DEFB 1 BYTE 5 ; DEFB 1 BYTE 5 ; DEFB 1 BYTE 6 ; DEFW 2 BYTE 7 ; DEFW 2 BYTE 1 ; DEFB 1 BYTE 2 ; DEFB 1 BYTE 3 ; DEFB 1 BYTE 3 ; DEFB 1 BYTE 4 ; DEFB 1 BYTE 5 ; DEFW 2 BYTE 5 ; DEFW 2 BYTE 6 ; DEFW 2 BYTE 7 ; DEFW 2 BYTE 7 ; DEFW 2 BYTE 9 ; DEFB 1 BYTE 9 ; DEFB 1 BYTE 9 ; DEFS 10 BYTE
; REQUEST CODES	
OPRRQEQUOOPWRQEQU1CLRQEQU2RDRQEQU3WRRQEQU4RWRQEQU5INRQEQU6ERRQEQU7;	;OPEN READ ;OPEN WRITE ;CLOSE ;READ ;WRITE ;REWIND ;INITIALIZE ;ERASE
; FORMAT CODES	
; BYTE EQU 00 LINE EQU 10 LBUF EQU 20 BIN EQU 30 ;	CH ;ASCII LINE I/O, TERMINATED BY CR/LF CH ;LOGICAL BUFFER, LENGTH IN USIZE
; ; CFLGS CODES ;	

OUNT CHO RET DRW RRPR PAR	EQU EQU EQU EQU EQU	1 2 4 8 16 32	;MOUNT/DISMOUNT ;AUTO ECHO FOR CONSOLE DEVICES ;IMMEDIATE RETURN REQUESTED ;READ AFTER WRITE ;ERROR PRINT ;STRIP PARITY
SFLGS	CODES		
	EQU EQU EQU EQU	1 2 4 8	;UNIT OPEN ;UNIT OPEN FOR WRITE ;UNIT ON ;END OF FILE DETECTED
;	CODES FO	OR ERRC	
FNF IOTIME NOPEN EOFERR ;	EQU EQU	1 2 4 7 8 9	;INVALID OPERATION ;DUPLICATE FILE ;FILE NOT FOUND ;IO TIME OUT ;FILE NOT OPEN ;ATTEMPT TO READ PAST END OF FILE
	SPECIAL	CHARACTE	ERS
; ETX EOT BEL HT LF FF CR DEL ; ;	EQU EQU EQU EQU EQU EQU EQU	03H 04H 07H 09H 0AH 0CH 0CH 7FH	
	LIST		

FIGURE 4-13. NLIST ********** IOMAC MACRO DEFINITIONS FOR I/O FUNCTIONS ************ ,#EXT=' #LUN,#DEV='DKO',#NAME=' •,#FMAT,#CFLGS,#UB VECTOR MACRO L1,L2,L3,L4,L5,L6,L7,L8,L9,L10,L11,L12 MLOCAL DEFB #LUN '#DEV' DEFM DEFM '#NAME' '#EXT' DEFM DEFB 0,0,0 '#FMAT'='' THEN L1 ELSE L2 MIF BYTE+4L1 DEFB MGOTO L3 #FMAT L2 DEFB L3 DEFW 0,0 '#CFLGS'='' THEN L4 ELSE L5 MIF L4 DEFB 0 MGOTO L6 L5#CFLGS DEFB L6 DEFB 0,0,0 '#UBFFR'='' THEN L7 ELSE L8 MIF L7 DEFW 0 MGOTO L9 L8 DEFW #UBFFR L9 '#USIZE'='' THEN L10 ELSE L11 MIF L10 DEFW 0 MGOTO L12 L11 DEFW #USIZE L12 DEFB 0 DEFW 0,0,0,0,0,0,0,0,0 MEND ; ; OPENR MACRO #VECTOR, #ERR, #ERRPR GLOBAL JIOCS, JTASK MLOCAL L1,L2,L3,L4,L5,L6,L7 '#VECTOR'='' THEN L6 ELSE L7 MIF L7 LD IY, #VECTOR L6 LD (IY+RQST), OPRRQ MIF '#ERRPR'='' THEN L3 ELSE L4 L3 LD (IY+CFLGS),0 MGOTO L5 L4 (IY+CFLGS), #ERRPR LD L5 CALL JIOCS LD A, (IY+ERRC) AND A '#ERR'='' THEN L1 ELSE L2 MIF L2 JP NZ, #ERR L1 LD A, 1 JP NZ, JTASK MEND ; ; OPENW MACRO #VECTOR, #ERR, #ERRPR GLOBAL JIOCS, JTASK MLOCAL L1,L2,L3,L4,L5,L6,L7

7 5 3 4 5 2	MIF LD MIF LD MGOTO LD CALL LD AND MIF JP MEXIT LD JP MEND	<pre>'#VECTOR'='' THEN L6 ELSE L7 IY,#VECTOR (IY+RQST),OPWRQ '#ERRPR'='' THEN L3 ELSE L4 (IY+CFLGS),0 L5 (IY+CFLGS),#ERRPR JIOCS A,(IY+ERRC) A '#ERR'='' THEN L1 ELSE L2 NZ,#ERR A,1 NZ,JTASK</pre>
CLOSE	MACRO MLOCAL MIF	
L9 L8 L7	LD MIF LD LD LD CALL	IY,#VECTOR '#EOT'='' THEN L6 ELSE L7 (IY+RQST),WRRQ (IY+FMAT),BYTE A,EOT JIOCS
L6	LD	(IY+RQST),CLRQ
L3	MIF LD	'#ERRPR'='' THEN L3 ELSE L4 (IY+CFLGS),0
L4 L5	MGOTO LD CALL LD AND MIF	L5 (IY+CFLGS),#ERRPR JIOCS A,(IY+ERRC) A '#ERR'='' THEN L1 ELSE L2
L2 L1 ;	JP LD JP MEND	NZ,#ERR A,1 NZ,JTASK
;	MACRO	
PARSE	GLOBAL MLOCAL LD LD CALL	#VECTOR, #ERR JTASK, PTXT L1, L2, L3 IY, #VECTOR A,6 ;CSIPAR JTASK ;CALL VIA TASK '#ERR'='' THEN L1 ELSE L2
L1	MNOP JR LD LD CALL LD	Z,I%NEXP ; IF NO ERRORS, SKIP HL,MS%NEXP ; GET SYNTAX ERROR MESSAGE E,1 ; PRINT ON LUN 1 PTXT A,1 ; RETURN TO MONITOR
MS%NEXP	JP DEFM	JTASK 'SYNTAX ERROR'
I%NEXP	MGOTO	L3

L2 L3	JP MNOP	NZ,#ERR
L2	LD	A,(IY+DVCE)
	LD LD	NZ,L%NEXP (IY+DVCE),'D' (IY+DVCE+1),'K'
L%NEXP	EQU MEND	\$
;;		
READ	MLOCAL	<pre>#VECTOR,#ERR,#ERRPR ;READ BYTE AT A TIME L1,L2,L3,L4,L5,L6,L7 '#VECTOR'='' THEN L7 IY,#VECTOR</pre>
L7	LD MIF	(IY+RQST),RDRQ ;READ REQUEST '#ERRPR'='' THEN L3 ELSE L4
L3	LD MG OTO	(IY+CFLGS),0 L5
L4 L5	LD CALL	(IY+CFLGS), #ERRPR
C.T	LD LD AND	D,A ;SAVE CHARACTER FOR BYTE MODE A,(IY+ERRC) ;CHECK FOR ERROR A
10	MIF	'#ERR'='' THEN L1 ELSE L2
L2 L1	LD	NZ,#ERR ;RETURN VIA ERROR EXIT A,1
	JP LD MEND	NZ,JTASK ;RETURN TO MONITOR A,D ;RESTORE BYTE FOR BYTE I/O
;		
; WRITE	MLOCAL MIF	<pre>#VECTOR,#ERR,#ERRPR ;WRITE L1,L2,L3,L4,L5,L6,L7 '#VECTOR'='' THEN L7 IN #VECTOR'='' THEN L7</pre>
L 7	LD LD	IY,#VECTOR (IY+RQST),WRRQ ;WRITE REQUEST
L3	MIF LD	'#ERRPR'='' THEN L3 ELSE L4 (IY+CFLGS),0
	MGOTO	L5
L4 L5	LD CALL	(IY+CFLGS),#ERRPR JIOCS
	LD AND	A,(IY+ERRC) ;CHECK FOR ERROR A
	MIF	'#ERR'='' THEN L1 ELSE L2
L2 L1		NZ, #ERR ;RETURN VIA ERROR EXIT
ן ע <u>ו</u>	LD JP MEND	A,1 NZ,JTASK ;RETURN TO MONITOR
;		
;	LIST	

/ . .

approach to creating and calling IOCS related functions. Each is described below.

4-49. VECTOR lun, device, filename, file extension, format, cflgs, ubffr, usize

This macro creates an IOCS parameter vector with several default parameters supplied. Use of this macro eliminates the need to write out a complete parameter vector definition using DEFB, DEFW, and DEFM pseudo-ops in the program. The user calls the macro and specifies the logical unit number (LUN), device mnemonic and unit number (DEV), file name (NAME), and file extension (EXT). Optionally, the user may specify the format (FMAT), control flags (CFLGS), user buffer address (UBFFR), and user buffer size (USIZE). The following defaults are applied:

> LUN = OFFH DEV = DK1: NAME = blanks EXT = blanks FMAT = 0 (byte I/0) CFLGS = 0 UBFFR = 0 USIZE = 0

All of the required bytes for the parameter vector are allocated when the macro is expanded.

4-50. OPENR vector name, error abort address, error print flag

This macro performs an open-for-read request via the vector specified in the first parameter. If the vector is not specified, then it is assumed that the IY register is pointing to the proper vector. If any errors were encountered, then exit is made via the error-abort address (second parameter), which is optional. If the error-exit address is not specified, then the macro returns control to the Monitor in case of an error. The third parameter, error-print flag, defaults to zero but can be set to 16H to force error printing via IOCS (this is the CFLGS parameter).

4-51. OPENW vector name, error-abort address, error-print flag

This macro performs an open for write request via the vector specified in the first parameter. All other operations are identical to OPENR.

4-52. CLOSE vector name, error abort address, error print flag

This macro performs a close function via the vector specified in the first parameter. All other operation is identical to OPENR.

4-53. PARSE vector name, error abort address

This macro provides a call to CSISYN and CSIPAR via the system routine

JTASK. Entry is with the HL register pair pointing to the dataset specification to be checked and parsed. The validity of the dataset specification is first checked, then it is parsed into the vector specified by the first parameter of the call to the macro. If any errors are found, then return is made via the second parameter. If this parameter is not given, then a message is printed (SYNTAX ERROR) and control is returned to the Monitor. If no errors are found and the device type is not given, then the device is defaulted to DKO.

4-54. EXIT

This macro returns control to the Monitor.

4-55. Figure 4-14 shows a typical program written using these macros. This program reads a dataset and prints it on the console output device (TT:). The dataset is specified in the Monitor command line which calls up this program. Upon entry to the program, the DE register pair points to the dataset specification. After initializing the stack pointer and interrupt mode, the dataset specification pointer is placed into the HL register pair. The dataset is parsed into INPUT, the input vector. The dataset is then opened. The output dataset is opened for write. This dataset is specified in the vector OUTPUT, which appears later in the program. Then a series of read/write operations are performed in byte I/O mode. The end of the data is specified by an ASCII 04H (end-of-file). When this character is read, the input dataset is closed and the program is terminated. (Closing the output dataset, the console device, is not necessary here).

ſ	RE 4-14. OBJ.CODE	STM	T – N R	MOSTEK MACRO-80 ASSEMBLER V2.0 PAGE 1 SOURCE-STMT PASS2 FIG414 FIG414 FIG414 REL							
			1		TITLE FIGURE 4-14.						
				-	; APPLICATION OF I/O MACROS						
				; AND C ; TO EX	; THIS PROGRAM READS A DATASET IN BYTE I/O ; AND COPIES IT TO THE CONSOLE DEVICE (TT:). ; TO EXECUTE THE PROGRAM:						
				; \$V ; -	SVIEW DATASET(CR)						
				; ; INCLU	DE IO	CS DEFINITIO	NS				
0			14								
		100	114	; F	IGURE 4-12. LIST						
				; INCLU	DE I/	O MACROS					
10		18	118		INCLUDE IOMAC IGURE 4-13.						
		172	290		LIST	4 130					
		21	293	;	CLIS	ГО	;CODE LISTING ONLY				
				; ; START	OF P	ROGRAM					
03 05		27 28	299	,	LD IM EI EX		;SET STACK POINTER ;INTERRUPT MODE FOR Z80 ;ENABLE INTERRUPTS ;HL POINTS TO DATASET SPEC				
	FD212101'	31	303 306	; PARSE	PARSI LD	DATASET INTO E INPUT IY,INPUT	THE INPUT VECTOR				
0 D	3E06 CDFFFF	45	307 308			A,6 JTASK	CSIPAR CALL VIA TASK				
12	2819 211F00'	8 9	311 312			Z,I0001 HL,MS0001	;IF NO ERRORS, SKIP ;GET SYNTAX ERROR MESSAGE				
17	1E01 CDFFFF	10 11	313 314			E,1 PTXT	; PRINT ON LUN 1				
)1C	3E01 C30E00' '53594E54	12 13 14	315 316 317	MS0001	LD JP DEFM	A,1 JTASK 'SYNTAX ERRO	;RETURN TO MONITOR				
	41582045 52524F52										
	FD7E01 FE20	19 20	321 322		LD CP	A,(IY+DVCE)					
	2008 FD360144	21 22	323 324		JR	NZ,LOOO1 (IY+DVCE),'I					
	FD36024B	23	325	; OPEN (LD .	(IY+DVCE+1)					
0 3 A		33	329		OPENH	R INPUT,,ERRE	M. PR				
	FD212101' FD360F00	4 5	333 334		LD LD	IY, INPUT (IY+RQST), OF	PRRQ				

FIGURE 4-14. MOSTEK MACRO-80 ASSEMBLER V2.0 PAGE 2 STMT-NR SOURCE-STMT PASS2 FIG414 FIG414 FIG414 REL LOC OBJ.CODE 0042 FD361510 9 336 L4 LD (IY+CFLGS), ERRPR 10 337 L5 CALL JIOCS 0046 CDFFFF 0049 FD7E17 11 338 LD $A_{\star}(IY+ERRC)$ 004C A7 12 339 AND Α 004D 3E01 15 341 L1 LD A.1 004F C21D00' 16 342 JP NZ, JTASK ; OPEN CONSOLE OUTPUT DRIVER. IGNORE ANY ERRORS OPENW OUTPUT, CONTINUE 0052 35 345 0052 FD215101' 4 349 L7 LD IY, OUTPUT 0056 FD360F01 5 350 L6 LD (IY+RQST), OPWRQ 7 352 L3 LD 005A FD361500 (IY+CFLGS),0 005E CD4700' 10 354 L5 CALL JIOCS 0061 FD7E17 11 355 LD A, (IY+ERRC) 356 0064 A7 12 AND Α 0065 C26800' 14 358 L2 JP NZ, CONTINUE =0068' 36 360 CONTINUE ; ; READ BYTES FROM INPUT DATASET. ABORT IF ERRORS 39 =0068' 363 LOOP 0068 40 364 READ INPUT, ERRPR 0068 FD212101' 367 IY, INPUT 3 LD 006C FD360F03 4 368 L7 LD (IY+RQST), RDRQ ; READ REQUEST 370 L4 LD (IY+CFLGS), ERRPR 0070 FD361510 8 371 L5 CALL JIDCS 0074 CD5F00' 9 0077 57 10 372 LD D,A ;SAVE CHARACTER FOR BYTE 0078 FD7E17 11 373 LD A,(IY+ERRC) ;CHECK FOR ERROR 007B A7 12 374 Α AND 007C 3E01 15 376 L1 LD A,1 007E C25000' 16 JP NZ, JTASK ;RETURN TO MONITOR 377 17 378 0081 7A LD A,D ;RESTORE BYTE FOR BYTE I, ; CHECK FOR END OF FILE BYTE 0082 FE04 42 381 CP 04H 0084 281A 43 JR Z, DONE ; IF SO, DONE 382 ; WRITE BYTE TO THE CONSOLE DEVICE 45 0086 384 WRITE OUTPUT 0086 FD215101' 3 387 LD IY, OUTPUT 008A FD360F04 4 388 L7 LD (IY+RQST), WRRQ ; WRITE REQUEST 008E FD361500 6 390 L3 LD (IY+CFLGS),0. 0092 CD7500' 9 392 L5 CALL JIOCS 0095 FD7E17 10 393 LD A, (IY+ERRC) ; CHECK FOR ERROR 394 0098 A7 11 AND Α 0099 3E01 14 396 L1 LD A,1 009B C27F00' 15 397 JP NZ, JTASK ;RETURN TO MONITOR JR 009E 18C8 46 399 LOOP ;LOOP FOR MORE BYTES ; ; END OF FILE FOUND, CLOSE THE INPUT DATASET 403 DONE CLOSE INPUT 00A0' 50 406 L9 00A0 FD212101' 3 LD IY, INPUT 9 408 L6 LD 00A4 FD360F02 (IY+RQST),CLRQ 00A8 FD361500 11 410 L3 LD (IY+CFLGS), 000AC CD9300' 14 412 L5 CALL JIOCS OOAF FD7E17 15 413 LD A, (IY+ERRC) 00B2 A7 16 414 AND A 00B3 3E01 19 416 L1 LD A, 1 00B5 C29C00' 20 417 JP NZ, JTASK 00B8 3E01 51 419 LD A,1

; U	;URE 4-14. : OBJ.CODE		C – N R	MOSTEK MACRO-80 ASSEMBLER V2.0 PAGE 3 SOURCE-STMT PASS2 FIG414 FIG414 FIG414 REL					
3 A	C3B600'	52	420		JP	JTASK	;RETURN TO	MONITOR	
3D	=0121'	56 57	424 425	STACK	DEFS				
				; DEFIN:	E I/O	VECTORS			
22	FF 444B30 20202020 2020	61 2 3 4	429 431 432 433	INPUT	DEFB D EF M	DR OFFH,,,,04 OFFH 'DKO'	Н		
2E 31 32 36 37 3A 13C 13E	0000000 00 000000 0000 0000	5 6 10 11 13 16 18 22 25 26	434 435 437 438 440 442 444 447 449 450	L3 L4 L6 L7 L10	DEFB DEFW DEFB DEFB DEFW DEFW DEFB	0 0,0,0 0 0	0.0.0		
	00000000 00000000 00000000 00000000	20		• (EM)T		(TE I/O WITH		DED DICK	ACCESS
				;				PER DISK	ACCESSI
		64 2 3 4	454 456 457 458	OUTPUT	DEFB	'TTO'	,,		
15E 161 162 166 167 16A 16C 16C	0000000 00 000000 0000 0000	8 11 13 16 18 22 25	459 460 462 464 466 473 475 475	L3 L4 L6 L7 L10 L12 ; (THE I	DEFB DEFW DEFB DEFW DEFW DEFB DEFW	0,0,0 BYTE+4 0,0 0,0,0 0,0,0,0 0 0,0,0,0,0,0,0,0,0 COMMAS ARE R	EQUIRED TO	DEFAULT	THE
0181		68	481	; FILEN/ ;	AME AN END	D EXTENSION	TO BLANKS)		

APPENDIX A

MACRO-80 ERROR CODES

3F RELOCATABLE USE - A relocatable value was used in an 8-bit operand. The user should assure that relocatable quantities are used only for 16-bit operand values (addresses).

40 BAD LABEL - An invalid label was specified. A label must start with an alphabetic character (A-Z) and may contain only alphanumeric characters (A-Z, 0-9) or question mark (?) or underline (_). A label may start in any column if followed by a colon. It does not require a colon if started in column one.

41 BAD OPCODE - An invalid Z80 opcode or pseudo-op or an undefined macro name was specified.

42 BAD OPERAND - An invalid operand or combination of operands was specified for a given opcode.

43 BAD SYNTAX - The specification of an operand or expression was invalid.

44 UNDEFINED - A symbol was used in an operand which was not defined in the program, either locally or as an external symbol.

45 MULTIPLE DEF - A symbol was defined more than once in the same program.

46 MULTIPLE PSECT - A PSECT pseudo-op was used more than once or was defined after the first code-producing statement of the program. The PSECT pseudo-op should be used only once at the beginning of a program.

47 MEMORY OVERFLO - This means that not enough memory exists in the system to assemble the given program. This can occur because the program contains too many symbols, macro parameters, or macro expansion arguments.

48 EXTERNAL USAGE - An external symbol was used in an expression or the operand of an EQU or DEFL pseudo-op. The user should assure that an external symbol is not used in these situations.

49 not used.

4A UNBAL QUOTES - An uneven number of quote characters (') occurred in an operand.

4B LABEL REQUIRED - A label was not used in a statement that required it. A label is required for EQU, DEFL, and MACRO statements.

4C OVERFLOW - In evaluating an expression, the value of the expression exceeded 65536 (OFFFFH). The user should check the expression for validity. Alternatively, the .RES. operation may be used to ignore the overflow condition and only the least significant 16 bits of the expression will be used.

4D OUT OF RANGE - The final value of an operand was found to be out of the range allowed for the given opcode. For example, the valid range of the JR instruction is -126 through +129.

4E BAD DIGIT - An invalid digit was found in a number.

4F not used.

50 not used.

51 not used.

52 MULTIPLE NAME - The NAME pseudo-op was used more than once in the same program.

53 NESTED INCLUDE - An included file contained another INCLUDE pseudo-op. The user should assure that the INCLUDE pseudo-op is not used in the body of an included module.

54 EXPR TOO BIG - The expression evaluator stack reached its limit. The user should reduce the complexity of the expression in the statement which caused the error.

55 not used.

56 NUMBER TOO LARGE - A constant in an operand was too large in value for the given operation.

57 OUT OF RANGE - The value of either operand in the string operand [,] was found to be out or range. The limits are 1 and 63.

58 TOO MANY IFS - The nesting of conditional assembly pseudo-ops (IF and ENDIF, or COND and ENDC) was too large or unmatched. The maximum level of nesting is 11, and each IF (COND) statement must be matched by an ENDIF (ENDC) statement.

59 STRING TOO BIG - The size of the substring in a sequence of substring operations exceeded the available space. The user should reduce the number of substring expressions within the statement or macro body.

5A MERROR INDICATION - This error code is output when an MERROR statement is expanded in a macro.

5B BAD THEN/ELSE - A THEN-clause or ELSE-clause operand was incorrectly specified. The operand must be a local macro label defined by an MLOCAL pseudo-op.

5C TOO MANY PARMS - The maximum number of parameter substitutions in calling a macro was exceeded. Maximum is 99.

5D BAD MACRO STMT - A macro pseudo-op was used outside of a macro body.

5E INCLUDE IN MAC - An INCLUDE statement was used inside a macro body.

5F LABEL USAGE - The usage of a label in a macro expansion was not allowed.

60 NO MEND STMT - A macro was defined without an MEND statement.



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