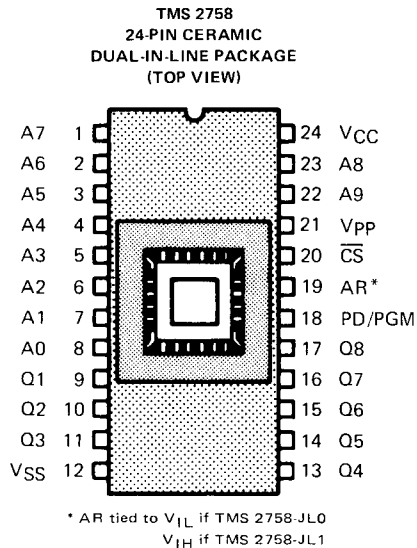- 1K X 8 Organization
- Single +5-V Power Supply
- Pin Compatible with Existing ROMs and EPROMs (8K, 16K, 32K, and 64K)
- Pin-Compatible With I2758
- All Inputs/Outputs Fully TTL Compatible
- Static Operation (No Clocks, No Refresh)
- Max Access/Min Cycle Time . . . 450 ns
- 8-Bit Output for Use in Microprocessor-Based Systems
- N-Channel Silicon-Gate Technology
- 3-State Output Buffers
- Low Power Dissipation
  - Active . . . 285 mW Typical
  - Standby . . . 50 mW Typical
- Guaranteed dc Noise Immunity with Standard TTL Loads
- No Pull-Up Resistors Required

TMS 2758
24-PIN CERAMIC
DUAL-IN-LINE PACKAGE
(TOP VIEW)

A7	1		24	V_{CC}
A6	2		23	A8
A5	3		22	A9
A4	4		21	V_{PP}
A3	5		20	$\overline{CS}$
A2	6		19	AR*
A1	7		18	PD/PGM
A0	8		17	Q8
Q1	9		16	Q7
Q2	10		15	Q6
Q3	11		14	Q5
V_{SS}	12		13	Q4

* AR tied to V_{IL} if TMS 2758-JL0
V_{IH} if TMS 2758-JL1

PIN NOMENCLATURE	
A0 thru A7	Address inputs
AR	Array Select
PD/PGM	Power Down/Program
Q1 thru Q8	Input/Output
$\overline{CS}$	Chip Select
V_{CC}	Operating Power Supply
V_{PP}	Programming Power Supply
V_{SS}	0 V Ground

description

The TMS 2758 is an 8,192-bit, ultraviolet-light-erasable, electrically programmable read-only memory. This device is fabricated using N-channel silicon-gate technology for high speed and simple interface with MOS and bipolar circuits. All inputs (including program data inputs) can be driven by series 74 TTL circuits without the use of external pull-up resistors, and each output can drive one Series 74 TTL circuit without external resistors. The data outputs are three-state for wired-OR connection of multiple devices on a common bus. The TMS 2758 is pin-compatible with the TMS 2516 16K EPROM.

Since this EPROM operates from a single +5-V supply in the read mode, it is ideal for use in microprocessor systems. One other supply (+25 V) is needed for programming, but all programming signals are TTL levels and require a single 50-millisecond pulse. For programming outside of the system, existing EPROM programmers can be used. Locations may be programmed sequentially, in blocks, or at random. Total required programming time for all bits for the TMS 2758 is 50 seconds.

The TMS 2758 is offered in dual-in-line cerpak (JL suffix) package rated for operation from $0°C$ to $70°C$.

Complete information may be obtained from your local TI Sales Office or authorized distributor listed near the front of this data book.