



LAST USED REF DESIGN.
U40, C23, R13, L1, JP2

DASH NO. CHART				
ASCY DASH NO.	-01 (SH 2)	-02 (SH 2)	-03 (SH 2)	-04 (SH 2)
DESCR.	PARALLEL	SEQUENTIAL	DUAL DRIVE	DUAL DRIVE
U6	-	74174	-	74174
U9	-	74174	-	74174
U16	-	74LS08	-	74LS08
U23	-	74LS74	-	74LS74
U32	-	74LS74	-	74LS74
U40	-	74LS74	-	74LS74
U31	-	74LS86	-	74LS86
U39	-	74LS14	-	74LS14
U24	-	74LS00	-	74LS00
JPA-JPM	OPEN	CLOSED	OPEN	CLOSED
JP1	CLOSED	CLOSED	OPEN	OPEN
JP2	OPEN	OPEN	CLOSED	CLOSED

NOTES:

UNLESS OTHERWISE SPECIFIED:

1. ALL RESISTORS ARE IN OHMS, 1/4W, 5%.
2. CAPACITORS C1, C2, C3 AND C4 ARE 4.7MFD, 10V, 20%.
3. ALL CAPACITORS ARE 0.02MFD, 50V, 20%.

4. SHEET 2 IS FOR PARALLEL LOGIC OPTION.
SHEET 4 IS FOR SEQUENTIAL LOGIC OPTION.
SEE DASH NO. CHART.

5. ALL JUMPERS AS SHOWN ARE USED ON SEQUENTIAL -02 VERSION ONLY.

6. FOR DUAL DRIVE OPTION CUT JUMPER "JP1" INSTALL JUMPER JP2" (190672 -03, -04)

7. THIS SCHEMATIC VALID FOR:
90672-01 REV B PCN 0743
90672-02 REV B PCN 0743
90672-03 REV B PCN 0743
90672-04 REV B PCN 0743

DESIGNER AND IN CHARGE		DATE	Qume
DRAWN		6-22-77	
CHECKED		DATE	SCHEMATIC
APPROVED		4-25-77	
DATE		DATE	SM3/TT75 COORD. LOGIC
0801		4-12-77	
REV		REV	SHEET 2X OF 5
A		B 0793	
PART NO.		REV	REV
90672-01		5	5
90670-XXC		D	