

PERIPHERAL EQUIPMENT DIVISION

TITLE EXCESSIVE OVERSHOOT/SETTLE TIME DELAY REGISTER				PIB NO. DK3025
PRODUCT LINE	TAPE DISK FORMATTER	EQUIPMENT CHANGED All Logic PCBA's	MODEL SERIES AFFECTED D3000	EFFECTIVE DATE 2-14-75
CLASS OF BULLETIN: <input type="checkbox"/> IMPROVEMENT <input checked="" type="checkbox"/> RETROFIT ON FAILURE <input type="checkbox"/> RETROFIT RECOMMENDED <input type="checkbox"/> SERVICE INFORMATION ONLY		ORDER PART KIT NO. N/A	EFFECTIVITY D3000's prior to S/N 1000	
PURPOSE: There have been some isolated cases where a particular D3000 would exhibit excessive overshoot and become one track off. Recent changes may possibly correct this condition on units prior to S/N 1000. UI09, the settle time delay register, can have its time factor shortened. By shorting this settle time, reducing the velocity and leaving the base seek time at approximately 35 milliseconds, the positioner will actually move slower. Results are the present overshoot is cut to a minimum, with no loss in seek performance. Because of the isolated number of instances, "Retrofit on Failure Only".				
SYMPTOMS: <ul style="list-style-type: none"> - Correct sector but one track off - Addressing errors - Formatter errors - Address compare errors - Unexplainable controller time-outs 				
PARTS REQUIRED: <ul style="list-style-type: none"> - Approximately one (1) inch of jumper wire 30 AWG, silver plated copper wire, Mylar insulation 				
TOOLS REQUIRED: <ul style="list-style-type: none"> - Soldering iron with 25 to 27 watt tip 				
REWORK INSTRUCTIONS: 1.0 Perform all servo adjustments and ensure overshoot is to the minimum. (Ref: D3000 Manual, 102780, Section 6) 2.0 Locate I.C. UI09. (Opposite component side) 2.1 Cut etch to Pin 12 (see Figure 1).				

Additional Information Be Required — Contact

PERTEC

PERIPHERAL EQUIPMENT DIVISION
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 Phone (213) 882-0030 / TWX (910) 494-2093
 ATTENTION: PRODUCT SUPPORT MANAGER

PERTEC 20 K012C(1)

Distribution Code - 6318

TITLE EXCESSIVE OVERTHOOT/SETTLE TIME DELAY REGISTER

PIB NO.
DK3025

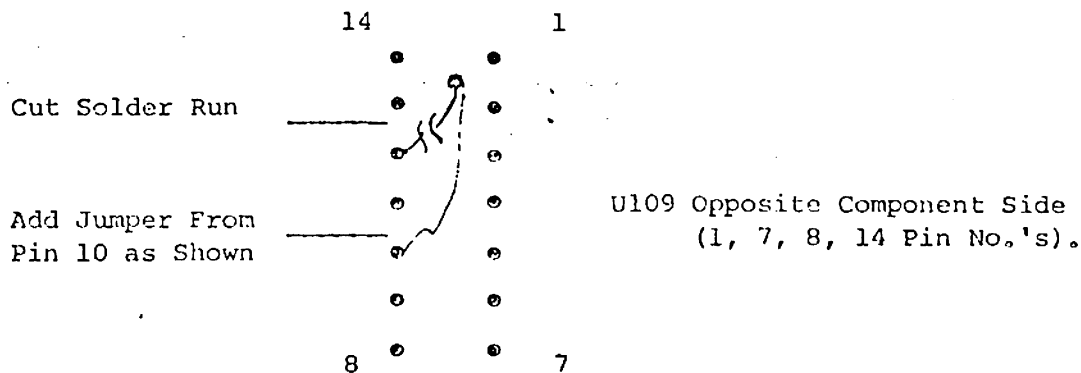


FIGURE 1

- 2.2 Add jumper from Pin 10 to output etch as shown in Figure 1.
- 2.3 Perform in D3000 Service & Maintenance Manual, Section 6, SEEK TIME FINAL ADJUSTMENTS.

TEST PROCEDURE:

Ensure overshoot on a one track seek is within the ± 2 volt specification.

DRAWING CHANGES ARE TO BE MADE AS FOLLOWS:

- A. Applicable Logic PCBA drawing.

<u>FROM</u>	<u>TO</u>
QG-12	QE-10

- B. Stamp Logic PCBA with PIB DK3025.

TIME REQUIRED:

Approximately one hour.

THESE CHANGES ARE ALSO APPLICABLE TO FOLLOWING LOGIC BOARDS:

103705	Diablo Compatible Logic PCBA
103689	SC907 Logic PCBA
103687	SC914 Logic PCBA
102831	Pertec Logic PCBA (standard)

NOTE

CHANGE IS ALSO APPLICABLE FOR WIRE WRAP VERSIONS AVAILABLE UPON REQUEST.