

REV **D** **PERSCI INCORPORATED** **MATERIAL LIST**

M/L NO. **200740-009**

TITLE **ASSY, PCB-DRIVE ELECTRONICS** M/L NO. **200740-009** REV **D**  
**ONE DRIVE SELECT, NO WRT. PRECOMP. (OPT 3,6)**

COMPILED BY: **22M** DATE **10/30/79** CHECKED BY: DATE APPROVED BY: DATE **10/31/79** RELEASE/CHANGE DATE/ECO **12/1/80 684**

NEXT ASSY. **200800** 1ST USED ON **299B** DWG SIZE **NONE** SHEET **1** OF **3**

ITEM NO.	DRAWING TITLE	DWG NO.	QTY	CODE	REMARKS
	USE BASIC BD. 200740-000				
	LATEST REVISION, AND				
	ADD/VERIFY THE FOLLOWING:				
1	<del>IC 74LS74</del>	<del>150629</del>	<del>0</del>		<del>OMIT</del> U2
2	<del>IC 74LS74</del>	<del>150629</del>	<del>0</del>		<del>OMIT</del> U3
3	<del>IC 75453</del>	<del>150618</del>	<del>0</del>		<del>OMIT</del> U22
4	<del>IC 75454</del>	<del>150696</del>	<del>0</del>		<del>OMIT</del> U23
5	IC 75453	150618	1		ADD U26
6	IC 75453	150618	1		ADD U28
7	IC IH5012	150620	1		ADD U35
8					
9					
<p><u>FOR THE FOLLOWING JUMPER POINTS:</u></p> <p>J = JUMPER</p> <p>O = OPEN</p> <p>(* = AN "ETECHEd" JUMPER EXISTS AND MUST BE CUT FOR OPEN CONDITION)</p>					
10	WIRE, BUS, 24 AWG	100052-024	A/R	*	J A2 TO A3
11	SLEEVE, INSULATING	100135-022	A/R		O A1 TO A3
12				*	J A5 TO A6
13					O A4 TO A6
14				*	J A7 TO A8
15				*	J A9 TO A10
16					O A7 TO A10
17					O A9 TO A11
18				*	J B5 TO B6

CODE: **D** = DETAIL PART WITH NO M/L    **A** = ASSEMBLY WITH M/L  
**R** = REFERENCE DOCUMENT            **S** = SHIP SEPARATE

REV D	<b>PERSCI INC</b>					<b>MATERIAL LIST</b>				
	TITLE ASSEMBLY,PCB DRIVE ELECTRONICS					M/L NO. 200740-009			REV D	
M/L NO. 200740-009	COMPILED BY: DATE		CHECKED BY: DATE		APPROVED BY: DATE		RELEASE/CHANGE DATE			
			<i>Mike Head 12-3-80</i>							
NEXT ASSY.			1ST USED ON 299B		DWG SIZE		SHEET <u>2</u> OF <u>3</u>			
ITEM NO.	DRAWING TITLE			DWG NO.	QTY	CODE	REMARKS			
19						O	B3 TO B2			
20						O	B3 TO B4			
21						O	B3 TO B1			
22						* J	B1 TO B7			
23						J	C1 TO C2			
24						* J	C3 TO C4			
25						O	C4 TO C5			
26						* J	C5 TO C6			
27						* J	C7 TO C8			
28						* J	C9 TO C10			
29						* J	C11 TO C12			
30						* J	D1 TO D2			
31						* O	D3 TO D4			
32						* O	D5 TO D6			
33						* J	D7 TO D8			
34										
35						O	E1 TO E2			
36						O	E3 TO E4			
37										
38						O	F1 TO F2			
39						* J	F2 TO F3			
40						O	G1 TO G2			
41						O	G3 TO G4			
42						* J	H1 TO H2			
43						O	H3 TO H4			
44						*G	J SIDE 1, C TO			G
	ITEM 45 SEE SHT 3 OF 3.					*G	J SIDE 2, C TO			G

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REV D  
 M/L NO. 200740-009

# PERSCI INC

# MATERIAL LIST

<b>TITLE</b> ASSEMBLY, PCB DRIVE ELECTRONICS	<b>M/L NO.</b> 200740-009	<b>REV</b> D
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<b>COMPILED BY:</b> R. Stevens	<b>DATE</b>	<b>CHECKED BY:</b>	<b>DATE</b>	<b>APPROVED BY:</b>	<b>DATE</b>	<b>RELEASE/CHANGE DATE</b> 12/1/80
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<b>NEXT ASSY.</b>	<b>1ST USED ON</b> 299B	<b>DWG SIZE</b>	<b>SHEET</b> 3 <b>OF</b> 3
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ITEM NO.	DRAWING TITLE	DWG NO.	QTY	CODE	REMARKS
45	( SIGNAL GROUND ISOLATION)			*	0 A

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