

CONTROL DATA® FLEXIBLE DISK DRIVE MODEL 9406-4

GENERAL DESCRIPTION
OPERATION
INSTALLATION AND CHECKOUT
THEORY OF OPERATION
DIAGRAMS
MAINTENANCE
MAINTENANCE AIDS
PARTS DATA
WIRE LISTS

MAGNETIC PERIPHERALS INC.

GD a subsidiary of CONTROL DATA CORPORATION

R E V I S I O N R E C O R D		A10-E	S8-F												-	
DATE SHEETS AFFECTED 1 2 3 4 5 6 7 8 9 10 11 12 AUTHORITY		REVISION RECORD														
A SIMAY29 JB A A A A A A A A A A A A A A A A A A	R DATE CHEET ACCESSED				<u> </u>						CHANGE AUTHORITY					
B 31 314 27 18	٧	1		1	2	3	4	5	•	7		•	10	11	12	
B 5.40 13 ii A A A A C A A B A PL 20598 C 5.40 13 ii, vi A D A D D A A B A PL 20644 D FEB3 ii, vi A D A D D A A B A PL 20679	A	ISSU	JE	A	A	A	A	A	A	A	A	A				Magdeburger
C 5. Wise ii A A A A C A A B A PL 20644 D FEB3 ii, vi A D A D D A A B A PL 20679	В	5 42	<u>11</u>	A	Α	A	A	В	A	A	A	A		<u> </u>		PL 20598
D FEB3 ii, vi A D A D D A A B A PL 20079		81 007	ii	A	A	A	A	С	A	A	В	A				PL 20644
	D	82 94 FEB3	ii, vi	A	a	A	D	D	A	A	В	A				PL 20679
			· 													
																r.
			<u> </u>				-									
								·								
															-	

COPYRIGHT © 1981, 1982
MAGNETIC PERIPHERALS INC.
Printed in the United States of America
All Rights Reserved

Address all comments concerning this publication to the distributor or use the enclosed user comment sheet located in the back of this publication.

This manual provides the information needed to install, operate and maintain the Control Data Corporation Model 9406-4 Flexible Disk Drive (FDD) and is intended to support customer engineers who require detailed information about the Flexible Disk Drive's operation.

The total content of the manual is comprised of two publications, each having a unique publication number, and is contained in one volume. The Manual's publication number, 77653520, is that of the front matter, Sections One through Seven, and Section Nine. This number should be used when making reference to the Model 9406-4 Flexible Disk Drive Hardware Maintenance Manual.

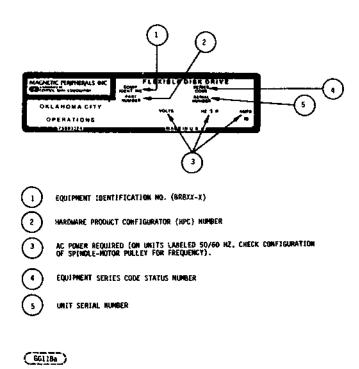
This manual applies to several configurations of the FDD. Refer to the equipment name plate located on the right hand side of the unit (as viewed from the front) to determine the appropriate Hardware Product Configurator (HPC) and Equipment number as shown in the Flexible Disk Drive Configurator Sheet, page iv.

Sections VIII Parts Data is identified by the unique Publication number 77653522.

EMI NOTICE

NOTICE: This equipment has been designed as a component to high standards of design and construction. The product, however, must depend on receiving adequate power and environment from its host equipment in order to obtain optimum operation and to comply with applicable industry and governmental regulations. Special attention must be given by the host manufacturers in the areas of safety, power distribution, grounding, shielding, audible noise control, and temperature regulation of the device to insure specified performance and compliance with all applicable regulations. This equipment is a component supplied without its final enclosure and therefore is not subject to standards imposed by FCC Rules for Electro-Magnetic Interference (EMI). Federal Docket 20780/FCC 80-148 Part 15.

77653520-A iii



NOTES:

- 1. PARTS BREAKDOWN IDENTIFICATION From unit nameplate (see above representation), find HPC number (2). After reading instructions for use of Section 8, Illustrated Parts Catalog, of this manual, use HPC number to determine specific parts configuration for unit in question.
- 2. EQUIPMENT INQUIRIES Equipment inquiries should reference the unit's Equipment Identification Number 1 and Series Code, Number 4 from unit nameplate, as represented above.

TABLE OF CONTENTS

SECTIO	V	PAGE
GENERA	AL DESCRIPTION	
1.1 1.2 1.3	PURPOSE AND USE OF EQUIPMENT. PRODUCT DESCRIPTION 1.3.1 Physical Description	1-1 1-1 1-1 1-1 1-1 1-2
OPERA?	TION	
2.1 2.2 2.3	2.2.1 Flexible Diskette Loading	2-1 2-1 2-1 2-1 2-1 2-1
2.4	2.3.2 Write Error	2-1 2-2 2-2
INSTAL	LATION AND CHECKOUT	
3-1 3.2 3.3 3.4	UNPACKING INSTALLATION CABLING AND CONNECTIONS 3.4.1 Input-Output Cable 3.4.2 DC Power Connection 3.4.3 AC Power Connection ENVIRONMENT	3-1 3-1 3-1 3-1 3-1 3-2 3-2
3.6	INITIAL CHE CKOUT · · · · · · · · · · · · · · · · · · ·	3-2 3-3
THEOR	OF OPERATION	
4.1 4.2 4.3	GENERAL DESCRIPTION FUNCTIONAL DESCRIPTION 4.3.1 Control Logic 4.3.2 Write Logic 4.3.3 Head-Select Logic 4.3.4 Disk Change 4.3.5 Read Logic	4-1 4-1 4-3 4-3 4-5 4-6 4-6 4-6 4-7

77653520-A

SECTIO	N	PAGE
4.4	4.3.7 Read/Write Heads	4-7
	4.4.2 Transmitter Characteristics	4 - 7 4-7
4.5	4.4.4 Control and Data Line Functions	. 4-10
4.6	ALTERNATE I/O	
	4.6.1 Radial Ready	
	4.6.2 Radial Index	
DIAGRA	AMS	
5.1	INTRODUCTION	5-1
MAINTE	INANCE	
6.1	INTRODUCTION	6-1
6.2	MAINTENANCE TOOLS	6-1
6.3	TROUBLESHOOTING	6~1
	6.3.1 DC Voltage and Signal Check	6-2
6.4	ADJUSTMENT PROCEDURES	6-4
	6.4.1 Write-Splice Check and Adjustment	6-4
	6.4.2 Actuator Alignment	6-6
	6.4.3 Clamshell-Closed Switch Adjustment	6-7
	6.4.4 Track 0 Stop Adjustment	6-8
	6.4.5 Diskette Ejector	6-8 6-9
	6.4.7 Head-Unload Clearance	6-9
	6.4.8 Low-Current-Switch Optical Sensor Adjustment	6-11
	6.4.9 Azimuth Adjustment	6-11
6.5	REMOVAL AND REPLACEMENT PROCEDURES	6-11
0.0	6.5.1 Printed-Circuit Board (PWA)	6-12
	6.5.2 Carriage Replacement	
	6.5.3 Drive Motor Assembly	6~14
	6.5.4 Stepper-Motor Replacement	6-14
6.6	FREQUENCY CONVERSION	
	6.6.1 Operating Frequencies Conv. Proc	
MAINT	ENANCE AIDS	
7-1	INTRODUCTION	7-1
7.2	PHYSICAL DESCRIPTION (Logie)	7-1
7.3	USE OF RELATIVE LEVEL INDICATORS	
7.4	INTEGRATED CIRCUITS	7-1
PARTS	DATA	
8.1	INTRODUCTION	
8-2	ILLUSTRATIONS	
8-3	PARTS LIST	
8-4	PRODUCT CONFIGURATIONS	
	8.4.1 Hardware Product Configurator (HPC)	8-1

vi 77653520-D

SECTIO	ON	PAGE
8.5	REPLACEMENT PARTS	8-2 8-2
8.6	PARTS LIST INSTRUCTIONS	
	8.6.3 Cross Reference Index	8-14
WIRE :	LISTS	
9.1	INTRODUCTION	0_1
9.2	UPPER-HARNESS ASSEMBLY	
9.3	LOWER-HARNESS ASSEMBLY	9-1
9.4	STEPPER MOTOR	9-1
9.5	DC HARNESS	9-1
9.6	SENSOR ASSEMBLY TRACK 43, TRACK 00 HARNESS ASSEMBL	
9.7	DOOR-LOCK-SOLENOID ACTIVITY LED	9 - 2
9.8	HEAD ASSEMBLIES	9-2
9.9	SOLENOID HARNESS ASSEMBLY	9-2

77653520-A vii/viii

1.1 INTRODUCTION

The Model 9406-4 Flexible-Disk Drive (FDD) is a compact, portable, random-access, data-storage device that interfaces with a central processor via a control unit. Input/Output data and control signals are transmitted by means of an I/O cable.

1.2 PURPOSE AND USE OF EQUIPMENT

Data, in the form of magnetized bits, is written on, or read from the tracks of a rotating diskette. The FDD uses a single, flexible, removable diskette enclosed in a sealed jacket. The unit may be configured for hard-sector or soft-sector operation.

1.3 PRODUCT DESCRIPTION

The major FDD components are the spindle, disk drive motor, read/write heads, stepping motor, track-indexing devices and printed-circuit board.

The options include Data/Clock Separation and Sector Separation.

1.3.1 PHYSICAL DESCRIPTION

The physical dimensions for the equipment are as follows:

	9406-4
Height	4.62 inches (117.4 mm)
Width	9.50 inches (241.3 mm)
Depth	14.25 inches (362 mm)
Weight	12 lbs. (5.44 kg)

1.3.2 ELECTRICAL DESCRIPTION

The electrical specifications for the equipment are as follows:

- DC Power Source (Supplied by Host Equipment)
 - +24 volts (±10%) @ 0.120A Max when Deselected @ 0.70 A Typical when Stepping + 5 volts (+ 5%) @ 0.6 A Typical
- AC Power Source Refer to the FDD nameplate to determine AC power requirements.

1.3.3 PERFORMANCE CHARACTERISTICS

The equipment specifications for the FDD are as follows:

• ACCESSING TIME

Maximum Access Time	248 ms
Maximum One-Track Access Time	23 ms
Average Access Time	91 ms

• RECORDING

Mode			
Density (nominal)	Double Frequency	MFM	Track
Head 0	1836 BPI (72BPmm)	3672 BPI(145BPmm)	Outer
	3268 BPI (129 BPmm)	6536 BPI (257 BPmm)	Inner
Head 1	1879 BPI (74 BPmm)	3758 BPI (148 BPmm)	Outer
	3408 BPI (134 BPmm)	6816 BPI (268 BPmm)	Inner
Data Transfer Rate	249,984 bits/sec	499, 968 bits/sec	
Bits/Byte	8	8	
Bits/Track	41, 664	83, 328	
Tracks/Surface	77	77	
Sectors	Format Determined	Format Determined	
DATA CAPACITY			
Bytes/Track	5,208	10,416	
Bits/Track	41,664	83,328	
Bits/Surface	3,208,128	6,416,256	

• FLEXIBLE DISKETTE (Optional)

Diskette Dimensions

Useable Diskette Recording Surfaces Diskette Surface Diameter Recording Radii (Nominal) Head 0

Head 1

Diskette Surface Coating Diskette Velocity

READ/WRITE HEADS

Heads/Unit Track Width Track Spacing Erase to Read/Write Gap CDC 421 Single-Sided, Single-Density CDC 423 Single-Sided, Double-Density CDC 425 Double-Sided, Double-Density

8x8 inches (203.2 x 203.2 mm) (including jacket)

7.88 in. (200.1 mm)

Track 76 2.0290 in. (51.5 mm) Inner Track 00 3.6123 in. (91.8 mm) Outer Track 76 1.9457 in. (49.4 mm) Inner Track 00 3.5290 in. (89.6 mm) Outer Magnetic Oxide 360 r/min

2 0.013 in. (0.33 mm) 0.02083 in. (0.529 mm) 0.036 in. (0.914 mm)

2 - 1

2.1 INTRODUCTION

The FDD is under direct control of the input/output and power sources. No special start-up procedure is required. Operation is fully automatic and requires no operator intervention during normal operation.

2.2 OPERATING INSTRUCTIONS

Verify that power and I/O cables are securely attached before operation.

2.2.1 FLEXIBLE DISKETTE LOADING

- a. Apply AC/DC power to unit.
- b. Open FDD door.
- c. Remove diskette from storage envelope as show in Figure 2-1.
- d. Be sure the Write-Protect slot in the jacket is open, as shown in Figure 2-1, if the diskette is to be write-protected.
- e. If a diskette with a Write-Protect slot is not utilizing the Write Protect, that is, it will be written on, the slot must be covered with a piece of tape which is opaque to infrared.
- f. Carefully slide diskette into FDD, as shown in Figure 2-1, until jacket is solidly against stops and sets the ejector mechanism.
- g. Carefully close unit door. Ensure that jacket is properly seated, spindle has engaged diskette, and door is closed and latched.
- h. Protect the empty envelope from liquids, dust, and metallic materials.

2.2.2 FLEXIBLE DISKETTE REMOVAL

- a. Open FDD door to stop diskette rotation and disengage spindle.
- b. Remove diskette from FDD and put it in its storage envelope.
- c. Close FDD door.

2.3 ERROR RECOVERY

The following paragraphs give information needed to recover from possible errors in equipment operation.

2.3.1 SEEK ERROR

Seek errors will rarely occur unless the stepping rate is exceeded. In the event of a seek error, recalibration of track location can be achieved by repetitive Step Out commands until a Track 00 signal is received.

2.3.2 WRITE ERROR

To guard against degradation from imperfections in the media, no more than four attempts to write a record should be used when read after write errors are encountered. In the event a record cannot be successfully written within four attempts, it is recommended that the sector or track be labeled defective and an alternate sector or track assigned. If more than two defective tracks are encountered, it is recommended that the diskette be replaced.

77653520-A

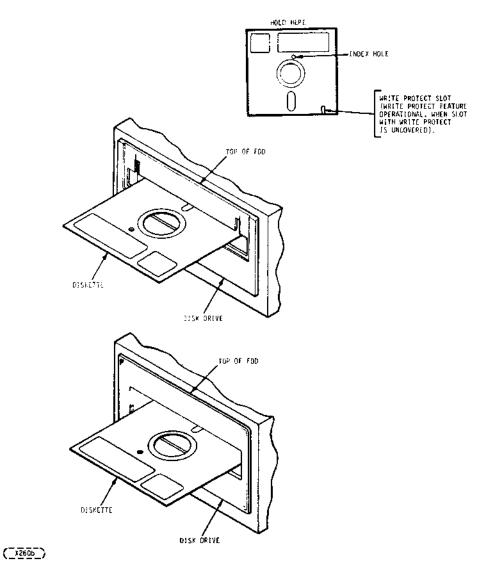


FIGURE 2-1. DISKETTE INSTALLATION

2.3.3 READ ERROR

In the event of a Read error, up to five attempts should be made to recover with rereads. If after five attempts the data has not been recovered, retract the head to Track 00, reseek to the data track and attempt five additional rereads. Unloading the head when data transfers are not imminent will increase the data reliability and extend the diskette life.

2.4 DISKETTE HANDLING RECOMMENDATIONS

Since the recorded diskette contains vital information, reasonable care should be exercised in its handling. Longer diskette life and trouble free operation will result if the following recommendations are followed.

Do not use a writing device which deposits flakes e.g., lead or grease pencils, when writing on diskette jacket label.

- b. Do not fasten paper clips to diskette jacket edges.
- c. Do not touch diskette surface exposed by jacket slot.
- d. Do not clean diskette in any manner.
- e. Keep diskette away from magnetic fields and from ferromagnetic materials that may be magnetized.
- f. Return diskette to envelope when removed from FDD.
- g. Protect diskette from liquids, dust, and metallic substances at all times.
- h. Do not exceed the following storage environmental conditions:

Temperature:
Relative Humidity:
Maximum Wet Bulb:

50° to 125° F (10° to 56.1°C)

8% to 80% 85°F (29.4°C)

- i. Diskettes should be stored in a box or cabinet when not in use.
- j. Remove diskette before applying or removing power to the FDD.

77653520-A 2-3/2-4

3.1 INTRODUCTION

This section provides the information and procedures necessary to put an FDD into operation.

3.2 UNPACKING

Unpack FDD as follows:

- a. Cut banding and lift top half of styrofoam shell from unit.
- b. Lift unit in polyethylene bag from bottom half of styrofoam shell and remove unit from polyethylene bag.

During unpacking, care must be used so that any tools being used do not inflict damage to the unit. As a unit is unpacked, inspect it for possible shipping damage. All claims for this type of damage should be filed promptly with the carrier involved. If a claim is filed for damages, save the original packing materials.

3.3 INSTALLATION

Install the FDD in the designated location in the host equipment. Remove blank head protective diskette from unit.

3.4 CABLING AND CONNECTIONS

Connect the AC cable, I/O cable, and DC cable if applicable between the FDD and host equipment. Adequate circuit protective devices must be provided by the host equipment to meet applicable safety standards.

3.4.1 INPUT-OUTPUT CABLE

The maximum cable length from connector to connector is 25 feet (7.62 m). The characteristic impedance should be 150 ohms.

The information relative to the I/O connector (J1) and pin/signal assignments are defined in Figures 5-1, 5-3 and 5-4.

The terminating resistor pack RM5 (see Figure 5-4) is to be installed in the end FDD (farthest from the controller) ONLY. Terminators in more than one FDD may result in damage to the controller.

3.4.2 DC POWER CONNECTION

The mating connector cable should consist of 18 AWG minimum. Refer to Figure 3-2 for connector part numbers.

3.4.3 AC POWER CONNECTION

The mating connector cable should consist of stranded wire, 18 AWG minimum with center-pin connection utilized as frame ground. Refer to Figure 3-1 connector part numbers and attachment.

3.5 ENVIRONMENT

Operating and storage environments of the FDD are as follows:

Operating:

40° to 115°F (4.4° to 46.1°C) 12°F (6.6°C)/hr.

max. fluctuation

20% to 80% relative humidity (providing

there is no condensation)

Non-Operating:

-30° to +150°F (-35° to 65°C)

5% to 95% relative humidity (providing

there is no condensation)
Max. Wet Buib 80°F (27°C)

3.6 INITIAL CHECKOUT

This procedure should be used to determine that the FDD is operational. The procedure assumes that the unit is installed and the I/O and power cables are connected.

- a. Assure that the shipping insert has been removed before applying power.
- b. Apply AC power to unit and visually check that the spindle rotates.
- c. Apply DC power to unit.
- d. Insert diskette as described in Section 2.
- e. Apply a head-load-command signal to the unit and close the access door. Check that the head-load solenoid actuates, and the door-closed switch is actuated.
- f. Apply a stepping-command signal to the unit and check that the actuator steps the head as commanded.
- g. Remove diskette.
- h. Remove the command signals and power from the unit.

3.6.1 OPERATION FREQUENCY

If the required operating frequency is different than that which the unit is configured, a procedure for converting operating frequencies using the dual-diameter reversible pulley is provided in Section 6, "Frequency Conversion".

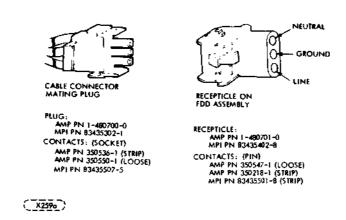
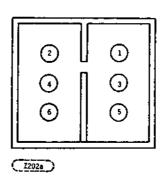


FIGURE 3-1. AC CABLE ASSEMBLY



PIN 8	<u>USE</u>	•
1	+24	Mating Connector Amp 1-480270-0
2	+24 Return	Pins Amp 60619-1
3	NC	
4	NC	
5	+5	
6	+5 Return	

FIGURE 3-2. DC CONNECTOR

4.1 INTRODUCTION

The basic functions performed by the FDD are: (1) receive and generate control signals, (2) position the Read/Write heads on selected tracks, and (3) write or read data upon command from the FDD controller. These functions are accomplished upon selection after initial indication to the controller that the FDD is ready to operate and accept commands.

The theory of operation for the FDD is divided into two parts. The first part gives a general theory of operation. The second part gives a detailed functional description of all major components, both electronic and mechanical, and describes all signals exchanged between the FDD and the controller.

Sections 4 and 5, Theory of Operations and Diagrams, respectively, which follow, detail operation of both hard-sector/data-separation configurations, and soft-sector/composite-read-data configuration(s).

Separate PWA's and schematics for both sets of configurations are contained in Section 5.

4.2 GENERAL DESCRIPTION

The basic function of the FDD is to indicate to the controller when it is ready to operate, and respond to the commands of the controller to: (1) receive and generate control signals; (2) position the Read/Write heads to selected tracks; and (3) write or read data on the diskette when selected. All of the functions described which are options are switch selectable.

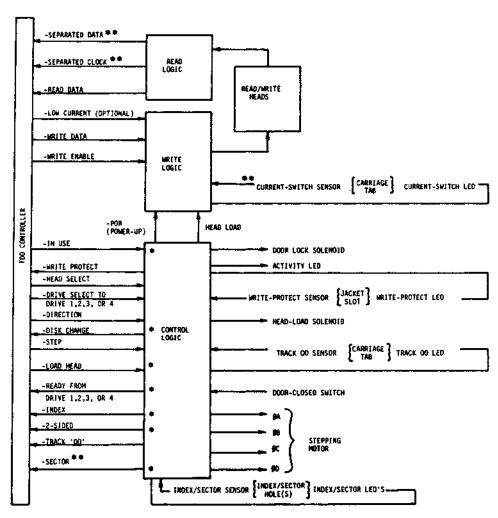
Signals received and transmitted by the FDD are shown in Figure 4-1. Some signals received by the FDD are gated with Drive Select so that no stepping, reading or writing can be performed on an unselected FDD. Also, some signals generated within the FDD are gated with Drive Select so that they can not be transmitted from an unselected FDD.

During the write operation, the selected FDD must have heads loaded, Head Select, Write Enable and Write Data signals. The Write Enable line remaining high implies a read operation. Under these conditions, the FDD will transmit -Read Data signals to the controller. Some models of the FDD which contain a data separator will also transmit -Sep Clock and -Sep Data signals to the Controller.

Controller Step and Direction commands are received initiating a track seek operation on a selected FDD.

Positioning the carriage-mounted Read/Write heads is accomplished by a band-driven stepper motor. Each step command from the user system increments the stepper motor which, in turn, moves the band. The band increments the Read/Write heads one track position for each step command. The selected FDD transmits a Track 00 signal to the controller whenever the Read/Write heads are at Track 00.

77653520-A 4-1



MAY USE OPTIONAL ALTERNATE 1/D LINES
 OPTIONAL AVAILABLE IN SOME DRIVE CONFIGURATIONS
 GOILSA

FIGURE 4-1. FUNCTIONAL BLOCK DIAGRAM

A reading or writing operation begins by placing the Read/Write heads in contact with the diskette with a Head-Load command and at the desired track. To write on the diskette. Write Enable is sent by the controller to condition the write logic. The write current then in the head, reverses polarity synchronous with the high-to-low transitions of the Write-Data pulses from the controller. The current reversals cause magnetic flux reversals on the desired diskette track. Erasure of previously recorded data is simultaneously accomplished during the writing operation in addition to a delayed-tunnel-erase which ensures disk inter-changeability.

To read from the diskette, magnetized bits in the format of the pre-recorded data are sensed by the Read/Write heads. This signal is amplified, digitized and transmitted to the user system.

FUNCTIONAL DESCRIPTION 4.3

Refer to Figures 4-1, 5-2, and the Schematic Diagram (Section 5) for the following discussion.

The FDD is divided into the following major functional areas:

a. Control Logic

b. Write Logic

c. Stepper Control

d. Read Logic

e. Diskette Drive

f. Read/Write Head

Index g.

Door Lock h.

Drive Select

4.3.1 CONTROL LOGIC

The functions of the control logic are to generate the signals that: (a) establish the ready status of the FDD; (b) step the Read/Write heads in or out upon selection and command of the controller; (c) load the heads on the diskette for read/write operations; (d) protect the diskette from writing if the write-protect slot is present; (e) indicate when the Read/Write head is at Track 00; (f) generate the Index and Sector pulses when the diskette is rotating and the FDD is selected; (g) lock the FDD door latch; (h) unit selection of the FDD; (i) select head 0 or 1 for Read/Write operation; (j) indicate that the door has been open while the drive was not selected; (k) indicate single or two-sided diskette; (l) indicate visually that the drive has one or more functions performed by the Activity LED.

a. Drive Ready

This line is used to indicate to the FDD controller that the diskette is inserted correctly, the door is closed, and that two index pulses have been detected. This line is not inhibited by the select line within the drive. This line can be inhibited by Drive Sel at the ready output. Switches R, RR and DR may apply.

If a single-sided diskette is installed when switch DR is closed; READY will be active (logical zero) if head 0 is selected, but false (logical 1) if head 0 is selected, Conversly, if a two-sided diskette is installed, READY will be active when either side of the diskette is selected.

When switch DR is open and a single-sided diskette is inserted, head 0 or head 1 may be selected and READY will not be inhibited.

4-3 77653520-A

(1) Radial Ready

This option enables the user to monitor the Ready line of each drive on the interface. This can be useful in detecting when an operator has removed or installed a diskette in any drive. Normally, the Ready line from a drive is only available to the interface when it is selected.

Switch RR must be open on each FDD used on the interface when this option is used. Switch R may be closed on only one FDD on the interface: this FDD will have Ready on output pin 22. The remaining FDDs in the interface must utilize their own Ready lines, each using a different alternate I/O line. These outputs may be wire-wrapped or soldered to the appropriate staked pins with 30-gauge wire. (see Figure 5-4).

b. Power on Reset

At initial voltage application, comparator U18 generates a reset pulse of approximately 70 ms in length. This prevents the drive from writing during power on and resets the Index, In Use, Disk Change and Stepper Motor Logic.

c. Step and Direction

Each step command received causes the Read/Write heads to move with the direction of motion as defined by the Direction Select line.

The access motion is initiated on each logical zero-to-one transition, or the trailing edge of the signal pulse. Any change in the Direction Select line must be at least 1 us before the trailing edge of the step pulse. Refer to Figure 5-2 for these timings.

Step pulses are inhibited during a write operation and movement which would position the Read/Write heads behind Track 0.

d. Head Load and Door Closed

The Read/Write heads of a selected FDD can be loaded only when the disk is fully installed and the front-panel door is closed.

When the controller sends a Head-Load signal, the head-load solenoid is energized causing the load plate to actuate. The actuation of the load plate permits the head arms to load the heads against the diskette surface. The door-closed switch also is used to inhibit the READY signal when the door is opened.

(There are several different options when configuring head load (see Figure 5-3, sheet 1). Shunts C DD, A, D, X and B may apply).

e. Write Protect

The Write-Protect function is accomplished through use of an LED (light-emmiting diode) and a photo-transistor. These are mounted such that the presence of a Write Protect slot in the jacket of the diskette will cause pin 5 of U24 to be driven low. This signal is gated with Drive Select and Write Enable to inhibit writing on any diskette possessing a write-protect slot. Closing switch WP enables write inhibit. CAUTION. If switch WP is off, the drive can write on a protected diskette.

f. Track Zero

Track 00 signal is generated when the carriage-assembly tab is sensed by the Track 00 optical switch. Closing this switch causes U12 Pin 5 to switch high assisted by hysteresis. The output is gated with QAC and Drive Select to provide the Track 00 signal that is transmitted to the controller from U10 pin 11.

4-4 77653520-A

g. Index, Sector and Diskette-Type Circuitry

The beginning of each diskette track is indicated by an Index pulse. The diskette rotates between a light source (LED) and a sensor (photo transistor). When the Index hole in the diskette passes under the light source, light is detected by the sensor. The sensor output is amplified and transmitted to the controller as the Index pulse when the FDD is selected. The drive has two Index detectors, one for two-sided diskettes and one for single-sided diskettes. U16 determines the type of diskette involved. This signal is gated with Drive Select and sent to the interface by U25 pin 3 through switch 2S.

Two-sided and single-sided Index is gated at U11 pins 12 and 13 and is provided to U19 pin 12 for shaping.

An Index/Sector separator is provided on some models of the FDD. Proper operation of the Ready function requires that the Index pulses be separated in the FDD.

Whenever a 32-hole hard-sectored diskette is used, Index/Sector pulses arrive at 5.2 -ms intervals with one Index pulse nested between two sector pulses at 2.6 ms nominally. When using the hard-sector functions, Index and Sector pulse-output widths are 0.4 + 0.2 ms when switch E is closed.

When using the soft-sector function, the Index output-pulse width if 1.8 + 0.4 us. Switches SS, HS, RI, S and I may be applicable (see Figure 5-3, sheet 3).

h. Door Lock and In Use

The Door Lock circuit can be latched on under Drive Select control so that the door can remain locked without maintaining the active state of In Use. To implement this option, close switches D and DL. Then, if the appropriate Drive Select line is activated while In Use is active U28-9 will be set, which holds the door-lock circuit active. To unlock the door, Drive Select is activated again while In Use is inactive. This will reset U28-9.

The Door Lock may also be optioned such that it is only active while the In Use line is active. Switch D is closed; Switch DL is open.

i. Drive Select

The Drive Select function will inhibit command and status signals such as Index, Sector, Head Load, Write Data and Ready unless optioned otherwise. The position of the FDD in a daisy-chain configuration is determined by the activation of Switch 4. Switches DD and A apply. (see Figure 5-3, sheet 1).

4.3.2 WRITE LOGIC

A write operation begins with a Write Enable command from the controller when the FDD is selected. This command simultaneously enables the Write-Data switching drivers (flip flop U28 pins 5 and 6), the Write-Data gate U22 pin 6, blocks the input to the read circuit by reverse-biasing diodes in U3, and after a delay energizes the erase windings. Data applied to the Write-Data input alternately switches a constant write current through the write drivers to the head windings. Low-current operation used when writing on physical track 43 and greater, is selected by switching a shunt resistor R59 into the write-current source. Current source U13 provides current to the emitters of the write transistors U30. Switch LC applies.

4.3.3 HEAD-SELECT LOGIC

Head-Select signal when low selects head 1 by turning on U9 pin 7 causing its collector to be at +12 volts while U9 pin 1 is at ground. When the Head-Select signal goes high, it will cause U9 pin 7 to ground and U9 pin 1 to +12 volts selecting head 0.

In systems containing no more than two drives per controller, each Read/Write head can be assigned a separate drive address. In such cases, the four Drive Select lines can be used to select the four Read/Write heads. To implement this option, close switch S3 and properly set switch S5. For example, the first drive may have switch 4-1 and switch 5-3 closed while the second drive has switch 4-3 and switch 5-1 closed. With this jumper configuration installation, the four Drive Select lines have the following selection functions:

- 1. Drive Select 1 selects head 0 of first drive;
- 2. Drive Select 2 selects head 1 of first drive;
- 3. Drive Select 3 selects head 0 of second drive;
- 4. Drive Select 4 selects head 1 of second drive.

U13 and U9 pin 8 control the +12 voltage with respect to loss of +5 control voltage. Switch S2 is closed for this option. Head selection may be performed by the direction line if optioned by closing switch S1. When direction is low, head 1 is selected. When direction is high head 0 is selected.

(Refer to Figure 5-3, schematic sheets 2 and 3.)

4.3.4 DISK CHANGE

This customer-selectable option is enabled by closing switch DC. It will provide a true signal (logical zero) to the interface (pin 12) when Drive Select is activated, if while deselected the drive has gone from a Ready to a Not Ready (door open) condition. This line is a reset on the true to false transion of Drive Select if the drive has gone Ready. Timing of this line is illustrated in Figure 5-2. The circuitry is illustrated in Figure 5-3, schematic sheet 4. The output of flip-flop U7 pin 6, goes high when the door is opened, but output gate U10-6 is not enabled until the drive is selected. When the Drive Select line goes false, U7 pin 6 will be clocked high.

4.3.5 READ LOGIC

Read operation is enabled when the Read/Write heads are loaded on the diskette and Write Enable is not commanded. With Write Enable not commanded, the data blocking diodes U3 are forward-biased and data sensed by the Read/Write head is fed to the Read Data circuit. The read signal from the diskette is in the form of a sine wave.

This analog signal is amplified by U1, filtered, differentiated by C6/R11 and C7/R12 amplified by U2, and coupled to a comparator/logic circuit to detect zero crossings and reject noise in the differentiated read signal.

The out-of-phase comparators U5 pins 7 and 12 have rise and fall times whose differences are exaggerated by slow-down capacitor C27. This results in a narrow negative pulse at U11 pin 6 which triggers a one-microsecond retriggerable one-shot U15 pin 9.

4-6 77653520-A

Flip flop U7 pins 8 and 9 perform a noise-rejection function in that noise near the zero crossings of the amplified differentiated data only result in retriggering U15 pin 9. This appears as jitter in the clock for the flip flop whose data input, derived from redundant comparator U8 pin 12, has by that time stabilized.

Another slow-down capacitor, C32 causes a negative pulse to appear at the output of U11 pin 8 whenever the flip flop toggles. Although shifted in time by approximately the delay of one-shot U15 pin 9, each pulse corresponds to a zero crossing of the differentiated signal, and a peak of the analog read signal. Jitter at the flip-flop clock input and U9 pin 8, which is due to noise at the zero crossings, will not affect the 200-ns composite-data pulse width (see Figure 5-3, schematic sheet 4).

4.3.6 DISKETTE DRIVE

Diskette drive is accomplished by clamping the diskette between the cone assembly and belt-driven spindle. The spindle is rotated at 360 r/min by the diskette drive motor. A dual pulley permits 50- or 60-Hz operation without a motor change.

4.3.7 READ/WRITE HEADS

The Read/Write heads are in direct contact with the diskette during read or write operation. Head load is achieved by a solenoid-actuated load plate allowing the head arms to load the Read/Write heads against the diskette. The head surfaces are designed for maximum signal transfer to and from the magnetic surface of the diskette with minimum head/disk wear. The tunnel-erase gap DC-erases the intra-track area to improve offtrack signal-to-noise ratio and permit diskette interchange between drives.

4.4 CONTROL AND DATA LINE CHARACTERISTICS

All signal lines must be terminated at the receiver with a characteristic impedance of 150-ohms, typically. Transmission is by 26 AWG (min.), 150-ohm flat cable or twisted pair (one twist per inch) with a maximum line length of 25 feet. Figure 5-1 shows the timing of typical operations.

4.4.1 LOGIC LEVELS

The following definitions will be used throughout this manual:

low = Logic 1, Active State

Refers to the low-voltage condition

+0.4VDC Max.

high = Logic 0, Inactive State

Refers to the high-voltage condition +2.4VDC Min.

4.4.2 TRANSMITTER CHARACTERISTICS

The FDD uses the TTL 7438 (quad 2-input buffer or driver) or equivalent to transmit all control and data signals. This transmitter is capable of sinking a current of 48 ma with an output voltage of 0.4 volts. The host controller must provide the necessary pull-up resistor.

4.4.3 LINE-RECEIVER CHARACTERISTICS

The FDD uses SN7414 gates or equivalent for line receivers. The input of each

77653520-A 4-7

receiver is terminated in 150 ohms.

4.4.4 CONTROL AND DATA LINE FUNCTIONS

The signals that are exchanged are described in Table 4-2 and are shown relative to a point of origin in Figure 4-1.

INPUT LINES	ABLE 4-1. INPUT/OUTPUT LINES				
SIGNAL	FUNCTION				
-STEP	A 1- microsecond (minimum) logic 1 level pulse on this line causes the head to move one track as determined by the direction line.				
-DIRE CTION	A logic 1 level on this line and step pulse causes the head to move one track inward toward the center of the diskette. A logic - level on this line and step pulse causes the head to move one track outward from the center of the diskette. (Refer to paragraph 4.3.3, Head-Select Logic for further usage of the line).				
-HEAD LOAD (Alternate I/O)*	A logic 1 level on this line loads the heads against the diskette.				
-WRITE ENABLE	To enable the FDD write driver, this line is held at a logic 1.				
	To disable the FDD write driver and enable the FDD read circuitry, this line is held at logic zero.				
-WRITE DATA	This line contains the composite coded write clock and data information to the FDD.				
-LOW CURRENT (Alternate I/O)*	This line reduces write current for physical tracks 43 or greater. A logic 1 level reduces write current. If the FDD includes the Track 43 kit this line will not be applicable.				
-DRIVE SELECT (1 of 4 lines)	A logic 1 level on this line with switches DD, A, and one set of switch 1 contacts closed enables the FDD interface. (Refer to paragraph 4.3.3a, Head Selection, for further usage of these lines.)				
-IN USE (Alternate I/O)*	A logic 1 level on this line illuminates an LED indicator on the front panel of the FDD and activates a solenoid which locks the door-latch mechanism preventing opening of the door.				
-HEAD SELECT	A logic 1 level on this line selects head 0 (lower diskette surface). A logic 0 selects head 1.				

^{*}Alternate I/O Unassigned - Unused I/O pins 4, 6 and 8. These may be customer defined.

OUTPUT LINES

-WRITE PROTECT

-READY	A logic 1 level indicates that the door is closed, a diskette is rotating, and two Index pulses have been sensed. This output may be optioned to use an alternate I/O pin.
-INDEX	This line gives an indication of the rotational position of the diskette by outputting a logic 1 pulse for every Index hole of the diskette. This output may be configured to use an alternate I/O pin if desired.
-DISK CHANGE (Alternate I/O)*	This line gives indication that there was a loss of Ready from the Door Closed signal going false while the drive was not selected. The status of this output can only be monitored when the drive is selected.
-TRACK 00	A logic 1 level indicates that the head is positioned over Track 00.

-READ DATA	This line contains the unseparated data and clock information.
-TWO-SIDED	A logic 1 indicates a two-sided diskette and a logic 0 a
(Alternate I/O)*	single-sided diskette

Some models of the FDD contain Data/Clock and Index/Sector separators. For these models the following output lines are functional:

-SEPARATED DATA** This line contains the separated data information.

single-sided diskette.

diskette is uncovered.

-SEPARATED CLOCK**	This line contains the separated clock information.
-SECTOR	This line gives an indication of the rotational position of the

diskette by outputting a logic 1 pulse for every sector hole of the diskette. (For soft-sector configurations this line is inactive.) This output may be configured to use an alternate

Logic 1 level indicates that the write-protect slot on the

I/O pin.

77653520-A 4 - 9

^{*}Alternate I/O Unassigned - Unused I/O pins 4, 6, and 8. These may be customer defined.

^{**}The signals are valid when double-frequency recording without missing clock is used, and switch FS is closed. The signals are valid when double-frequency recording with missing clock is used and switch TS is closed.

4.5 CUSTOMER-SELECTABLE FEATURES

This section details the numerous customer-selectable features available. Standard and optional PWA configurations are presented in the following paragraphs and in Table 4-2.

Part numbers for switches are included below.

SWITCH	CDC PART NUMBER AMP PART NUMBER
S1 (10-position)	83462207
S2 (8-position)	83452205
S3 (8-position)	83452205
S4 (4-position)	83452201
S5 (4-position)	83452201

As shipped from the factory, the PWA's are configured as detailed in Table 4-2. The following is an alphabetical listing of each feature and its description.

- A allows gating of Drive Select with the Head-Load signal to create drive selection.
- B allows interactive gating of Drive Select and Head-Load. Without this feature there can be no interaction (gating) at the interface between these two signals.
- C brings the Head-Load signal from J1-18 to the Head-Load control logic. CC must be used with C. Also, CC must be off when C is off.
- CC brings the Head-Load signal from J1-18 to the Head-Load control logic. CC must be used with C. Also, CC must be off when C is off.
- D incorporates the In-Use input control signal on the interface (signal supplied by user's controller). Control of the door-lock solenoid, activity light and Head-Load solenoid can be affected by use of this feature.
- brings the interface the following information. Ready condition on the drive became inactive (false) either while the drive was selected or deselected. Drive Select must be strobed (toggled) to reset a diskette change "true" condition. It is assumed that the loss of Ready is due to the door on the drive being opened, thereby alerting the system operator to a possible diskette change in the drive.
- DD brings the Drive Select input into gating with control logic. Without this feature, all of the Drive Select inputs will be isolated from the FDD logic.
- DL allows the low-to-high transition of unit selection to act as a trigger for a D flip-flop. This output status of the flip-flop depends upon the logic status of the In-Use input line which controls the door-lock solenoid, activity LED and Head-Load solenoid to activation with drive deselection and reselection. This is dependent upon the status of the In-Use I/O line at the time of Drive Select (or reselect) with feature IU enabled.

- The Ready output from the drive will go false if a single-sided diskette is installed in the drive and head #1 is selected. This feature prevents using the wrong side of a single-sided diskette in a double-sided drive.
- reduces the pulse width of index/and/or sector pulses from 1.8 to 0.4 milliseconds. This feature is not present on all PWA configurations; it is only available with the Sector/Index feature.
- provides Separated Data and Separated Clock if a "missing clock" format is not being utilized with the FM only recording. This feature is not present on all PWA configurations; it is only available with the Sector/Index feature.
- HO allows control of the Head-Load solenoid via the Head-Load or Drive Select inputs on the interface.
- with a soft-sector sidkette installed, produces no Index pulses on the interface so no Ready signal will be generated; however it does produce one sector pulse on the sector output. With a 32-hole hard-sector diskette installed, produces separated sector/index at the interface at the designated locations with a 1.8-millisecond pulse width (true) on the sector and index outputs. If feature E is enabled along with HS, produces separated sector/index at the interface with a 0.4-millisecond pulse width (true) on the sector and index outputs. In most hard-sector applications, both features E and HS are installed. This feature is not present on all PWA configurations.
- I brings the Index signal to the drive interface at J1-20.
- IU allows control of the Head-Load solenoid via the In-Use signal on the interface after Drive Select strobing to "latch" activation. Feature DL must be applied for this latching of Head-Load solenoid will not deactivate.
- MM (multi-media) optimizes inner track write current for high resolution media.
- R brings Drive Ready to the interface at J1-22.
- RI gates Drive Select with Index and Sector. The Index and Sector status will be at the interface only while the drive is selected. (Sector is required at the interface only if hard-sector formatting is being utilized.)
- RR gates Drive Select with Ready. The Ready status will be at the interface only while the drive is selected.
- brings hard sector pulses to the interface at J1-24 if hard-sector formatting is being utilized.) This feature is not present on all PWA configurations.
- brings Index to the interface if a soft-sector diskette is installed. If a hard-sector diskette (32-hole) is installed, Index/Sector composite will be on the Index output line and Separated Sector will be on the Sector line if feature S is also implemented. Feature SS is not present on all PWA configurations.
- allows control of head selection by the Direction line input signal after having accessed the desired track (conditional).

77653520-A 4-11

- s2 allows control of head selection by the Head Select input via the interface at J 1-14.
- allows control of head selection by a Drive Select input line. This is a conditional configuration. Only two drives can be addressed on a four-drive daisy-chain system. The standard DIP switch configuration is as follows:

DRIVE	HEAD 0	HEAD 1
1	S4-1 closed. Drive select by J1-26.	S5-3 closed. Drive select by J1-28.
2	S4-3 closed. Drive select by J1-30.	S5-1 closed. Drive select by J1-32.

- TS provides Separated Data and Separated Clock outputs at the interface in the FM recording mode only (single density). The data separator will not lose sync when the IBM missing clock format is being utilized (not to be confused with MFM recording) as with Feature FS. Feature TS is not present on all PWA configurations.
- WP inhibits writing internally in the drive when a write protected diskette has been inserted in the drive. (I/O is notified.) Allows write if protected when off.
- x allows gating of the Head-Load signal with Drive Select to "create" the Head-Load signal.
- y allows control of the activity light by the Head-Load signal if In-Use is not being utilized.
- z allows control of the activity light by the Drive Select signal if In-Use is not being utilized.
- brings the status of the diskette in the drive to the interface at J1-10. This signal status indicates that either a single- or double-sided diskette is in the drive after two index holes have been sensed.
- LC allows interface pin 2 to switch the FDD write current to a lower level for improved read margins on physical tracks 43 through 77.

TABLE 4-2. CUSTOMER SELECTABLE FEATURES

		STANDARD PWA	OPTIONAL PWA
SWITCH		- CONFIGURATION	CONFIGURATION
A	Radial Head Load	X	
В	Radial Head Load	Х	
C	Alternate Input-Head Load		
CC	Alternate Input-Head Load		
D	Alternate Input-In-Use		
DC	Disk Change		
DD	Standard Drive Select Enable	X	
DL	Door Lock Latch		
DR	Double Side Ready		
E	0.4 ms Index Pulse	N/A	
FS	False Separation	N/A	
НО	Allow Head Load	X	
HS	Hard Sector Enable	N/A	
I	Index Output	x	
IU	Head Load With In-Use		
MM	Reduced Write Current		
R	Ready Output	X	
RI	Radial Index and Sector	X	
RR	Radial Ready	Х	
S	Sector Output	N/A	
SS	Soft Sector Enable	N/A	
S1	Side Select Using Direction Sele	ect	
S2	Standard Side Select Input	X	
S3	Side Select Using Drive Select		
TS	True Separation	N/A	
WP	Inhibit Write When Write Protection	cted X	
X	Radial Head Load	Х	
Y	In Use From Head Load		
Z	In Use From Drive Select	X	
2S	Two-Sided Status Output		
LC	Low Current	х	

N/A = Not available on Standard PWA

4.6 ALTERNATE I/O

The Model 9406-4 Flexible Disk Drive can be modified by the user to function differently than the standard method described in paragraph 4.6 and listed in Table 4-2. This paragraph will describe how to achieve alternate functions.

4.6.1 RADIAL READY

This alternate function enables the user to monitor the ready line of the interface of each drive in a radial configuration. The normal function of the drive is to make the ready line available on the interface only when the drive has been selected. When 2, 3, or 4 drives are connected in a radial configuration, the "Radial Ready" function will be available when the drives are modified as follows:

Drive 1

No modifications are required if the user is satisfied with the Radial Ready signal on pin 22 of J1.

Drive 2

- 1. Open RR (Open SW1-5).
- 2. Open R (Open SW1-7).
- 3. With a wire wrap jumper, connect alternate I/O Pin 10 (Figure 4-3) to I/O pin #1. The ready line will now be on pin 12 of J1. Pin 10 can be jumpered to any unused 1 through 5 alternate I/O pin if the user desires.

Drive 3

- 1. Open RR (Open SW1-5).
- 2. Open R (Open SW1-7),
- 3. With a wire wrap jumper, connect alternate I/O Pin 10 (Figure 4-3) to I/O pin #2. The ready line will now be on pin 10 of J1. Pin 10 can be jumpered to any unused 1 through 5 alternate I/O pin if the user desires.

Drive 4

- 1. Open RR (Open SW1-5).
- 2. Open R (Open SW1-7).
- 3. With a wire wrap jumper, connect alternate I/O pin 10 (Figure 4-3) to I/O pin #3. The ready line will now be on pin 8 of J1. Pin 10 can be jumpered to any unused 1 through 5 alternate I/O pin if the user desires.

4.6.2 RADIAL INDEX

This alternative function enables the user to monitor the index line of each drive so that the drive can be selected just prior to the index. When 2, 3, or 4 drives are connected in a radial configuration, the index signal will be available at the interface when the drives are modified as follows:

Drive 1

No modifications are required if the user is satisfied with the Radial Index signal on pin 20 of J1.

Drive 2

- Open RI (Open SW1-8).
- 2. Open I (Open SW1-6).
- 3. With a wire wrap jumper, connect alternate I/O pin 9 (Figure 4-3) to I/O pin #1. The index signal will now be on pin 12 of J1. Pin 9 can be jumpered to any unused 1 through 5 alternate I/O pin if the user desires.

Drive 3

- 1. Open RI (Open SW1-8).
- 2. Open I (Open SW1-6).
- 3. With a wire wrap jumper, connect alternate I/O pin 9 (Figure 4-3) to I/O pin #2. The index signal will now be on pin 10 of J1. Pin 9 can be jumpered to any unused 1 through 5 alternate I/O pin if the user desires.

Drive 4

- 1. Open RI (Open SW1-8).
- 2. Open I (Open SW1-6).
- 3. With a wire wrap jumper, connect alternate I/O pin 9 (Figure 4-3) to I/O pin #3. The index signal will now be on pin 8 of J1. Pin 9 can be jumpered to any unused 1 through 5 alternate I/O pin if the user desires.

4.6.3 RADIAL HEAD LOAD

This alternative function enables the user to load the heads without a unit select signal (i.e., the heads can be loaded without the drive being selected). When 2, 3, or 4 drives are connected in a radial configuration, the heads of any drive can be loaded when desired by modifying the drives as follows:

Drive 1

No modifications are required if the user is satisfied with the Radial Head Load Command on pin 18 of J1.

Drive 2

- 1. Open C (Open SW2-8).
- 2. With a wire wrap jumper, connect alternate I/O pin 6 (Figure 4-3) to I/O pin #1. The head load command will be applied on pin 12 of J1. Pin 6 can be jumpered to any unused 1 through 5 alternate I/O pin if the user desires.

Drive 3

- 1. Open C (Open SW2-8).
- With a wire wrap jumper, connect alternate I/O pin 6 (Figure 4-3) to I/O pin #2. The head load command will be applied on pin 10 of J1. Pin 6 can be jumpered to any unused 1 through 5 alternate I/O pin if the user desires.

Drive 4

- 1. Open C (Open SW2-8).
- 2. With a wire wrap jumper, connect alternate I/O pin 6 (Figure 4-3) to I/O pin #3. The head load command will be applied on pin 8 of J1. Pin 6 can be jumpered to any unused 1 through 5 alternate I/O pin if the user desires.

77653520-D 4-15/4-16

5.1 INTRODUCTION

This section contains the printed-circuit-board documentation and related timing

Figure 5.1 shows interface connections of all AC, DC and I/O lines applicable to the FDD.

Figure 5-2 shows timing diagrams which illustrate signal /time relationships during read, write, step-in and step-out operations. Figure 5-3 is the printed-circuit board schematic and Figure 5-4 contains the assembly drawing.

FOO CONTROLLER	FLAT RIBBON OR TWISTED PAIR (MAX 25 FT)	FDD
	TWO SIDED *	109
i -	DISK CHANGE .	12
ļ -	HEAD SELECT	14 13
! 	IN USE ◆	16
l +	HEAD LOAD ◆	18 17
!	INDEX	
1 —	READY	20 19
│	-	22 21
l	SECTOR ● *	24 23
	DRIVE SELECT 1 (HEAD SELECT OPT)	2625
	DRIVE SELECT 2 (HEAD SECECT OPT)	28 27
	ORIVE SELECT 3 (HEAD SELECT OPT)	30 29
	ORIVE SELECT 4 (HEAD SELECT OPT)	32
	DIRECTION SELECT (HEAD SELECT OPT)	34 33
	STEP-	36 35
I +	WRITE DATA	38 37
 	WRITE GATE	
ļ 	TRACK 00	40 39
ļ	WRITE PROTECT	42 41 41
ĺ		44 43
ļ Ļ	READ DATA	46 45
	SEPARATED DATA	48 47
	SEPARATED CLDCK **	5049
		J5
	+5 VDC	§ ~ 6 →
ļ	+24 V0C	4 3
	+24 V RETURN	1 2
	AC INPUT	J4
1	FRAME GROUND AC INPUT	1 2 3
†	ACTIVED!	d 3
- 75 Mari	<pre># re alternate input/output lines and they are el</pre>	ashlad by should

- These lines are alternate input/output lines and they are enabled by shunts.
 Not shown are pins 2, 4, 6 and 8 which are alternate 1/0 pins.
 Universal Configurations only.

(Z203a)

* Reference Section 4 for uses of these lines.

FIGURE 5-1. INTERFACE CONNECTIONS

TABLE 5.1. PWA ASSEMBLIES

			AN NOSEMBLIES
HPC 77618	PWA ASSY. 7768 8 8 3 1 1 5 0 4 0 0 0 0	HPC 77618	PWA ASSY. 7768
101 102 103 104 105	X X X X		
106 107 108 109 110	X X X X		
111 112 113 114 115	X X X X X		
116 117 118 119 120	X X X		
119 120 121 122 123 124 125 126	X X X X X X		

77653520-D 5-1.1

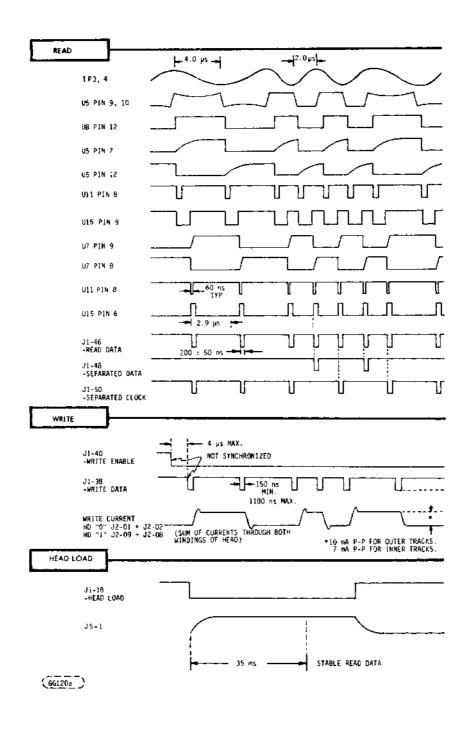


FIGURE 5-2A. TIMING (SHEET 1 OF 2)

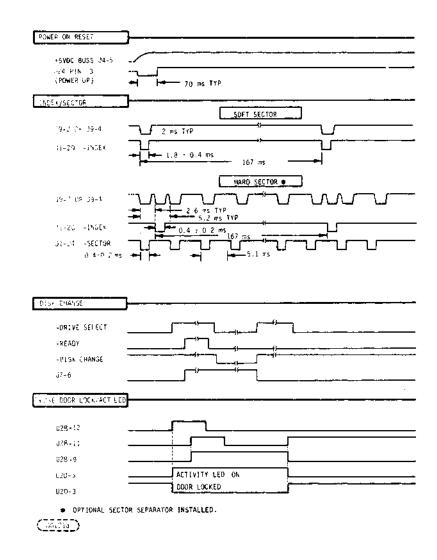


FIGURE 5-2A. TIMING (SHEET 2 OF 2)

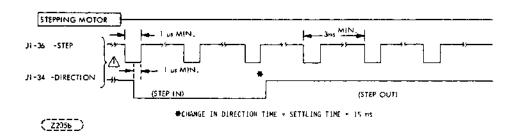
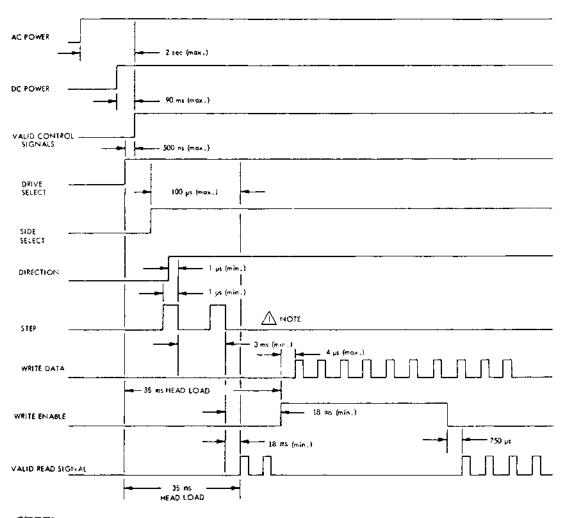


FIGURE 5-2B. STEP/DIRECTION OPERATION

5-4 77653520-A



A MINIMUM 18-MILLISECOND DELAY IS REQUIRED BETWEEN STEP PULSES.

IF A DIRECTION CHANGE HAS TAKEN PLACE AND NO READ/WRITE OPERATION NAS PERFORMED.

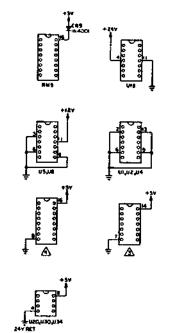
1 A minimum 18-millisecond delay is required between step pulses, if a direction change has taken place and no read/write operation was performed.

FIGURE 5-2E. GENERAL CONTROL AND DATA TIMING COMBINED

FI GURE

(SOFT-SECTOR CONFIGURATION)

#4-CI >> 424Y 7811(2 5-3. SCHEMATICS + C62 #4-63 , BAY RET 24-05,, ±31 (SHEET 1 OF 9) 71-01 THMU 11-49 COD DIEN



MOTES: (UNLESS OTHERWISE SPECIFIED)

1. RESISTOR VALUES ARE IN DIMES (MW. SM.
2. CAPACTION VALUES ARE IN WICROLARAOS

3. TYPICAL IN PIN IC INSTALLATION

4. TYPICAL IN PIN IC INSTALLATION

5. SEE TABLE A FOR COMPONENT VALUE

SIGNAL NAME	ensem
~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	SHEET
( PGR ) +24V	•
+READY +UNIT-SEL	7
+UNIT-SEL	4
+2-5:DED -Direction	, , , , , , , , , , , , , , , , , , ,
-DISK-CHANGE	4
-DOOR-LOCK -DOOR-LOCK	•
-DRIVE-SEL-6	i
-DRIVE-\$EL-2 -DRIVE-SEL-3	
-DRIVE-SEL-4	7
-HEAD-LOAD -HEAD-SEL	i
-IN-USE	5
-INDEX -LOW-CURRENT	7
-F 6R	•
-READ -READ	<b>6</b> 5
-read-Disable	9
-READY -SINGLE-SIDED- <b>E-NDI-SEL</b>	7 7
-SINGLE-SIDED-AND-HD1-SE	\$
-STEP -TAKO	6
-UNIT-SEL	i
-WRITE-PROTECT -WRT-DATA	•
CT-ERASE+0	9
CT-ERASE+O CT-ERASE+1	•
CT-ERASE+I	Š
CT-ERASE+1 Dogr-Closed	* 7
ERASE+0	á
ERASE-1 MD+1-SEL	•
HD-SEL(DIR)	:
HD-SEL(DIR) HD-SEL(UMI <b>T-SE</b> L)	•
HEAD-SEL(UNIT)	•
PULL-UP-1 PULL-UP-1	\$ 4
R/W+Q	i
A/Y+1 A/Y-0	•
R/V-1	
MEAD READ	8 5
READ-DATA	\$
SHIELD-O SHIELD-I	•
U28-08	4 5 5
US-07 US-12	5 \$
AB-05	;

I/O PIN	SHEET	SIGNAL NAME
J102 :  J110 0  J112 0  J114 1  J118 1  J118 1  J120 0  J122 6  J128 1  J136 1  J134 2  J134 3  J134 3  J134 3  J134 6  J144 6  J201 0  J202 0  J203 6  J204 0  J205 0  J207 0  J208 0	SHEET  7 4 9 5 4 4 4 4 4 6 8 6 8 6 8 8 8 8	SIGNAL NAME  -LGW-CURRENT -2-SIDED -DISK-CHANGE -HEAD-SEL -IN-USE -MEAD-LGAD -INDEX -READY -DRIVE-SEL-1 -DRIVE-SEL-2 -DRIVE-SEL-2 -DRIVE-SEL-4 -WRT-DATA -DIRECTION -STEP -TRKO -WRITE-PROTECT READ-DATA R/W+O SNIELD-2 CT-ERASE+D ERASE+D ERASE+D
J209 6 J210 6 J211 8 J212 6 J214 6 J308 8 J140 1	8 8 8 8 9	R/W+1 CT-ERASE+1 CT-ERASE+1 SHIELO+1 ERASE-7 +24V WRT-ENABLE

FIGURE 5-3. SCHEMATIC (SHEETS 2/3 OF 9)

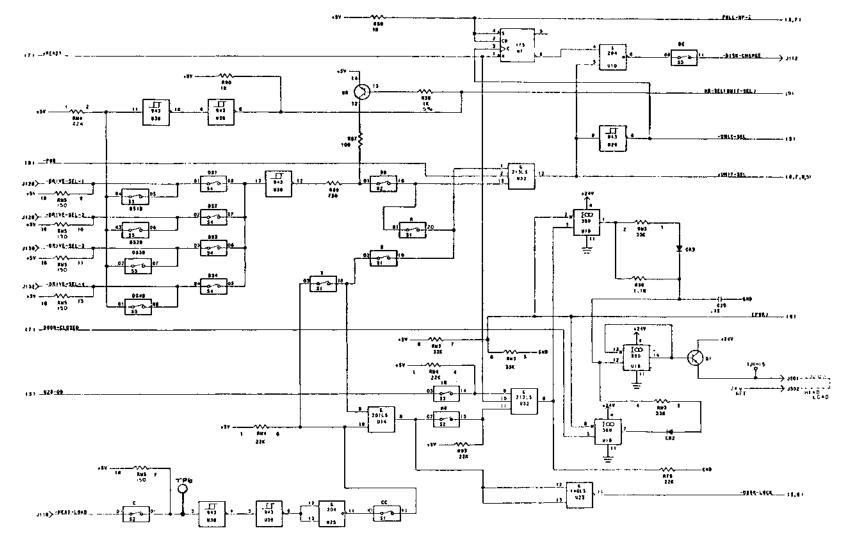
77653520**-**D 5-7

FIGURE 5-3. SCHEMATIC (SHEET 4

얶

9)

77653520-D



4

FIGURE 5-3, SCHEMATIC (SHEET Ų, 유 9

FIGURE

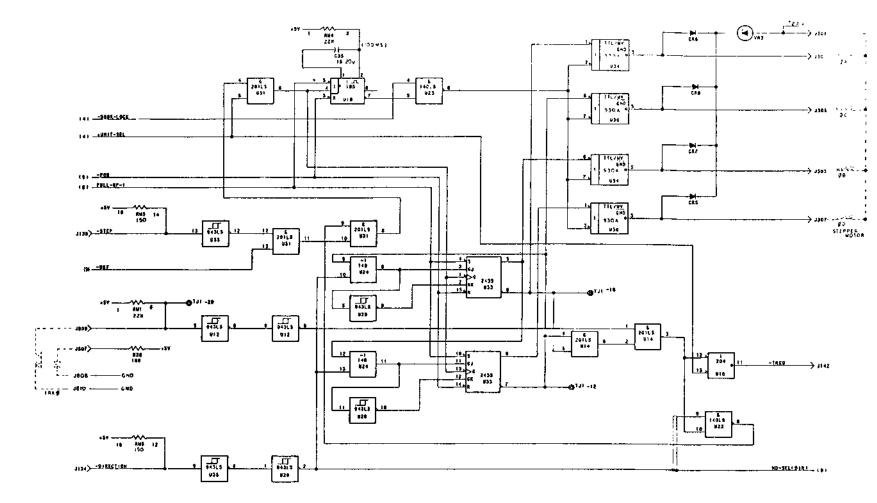
5-3.

SCHEMATIC (SHEET

σ

of 9)





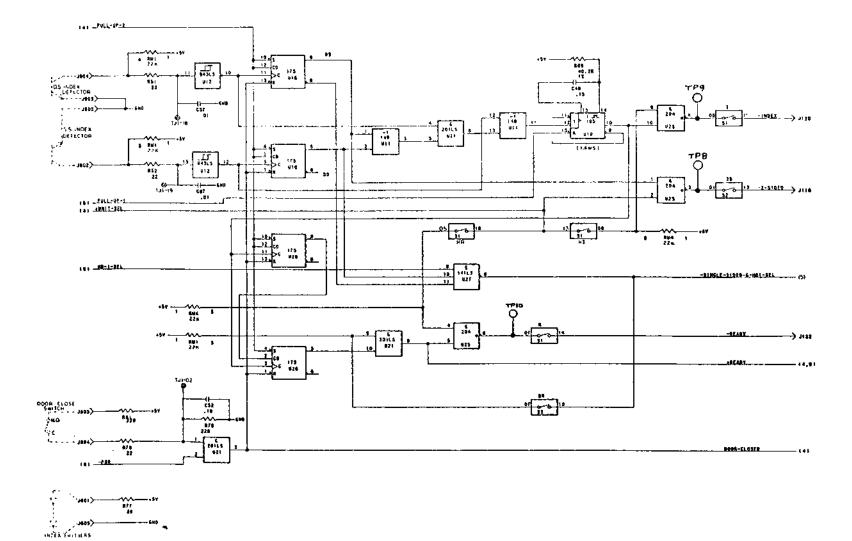


FIGURE 5-3. SCHEMATIC (SHEET 7 OF 9)

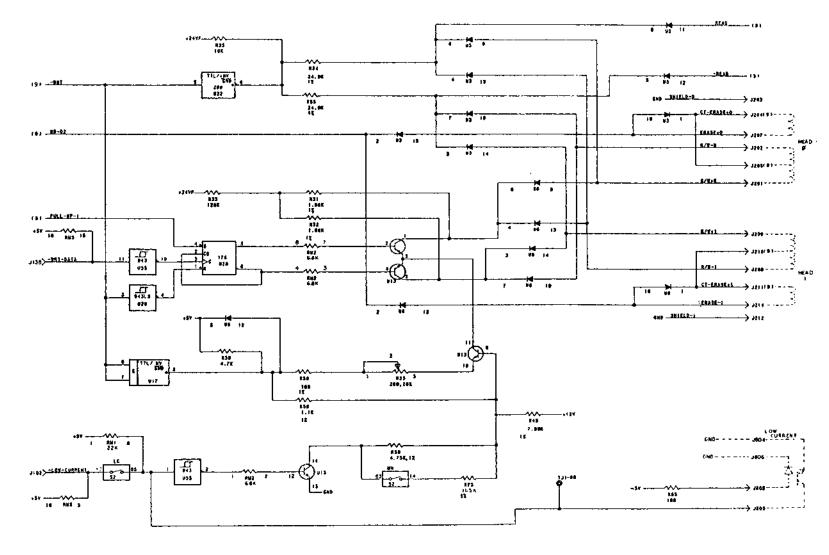


FIGURE 5-3. SCHEMATIC (SHEET 8 OF 9)

77653520-A

**@** 

FIGURE 5-3. SCHEMATIC (SHEET 9 OF 9)

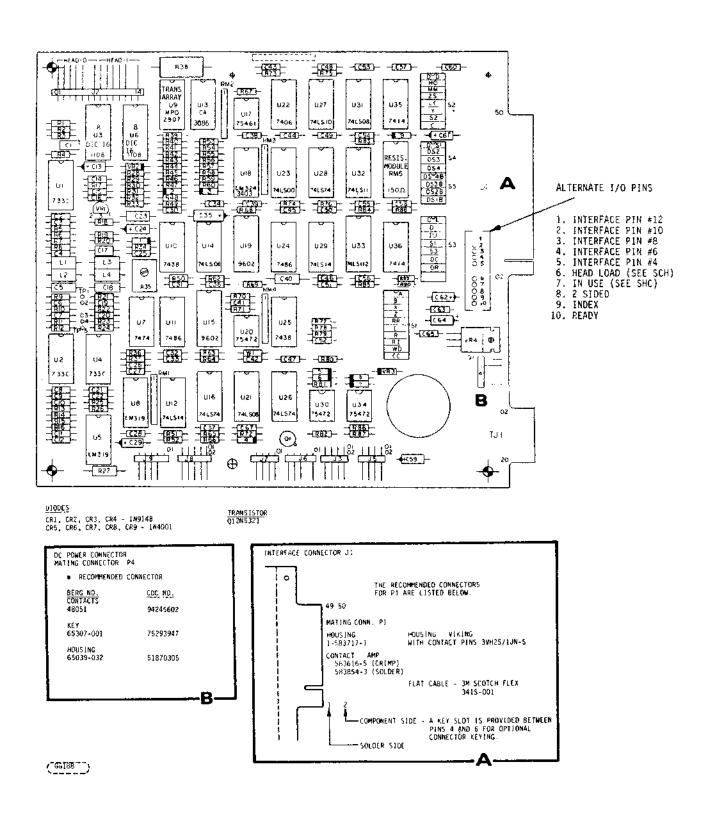


FIGURE 5-4. ASSEMBLY, INTERFACE AND DC POWER MATING CONNECTORS

## 6.1 INTRODUCTION

This section contains the instructions required to maintain the FDD. The information is provided in the form of preventive maintenance, troubleshooting and corrective maintenance.

## 6.2 MAINTENANCE TOOLS

The Special tools (or equivalent) required to maintain an FDD are as follows:

DESCRIPTION	CDC MODEL NO.		
Alignment Diskette (Single-Side)	421-51W*		
Alignment Diskette (Two-sided)	425-51W*		

## 6.3 TROUBLESHOOTING

An improperly adjusted FDD may exhibit symptoms of one that has a malfunction; therefore, the Adjustment Procedures (paragraph 6.4) should be performed before assuming that the drive has failed.

TABLE 6-1. ADJUSTMENT REFERENCE Adjustment Paragraph No. Adjustment Identification 6.4.1 Write-Splice Check and Adjustment 6.4.2Actuator Alignment 6.4.3 Clamshell-Closed Switch Adjustment 6.4.4 Track 00 Stop Adjustment 6.4.5 Diskette Ejector Adjustment 6.4.6 Diskette Load-Pad Adjustment 6.4.7 Head-Unload Clearance Adjustment 6.4.8 Low-Current-Switch Optical-Sensor Adjustment 6.4.9 Azimuth Adjustment

^{*}Available through local CDC sales office or distributor.

## 6.3.1 DC VOLTAGE AND SIGNAL CHECK

- a. Input power should be +5VDC ±5% and +24VDC ±10% measured at the input to the FDD (refer to paragraph 3.4.2).
- b. Test Points: The signals at the test points should conform to the various diagrams and waveforms as listed in Table 6.2.
- 3. Signals should conform to Figure 5-1 and Figure 6-1 through 6-3.

TABLE 6-2. TEST POINTS

Test Point No.	Refer to Fig. No.	Comments
3 4 1 2 5	5-2, 6-2, 6-3 5-2, 6-2, 6-3	Analog Read Data High Resolution (Differential) Analog Read Data Low Resolution (Differential) Ground
TJ-2 TJ-4 TJ-8 TJ-12 TJ-16 TJ-18 TJ-20 TJ-11 TJ-15 TJ-17 TJ-19		Door Close Write Protect Trk 43 Sensor Phase C Phase A D. S. Index Sensor Trk O Sensor Head-Load Output LED Driver Door Lock Driver SS Index Sensor

6-2 77653520-A

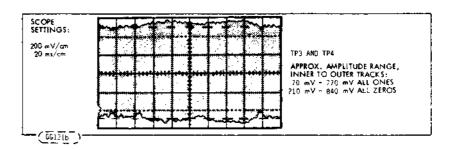


FIGURE 6-1. DIFFERENTIAL READ SIGNAL FOR ENTIRE TRACK

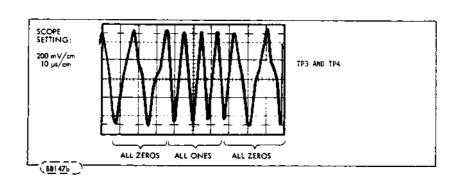


FIGURE 6-2. DIFFERENTIAL READ SIGNAL FOR PORTION OF OUTER TRACK

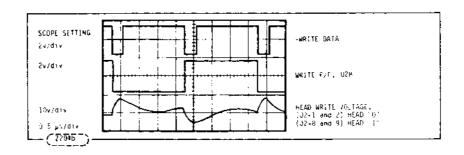


FIGURE 6-3 WRITE DATA, WRITE F/F OUTPUT, AND HEAD WRITE VOLTAGE FOR OUTER TRACK

## 6.4 ADJUSTMENT PROCEDURES

## 6.4.1 WRITE-SPLICE CHECK AND ADJUSTMENT

Alignment Diskette 421-51W and 425-51W are used to perform this procedure.

- a. Precondition the alignment diskette by allowing it to reach room temperature for one hour.
- b. Install the alignment diskette.

## CAUTION

The Alignment Diskette is for read only. Extreme caution should be used to assure this diskette is not written on.

- c. Seek to Track 00, then seek to Track 01 and Read on head 0. (No data is recorded on Track 1.)
- d. Connect Channel 2 of scope to TP3 on the PWA, Channel 1 to Index J1-20 of the PWA. Set up the scope as follows:

Chan 2 Volt/Div to: 0.1 volt/div Chan 1 Volt/Div to: 2 volt/div

Chan 2 voltage to: AC Source to: Chan 1 Chan 1 voltage to: DC Coupling to: Low Freq.

(High Freq. Reject)

Vert. Mode to: Add Trig Mode to: Channel 1
Slope (Sync) to: Pos Time Base to: 50 µs/div

- e. Adjust the time of the write-splice bit until it measures per 200 us ± 100 us. Refer to Figure 6-4a to adjust the time, loosen the single-sided sensor set screw holding the (single-sided-sensor) phototransistor located on the bottom of the chassis toward the front of the unit, (Figure 6-4b). Using the adjustment tab protruding through the casting, move the phototransistor until the specification is met. Tighten the set screw while observing the scope signal. Verify that the adjustment did not change.
- f. All scope settings are to remain as defined in the original setup in Step 1, but it may be necessary to slightly adjust the sync. Seek to Track 00 then seek to Track 01 and perform a read. While observing the signal on the scope, remove and reinsert the diskette three times.

After each insertion, verify that the change in the time from Index to write splice is less than 50 µs.

Repeat Steps b through f using Alignment Diskette 425-51W for the two-sided sensor adjustment tab and its associated set screw, as required.

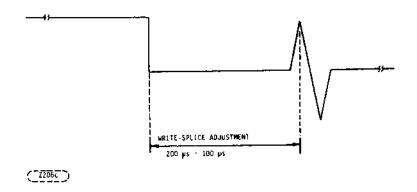


FIGURE 6-4A. WRITE-SPLICE-TIMING

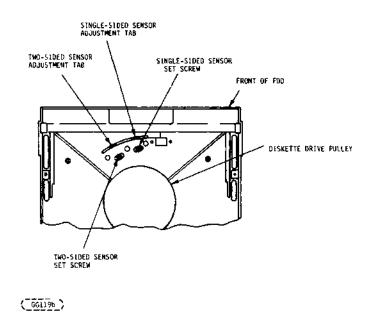


FIGURE 6-4B, SINGLE-AND TWO-SIDED SENSOR ADJUSTMENT MEANS

## 6.4.2 ACTUATOR ALIGNMENT

- a. The alignment diskette shall be preconditioned by allowing it to reach room temperature for one hour.
- b. Install the alignment diskette.

CAUTION

The Alignment Diskette is for read only. Extreme caution should be used to assure this diskette is not written on.

- c. Connect Channel 1 of scope to TP3 on the PWA and Channel 2 to TP4 on the PWA.
- d. Connect the external sync probe to index at J1-20 on PWA.
- e. Set up the scope as follows:

Channel 1: volts/div to:

0.1 volts/div

Channel 2: volts/div to:

0.1 volts/div (Inverted)

Channel 1: input to:

AC

Channel 2: inputs to:

AC

Vertical Mode to: Add Slope (Sync) to: Negative Trigger Source to: External

Trigger Coupling to: Low Frequency (High Frequency Reject)

Trigger Mode to: Normal Time Base to: 20 ms/div

- f. Apply DC power to the drive.
- g. Step to Track 38 (00100110) and perform a read on head 0.

#### NOTE

The trigger level is adjusted for repetitive display of data "Cateyes" consisting of two lobes (refer to Figure 6-6).

h. Change the volts/div of Channel 1 and Channel 2 to 0.02 volts/div. For an acceptable aligned unit, the voltage ratio of the smaller lobe to the larger lobe should exceed 80%.

6-6 77653520-A

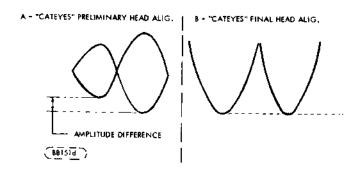


FIGURE 6-5. HEAD-ALIGNMENT AMPLITUDE

- i. If not in alignment, slightly loosen the stepper-motor mounting screws, (see Figure 6-9), slowly rotate the stepper motor until the amplitudes of both lobes are the same, and tighten the hardware. If there is insufficient adjustment range of the motor, perform the following:
  - i. Return the carriage to Track 0 and remove DC power.
  - ii. Loosen the stepper-motor hardware and rotate the stepper motor to each end of its adjustment range. leaving it approximately centered. Snug the hardware.
  - iii. Ensure that the pulley clamping screw is loose.
  - iv. With the probes and scope set per Steps c and d above, apply DC power.
  - v. Reading with Head 0, move the carriage back and forth in the vicinity of Track 0 to maximize the signal obtained (ensure that the pulley is slipping relative to the motor shaft). Tighten the pulley clamp hardware carefully so as not to move the carriage off Track 0.
  - vi. Seek to Track 38, again reading on Head 0.
  - vii. Using the adjustment tool, slowly rotate the motor until the amplitudes of both lobes is the same, and tighten the stepper-motor hardware.
- j. Return to Track 0, then seek back to Track 38. Verify the adjustment. If the specification is not met, readjust the stepper motor, return to zero and seek back to Track 38. Repeat the adjustment until the specification is met.
- k. Perform Track 0 Stop Adjustment.
- 1. Remove alignment diskette.

## 6.4.3 CLAMSHELL-CLOSED SWITCH ADJUSTMENT

Close the clamshell and check that it is latched. Turn the setscrew clockwise until the switch makes contact. Turn the setscrew one additional turn and a half. Open

77653520-A 6-7

and close the clamshell several times while observing the door-closed signal.

## 6.4.4 TRACK O STOP ADJUSTMENT

- a. After applying DC power to the drive, return the carriage to Track 0.
- b. Loosen Track 0 stop/cover hardware and slide the stop to the rear of the adjustment slot in the motor adapter.
- c. Place a 0.030-in. (0.76mm) shim through the adjustment slot and between the cover stop and the carriage stop.
- d. Slide the cover stop forward until contact is made with the 0.030-in. (0.76mm) shim, and tighten the hardware.
- e. After adjustment, the gap between the cover stop and the carriage stop should be greater than 0.20 in. (0.51mm) and less than 0.035 in. (0.89mm).

## 6.4.5 DISKETTE EJECTOR

Insert a diskette fully and note a clicking noise as the ejector engages a pin on the clamshell.

While observing the ejector, latch and latch block (Figure 6-6) through the 1/2 in. (12.7mm) hole in the sidewall, close the clamshell. Note that closing the clamshell moved the ejector further to the rear allowing the latch to rotate counterclockwise until the tip drops over the step in the latch block.

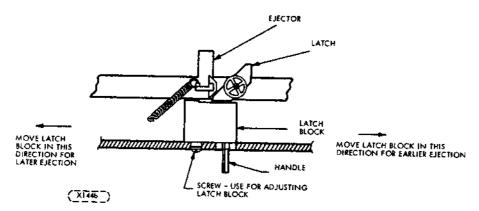


FIGURE 6-6. EJECTOR, LATCH AND LATCH BLOCK

With the clamshell closed, adjust the latch block (Figure 6-6) so the tip of the latch just clears the step.

Check by opening the clamshell slowly and observing the clamshell position when the diskette is ejected. To avoid damage, it is to be ejected when the clamshell is 1/4 in. (6.35mm) max. from the fully opened position. If further adjustments is required, move the latch block as indicated by the arrows and instruction in Figure 6-6.

Operate several times and observe that the diskette ejection is within the 1/4 in. (6.35 mm) max, described above.

6-8 77653520-A

## 6.4.6 DISKETTE-LOAD-PAD ADJUSTMENT

- a. Refer to Figure 6-7.
- b. Energize Solenoid
- c. Loosen Solenoid mounting screws (2x).
- d. Move solenoid down on bracket to obtain a clearance of 0.010 to 0.015 in. (0.254 to 0.381 mm) between the load plate and the lift extension of the upper-head arm at the location of minimum clearance. Move the carriage through its full travel manually to determine the location of minimum clearance.

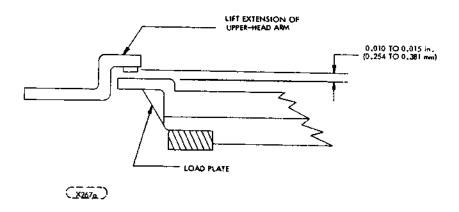
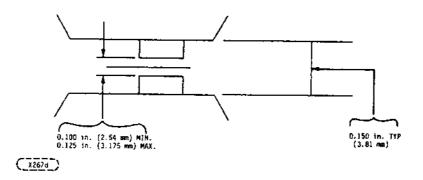


FIGURE 6-7. LOAD PAD ADJUSTMENT

## 6.4.7 HEAD-UNLOAD CLEARANCE

Adjust set screw on clamshell for 0.100 in. to 0.125 in. (2.54 to 3.175 mm) clearance per Figure 6-8 between flyer pads with head-load solenoid de-energized and clamshell closed.



# FIGURE 6-8. HEAD UNLOAD CLEARANCE AS VIEWED FROM THE FRONT OF THE CARRIAGE

## 6.4.8 LOW CURRENT SWITCH OPTICAL-SENSOR ADJUSTMENT

Some models may contain an internal Track 43 switch.

Verify adjustment 6.4.2 before beginning this adjustment.

Adjust the Low-Current-Switch optical sensor (on top of Track "0" bracket) for the proper output when positioned between Physical Tracks 42 or less and Physical Track 43 or greater.

a. Set up the scope as follows:

Channel Probe: TJ-14 or J8-2

Channel 1: volts/div to 1 V/div (0.1/div for X 10 Probe)

Channel 1: input to DC
Vertical Mode to: Channel 1
Scope (sync) to: Positive
Trigger Source to: Internal
Trigger Coupling to: DC
Trigger Mode to: Auto
Time Base to: 20 ms/div

- b. Perform a seek to Physical Track 42.
- c. Adjust the optical sensor for +2.4 V min.
- d. Perform a seek to Physical Track 43.

6-10 77653520-A

- e. Verify the scope reads +0.5 V max.
- Repeat b, c and d if necessary until the DC levels in Steps c and e are met.

## 6.4.9 AZIMUTH ADJUSTMENT

Using an alignment diskette, seek to Track 76 and adjust azimuth by turning the azimuth set screw in the guide-rod boss. The set screw should be adjusted in such a way that the azimuth pattern is optimized between head "0" and head "1." See Figure 6-10.1 for optimum azimuth alignment. The azimuth of both heads must be less than ±12 minutes from nominal.

## 6.5 REMOVAL AND REPLACEMENT PROCEDURES

The following procedures give the proper sequence for removal and replacement of major assemblies. To avoid damage to parts, the procedure must be performed in sequence.

## 6.5.1 PRINTED-CIRCUIT BOARD (PWA)

- a. Disconnect I/O Cable from J1 (refer to Figure 5-3).
- b. Disconnect harnesses from connectors on printed-circuit board.
- c. Remove screw from printed-circuit board adjacent to connector J1.
- d. Remove PWA by detaching it from the push-in clips.
- e. To replace printed-circuit board, push clips through printed-circuit board.
- f. Replace screw adjacent to connector J1.
- g. Reconnect harness and I/O cable.
- h. Set dipswitches.
- i. Perform write-splice check and adjust as necessary (par. 6.4.1).

## 6.5.2 CARRIAGE REPLACEMENT

Refer to Figures 6-9 and 6-10.

- a. Remove clamshell and front panel.
- b. Disconnect head and stepper-motor cables from PWA.
- c. Remove head cables from wire guide. Remove Track "0" cover/stop.
- d. Loosen hardware securing pulley to stepper-motor shaft.
- e. Remove hardware securing stepper motor to motor adapter.
- f. Hold pulley and carefully remove stepper motor from pulley and adapter.
- g. Slide carriage out (to approximately Track 0), and <u>loosen</u> guide bar clamp screw closest to spindle.
- h. Slide carriage in (to approximately Track 76), and <u>remove</u> the other guide bar clamp screw.
- i. Carefully remove the carriage, pulley and guide bar.
- j. After removing guide bar from carriage, unhook band spring from pin on carriage and remove spring.
- k. Remove pulley clip and screw from pulley.
- 1. Remove band clip and nut from end of carriage.
- m. Reverse above procedure to install new carriage, except:
  - i. Replace band with new band assembly;
  - ii. Leave pulley and band clips loose before installing band spring;
  - iii. After band spring is installed, rotate pulley the length of the foam pad to verify proper alignment. A misaligned pulley can be detected by either hearing a scraping or scratching noise, or a careful examination

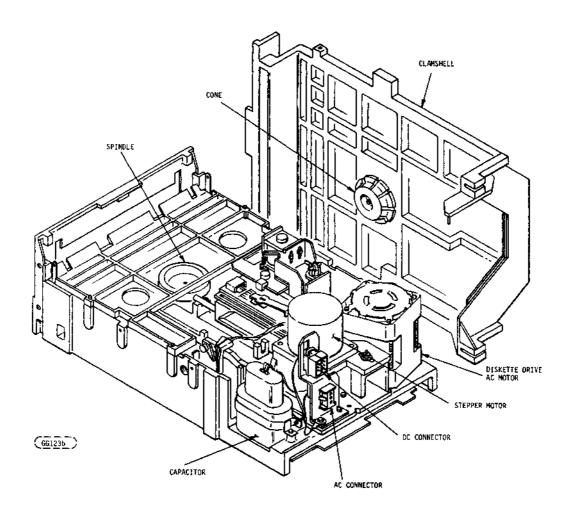


FIGURE 6-9. POSITIONING AND HEAD-LOAD MECHANISM, CLAMSHELL COVER RAISED.

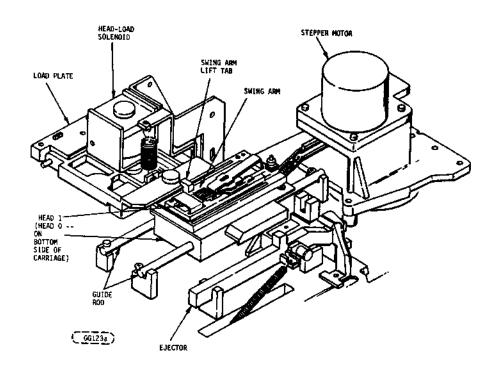


FIGURE 6-10. HEAD-LOAD CARRIAGE AND STEPPER MOTOR DETAILS

776535**2**0-A 6-13

#### 6.5.2 -contd.

of the gap between the split portion and the inner solid portion of the band. If the pulley is not exactly aligned, insert a small tool (screwdriver), into the spring hook closest to the band and apply sufficient force to relieve tension on the band. While holding the spring with hand, twist the pulley relative to the band in an effort to realign the band and pulley, and release the spring. Check for exact alignment. Repeat this procedure until the band and pulley are exactly aligned, and then tighten band and pulley clip hardware.

- iv. After carefully reinstalling the carriage assembly (ensuring that the swing arm tab is positioned above the load plate), and securing the guide bar, place the drive in a vertical position (motor up) so that the pulley is below the tail end of the carriage. At this point, check that the pulley is still properly aligned. If the pulley is misaligned, the carriage must be removed and the alignment procedure repeated. After ensuring the alignment is correct, with one hand position and hold the pulley in the approximate center of the locating hole in the motor adapter and carefully slide the motor shaft through the pulley bore, and seat the motor in the motor adapter. Use at least one screw at this point to hold the motor in place, ensuring that the screw is centered in the motor mount hole to facilitate later adjustment. Run the carriage back and forth by hand a few times so that the pulley is oriented properly on the shaft, and resume reassembly.
- n. Perform the Actuator Alignment (6.4.2) after completing mechanical assembly.
- o. Reinstall Track cover/stop and perform Track 0 Stop Adjustment (6.4.4).
- p. Perform azimuth adjustment (6.4.9).

## 6.5.3 DRIVE MOTOR ASSEMBLY

- a. Perform removal procedure for printed-circuit board (paragraph 6.5.1).
- b. Remove screws securing drive-motor cable clamps.
- c. Remove AC connector from bracket.
- d. Remove spindle drive belt.
- e. Remove three (3) nuts or screws securing drive motor.
- f. Remove drive-motor assembly (drive motor, capacitor, and AC connector).
- g. To replace drive-motor assembly perform in reverse Steps f through a.

## 6.5.4 STEPPER-MOTOR REPLACEMENT

- a. Disconnect stepper-motor cables from PWA and cut cable ties as required.
- b. Loosen hardware securing pulley to stepper-motor shaft.
- c. Remove hardware securing stepper motor to motor adapter.
- d. Hold pulley and carefully remove stepper motor from pulley and motor adapter.
- e. Reverse above procedure to install new motor, except: Check pulley alignment and installation per paragraph 6.5.2.m (iv).
- f. Perform Actuator Alignment, 6.4.2.

## 6.6 FREQUENCY CONVERSION

## 6.6.1 OPERATING FREQUENCIES CONVERSION PROCEDURE

This procedure is to be used to convert the FDD unit from 60 Hz operation to 50 Hz operation, or vice versa. This is accomplished by reversing the dual-diameter reversible pulley on the spindle-motor shaft using the following steps:

- a. Remove AC power.
- b. Remove printed-circuit board assembly per paragraph 6.5.1.
- c. Remove the belt from the spindle-motor pulley. (Accessible from the under side of unit.)
- d. Loosen setscrew and remove pulley.
- e. Reserve pulley and replace on motor shaft.
- f. Position pulley allowing tolerance of 0.039 in.  $(0.99 \text{ mm}) \pm 0.10$  in. (0.254 mm) between shoulder of motor mounting screws and pulley (Figure 6-11).
- g. Tighten down setscrew.
- h. Replace belt and printed-circuit board.

#### CAUTION

It is IMPORTANT that the new operating frequency be marked on the unit's rating nameplate.

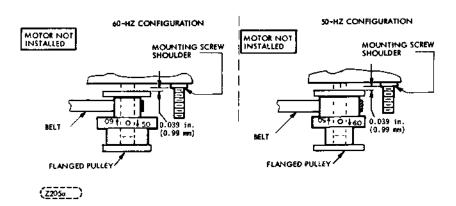


FIGURE 6-11. DRIVE-PULLEY REVERSAL

NOTE: When converting from 60 Hz to 50 Hz, the same belt may be used. When converting from 50 Hz to 60 Hz, a new belt must be installed.

## 7.1 INTRODUCTION

This section contains detailed information on the logic circuits used in the FDD. The logic consists of two types of circuits: discrete component and integrated circuits (IC). Integrated circuits are contained within a single chip and discrete component circuits contain individually identifiable resistors, capacitors, transistors, etc.

## 7.2 PHYSICAL DESCRIPTION (LOGIC)

All components are mounted on one side of the printed circuit board. The board is 7.0 x 8.0 inches (178 mm X 203 mm) and contain both IC and discrete component circuits.

## 7.3 USE OF RELATIVE LEVEL INDICATORS

The relative level indicator is a small triangle located on the input or output to a logic block. The presence or absence of this indicator indicates the conditions that are necessary to satisfy the function of the logic block. The presence of the triangle indicates a 0 logic level on that line is needed to satisfy the function. The absence of the triangle indicates a logical 1 is needed to satisfy the function.

The relative level indicator depicts the occurrence of inversion. Figure 7-1 shows some representative examples of the relative level indicator being used in this manner.

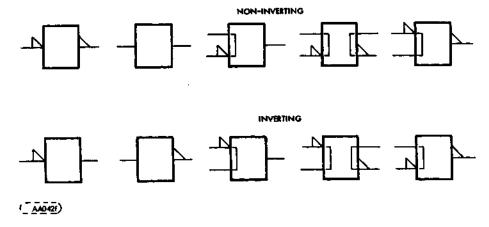


FIGURE 7-1. INVERSION CONVENTIONS

## 7.4 INTEGRATED CIRCUITS

Figure 7-2 shows an example of a schematic block and the information that it contains. The first line gives the function symbol which identifies the logic function that the block performs. Refer to Figure 7-3 for a summary of function symbols. The second line gives the CDC element number. The third line on the schematic block gives the circuit reference designation.

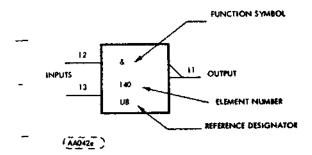


FIGURE 7-2. INTEGRATED CIRCUIT

	FUNCTION SYMBOLS
ð	AND GATE OR INVERTER
1	OR GATE OR INVERTER
=1	EXCLUSIVE OR
1.7	LONE SHOT
Σ	SUMMING CIRCUIT. NUMBER FOLLOWING (EXAMPLE 100) INDICATES GAIN OF 100
X/Y	LEVEL CONVERSION - TRANSMISSION LINE TO LOGIC LEVEL, SWITCH STATE TO LOGIC LEVEL OR LOGIC LEVEL TO POWER OUTPUT
₩0.	SCHMITT TRIGGER (LOWER TRIP POINT ADJUSTABLE)
	GENERAL SYMBOLS
	INDICATES NON STANDARD LOGIC LEVEL
<del></del> -	INDICATES ANALOG SIGNAL
<u>-ç</u>	TEST POINTS
$\overline{}$	INHIBITING INPUT

FIGURE 7.3. SCHEMATIC SYMBOLS

7-2 77653520-A

## 8.1 INTRODUCTION

This section contains an illustrated parts breakdown that describes and illustrates all variations of the (band-driven) Model 9406-4 Flexible Disk Drive (FDD). In general, parts are in disassembly sequence but do not necessarily indicate the maximum recommended disassembly of parts in this field.

## 8.2 ILLUSTRATIONS

Item numbers within a circle (1) indicate an assembly (group of parts). Item numbers without a circle, 1, indicate a single part; a group of parts that are pinned or press fitted together; or a group of parts which is normally replaced as an assembly.

## 8.3 PARTS LIST

In addition to the accompanying parts list on each illustration, two additional Parts Lists are available; the Top-Down Assembly/Component Parts List and the Cross Reference Index. Instruction for the use of all Parts Lists is given in para. 8.6.

## 8.4 PRODUCT CONFIGURATIONS

In conjunction with Table 8-1, Figure 8-1 serves two purposes;

- 1. When used with Table 8-1, it identifies all unique parts and assemblies for each FDD variation.
- 2. It identifies by sheet location where all major assemblies are broken down.

## 8.4.1 HARDWARE PRODUCT CONFIGURATOR (HPC)

To determine what parts are used on a particular model, find the applicable HPC number in Table 8-1. The item numbers at the top of Table 8-1 corresponds with the item numbers in Figure 8-1. All parts and assemblies that apply to the HPC number will be identified with an 'X' ('0' means not applicable). NOTE: The HPC Number is identical to the Equip. Ident. No. shown on the label.

## 8.5 REPLACEMENT PARTS

When ordering replacement parts for the FDD, the inclusion of the following information for each part ordered will ensure positive identification:

- 1. Equip. Ident. No.
- 2. Publication Number 77653522
- 3. Figure and Item Number
- 4. Identification Number and and Description
- 5. Equip. Series Code No.

#### NOTE:

Before ordering parts however, refer to paragraph 8.5.1 Spare Parts.

77653522<del>-</del>B

## 8.5.1 SPARE PARTS

This Illustrated Parts Breakdown is complete to the extent that all parts and assemblies are depicted and identified. Replacement part availability depends on the materials and provisioning operation of the supplier.

To assist the service representative in selecting replacement parts with minimum requisitioning lead times, engineering-recommended spare parts which reflect the intended service level of the device are identified with the letters SP adjacent to the item number on the face of each illustration. Replaceable non-spared items will require longer requisitioning lead times.

TABLE 8-1. PRODUCT CONFIGURATION

		<del>,, ,, ,, ,, ,, ,, ,, ,, ,, ,, ,, ,, ,, </del>	TEM NUMBERS				
	3333333333			<u>,                                    </u>	4444444444	444444444	4444444444
	00000000001					3333333333	4455667777
HPC	1234567890	0123456789	9012345678	9012345678	0123456789	0123456789	0101010123
77618101	x000000000	X00000000	000000000	0X0000X000		0000000000	XXXXXXXXX
1 1	X000000000		0X0X00X000	0X0000X000		0000000X00	XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
				0X0000X000		X000000000	XOXOXOGOXX
			0X0X00X000	000000000		X000000000	
			0000000000	0X0000X000		0000000000	
			000000000			00X0000000	
	X000000000		000000000		X000000000	X000000000	
. ,	X000000000	0X00000000	0X0X00X000	0000X000X0		X000000000	
	X000000000	X000000000	0X0X00X000	00X000X000	X000000000	0000000000	XOXOXOOXOO
				000X00X000	0X00000000	X000000000	
			0X0X00X000				XOXOXOOOXX
	X000000000		0X0X00X000			00X0000000	
			0X0X00X000		X000000000	X000000000	
	X000000000		0X0X00X000		X000000000	X000000000	
	X000000000		000X00X000		X000000000	00X0000000	XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
	X000000000		000X00X000		0000000000		XOXOXOOOXX
, ,	X000000000		000000000		X000000000	X000000000	
	X0000000000		0X0X00X000		X000000000	0000000000	
· · · ·		000000000			X000000000	000000000	
11010150	x000000000	0.000000000	000000000	0.000000000	X00000000	000000000	YOYOYOOXX
1							

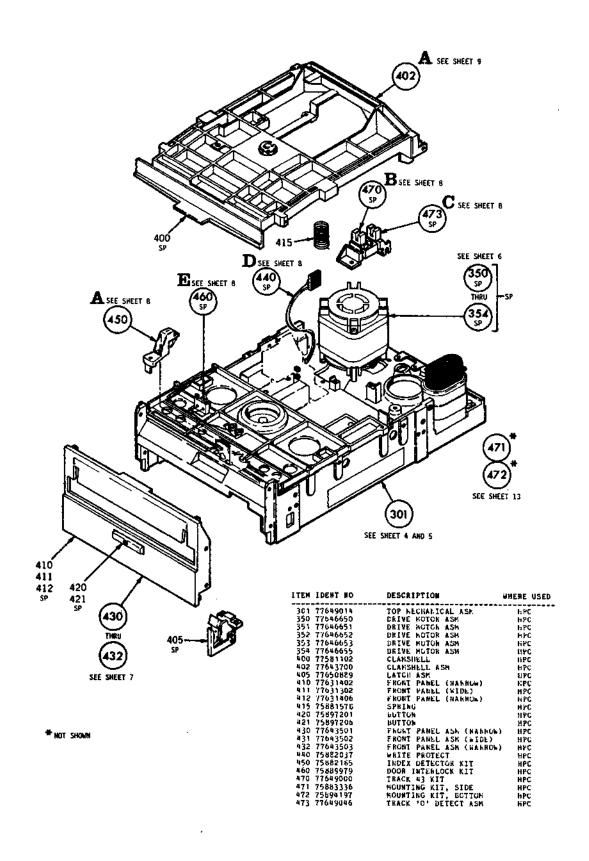


FIGURE 8-1. PRODUCT CONFIGURATION

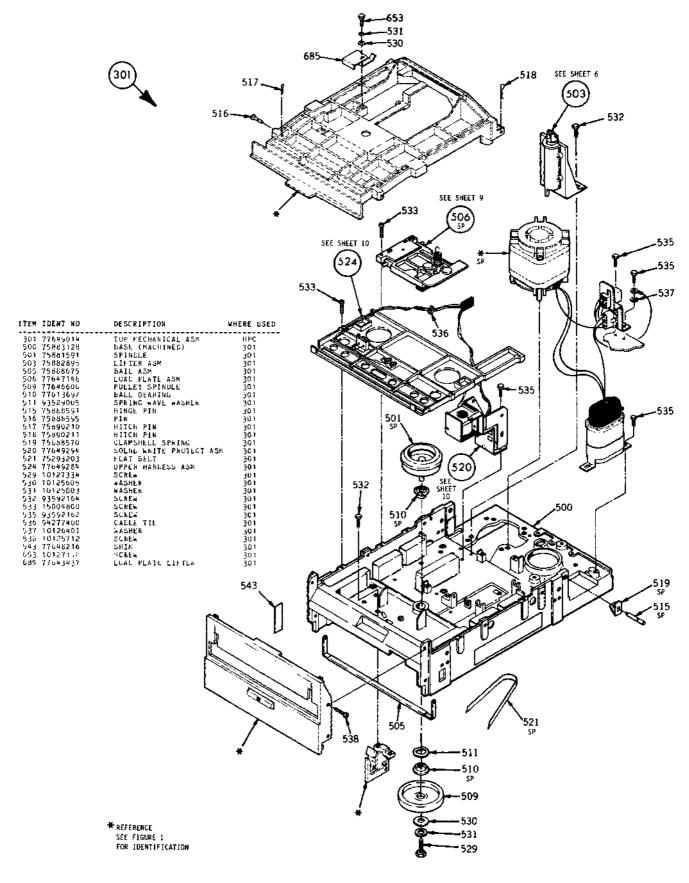


FIGURE 8-2. TOP MECHANICAL ASSEMBLY (SHEET 1 OF 2)

8-4

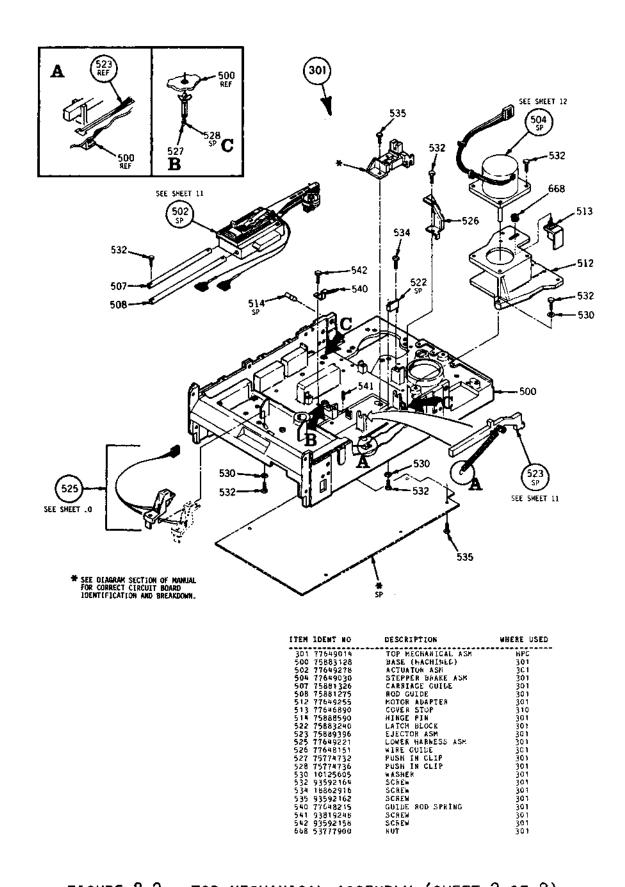


FIGURE 8-2. TOP MECHANICAL ASSEMBLY (SHEET 2 OF 2)

77653522~B

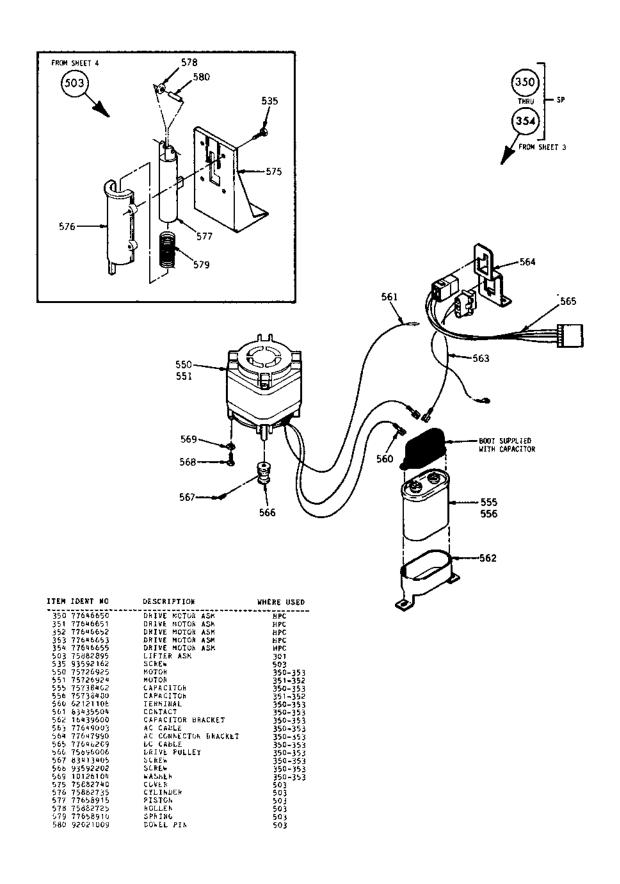
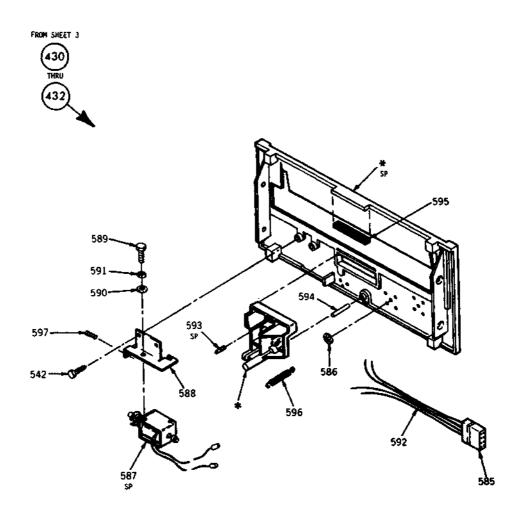


FIGURE 8-3. DRIVE MOTOR AND LIFTER ASSEMBLIES

8-6 77653522-B



MPREFERENCE SEE FIGURE 1 FOR INFORMATION

ITEM IDENT NO	DESCRIPTION WE	LERE USED
430 77643501	FRONT PANEL ASM (NANROW)	HPC
431 77643502	FRONT PAHEL ASH (WICE)	HPC
432 77643503	FRONT PAREL ASK (NARROW)	HPC
542 93592158	SCREW	430
585 75293954	CONNECTOR HOUSING	430-432
586 00848201	HETAINING RING	430-432
587 75882333	SCLENGID ASM	430
588 77636695	SOLENCID HOUNT	430
589 10127102	SCREW	430
590 75806502	<b>ASHER</b>	430
591 10125801	WASHER	430
592 77648205	CABLE	430-432
593 77612981	DIODE	430-432
594 77610030	DOWEL PIN	430-432
595 77646804	PORTER	430-432
596 75899166	LATCH SPHING	430-432
507 02820166	SCREW	#3U-#35

FIGURE 8-4. FRONT PANEL ASSEMBLY

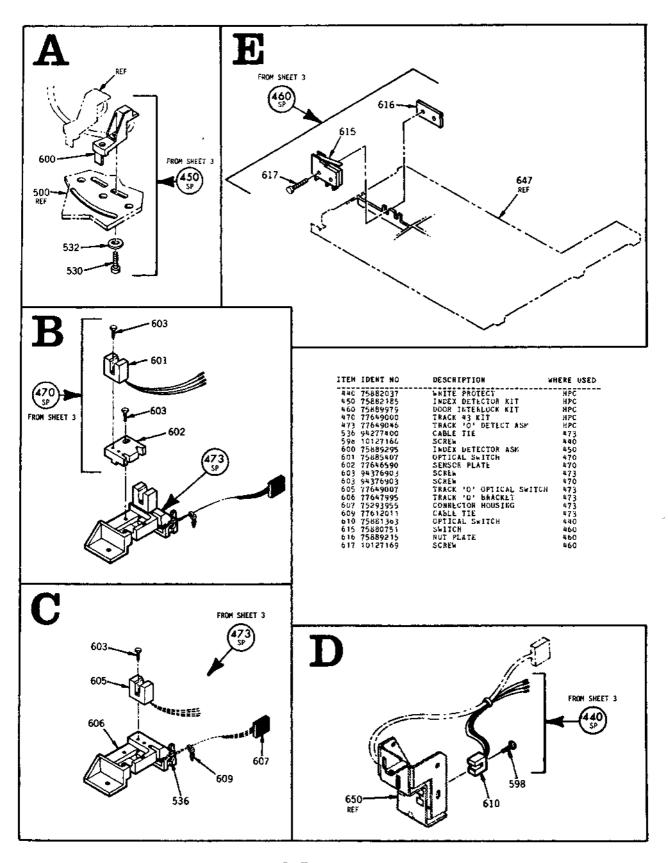


FIGURE 8-5. FEATURE KITS

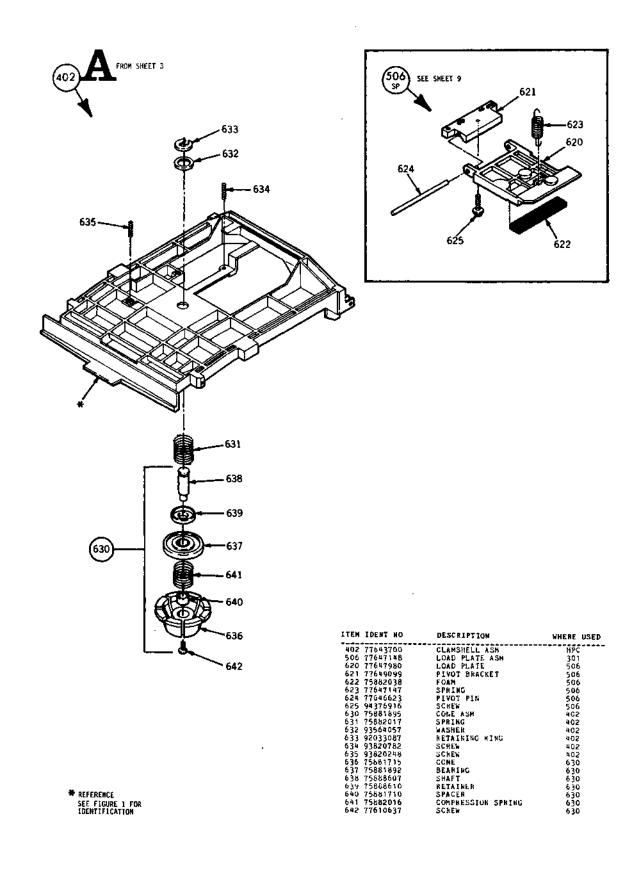


FIGURE 8-6. CLAMSHELL AND LOAD PLATE ASSEMBLIES

77653522-B

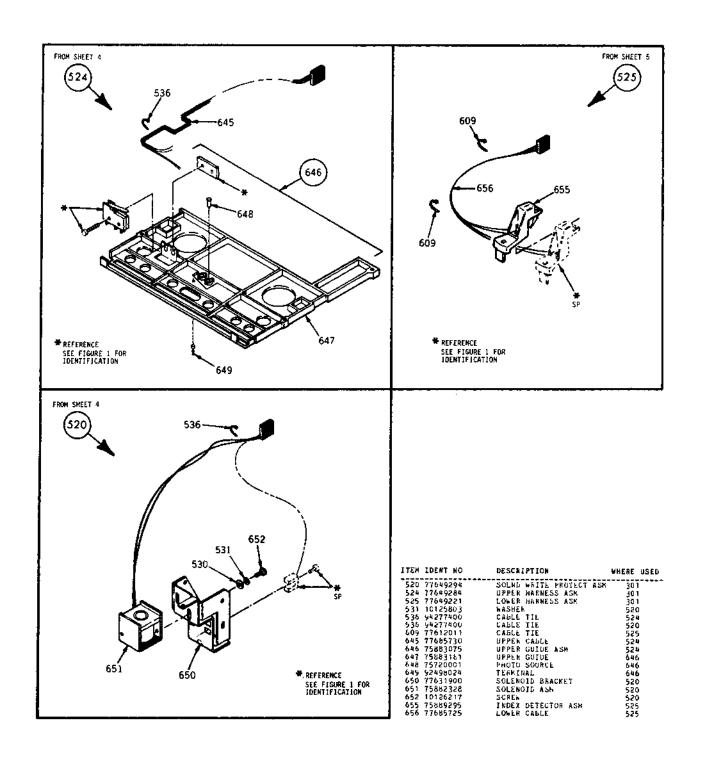


FIGURE 8-7, HARNESS ASSEMBLIES

8-10

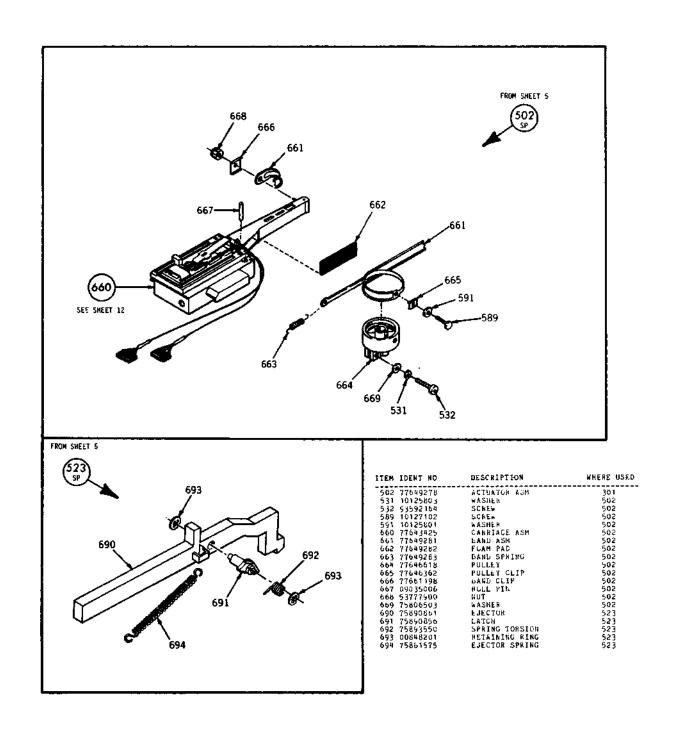


FIGURE 8-9. ACTUATOR AND EJECTOR ASSEMBLIES

77653522-B 8-11

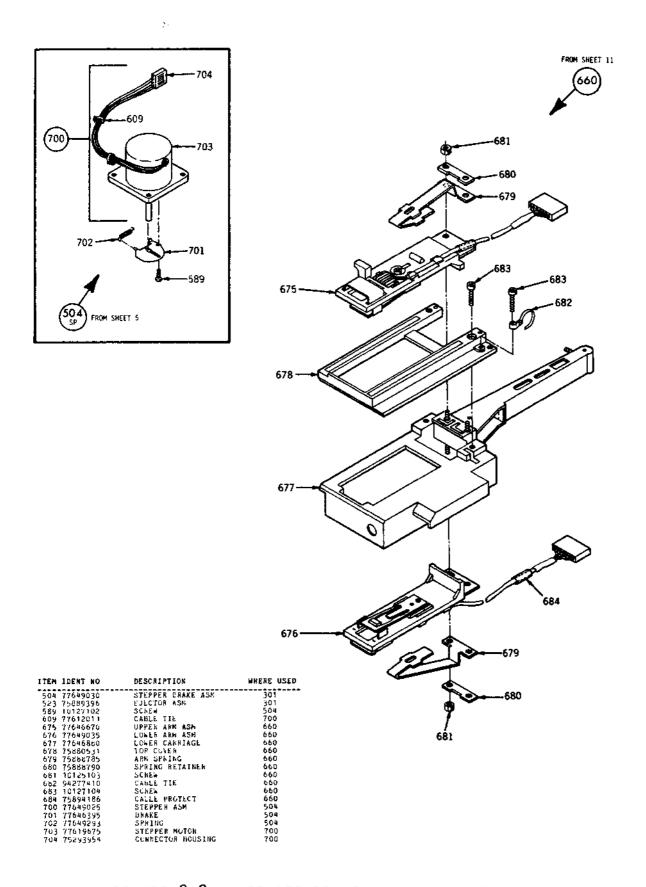
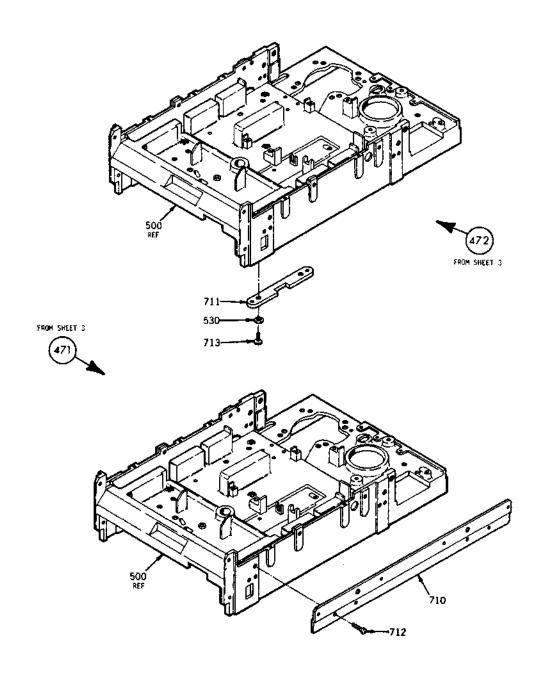


FIGURE 8-9. STEPPER BRAKE AND CARRIAGE ASSEMBLIES

8-12 77653522-B



ITEM IDENT NO	DESCRIPTION	WHERE USED
471 75883336 472 75894197 530 10125605 710 75883001 711 75881607 712 10127131 713 10127121	MOUNTING KIT, SIDE HOUNTING KIT, BOTTON WASHER, FLAT HOUNTING ADAPTER ADAPTER SCREW SCREW	NPC HPC 472 471 472 471 472

FIGURE 8-10. FEATURE KITS

### 8.6 PARTS LIST INSTRUCTIONS

#### 8.6.1 ILLUSTRATION PARTS LISTS

The parts list for each illustration is an extract from the Top-Down Assembly/Component Parts list and contains only those parts depicted. Refer to paragraph 8.6.2 for explaination of parts list.

#### 8.6.2 TOP-DOWN ASSEMBLY/COMPONENT PARTS LIST

- a. Starts at HPC level and lists all parts in Item Number sequence.
- b. Correlates Item numbers with part Identification numbers and the Description of each.
- c. Identifies where each part is used (where used column) within the device by listing the item number(s) of the next higher assembly.

#### NOTE

The same part may be used in any number of assemblies or sheet locations.

#### 8.6.3 CROSS REFERENCE INDEX

- a. Lists all parts in numeric sequence (by Identification Number).
- b. In conjunction with the referenced sheet number (third column) and illustrations, defines the physical location of each item identified.

#### 8.6.4 SHEET NUMBER REFERENCING

Sheet numbers referenced on Parts Lists and Illustrations refers to sheet locations in this section. Example: Sheet reference 3 represents sheet 8-3, sheet 4 represents sheet 8-4, etc.

## TOP-DOWN ASSEMBLY/COMPONENT PART LIST

ITEM IDENT NO	DESCRIPTION	WHERE USED	SHEET	ITE	IDENT NO	DESCRIPTION	WHERE USED	SHEET
301 77649014	TOP MECHANICAL ASM	HPC	503	540	77648215			505
301 77649014	TOP MECHANICAL ASM TOP MECHANICAL ASM TOP MECHANICAL ASM OBJUS MECHANICAL ASM	HPC	S05	541	93819248	SCREW	301	\$05
301 77649014 350 77646650	TOP MECHANICAL ASM Drive NCTOR ASM	нрс НРС	S04 S06	542	93592158 93592158	SCREW Screw	301	305
350 77646650	DRIVE MOTOR ASM	HPC	503		77648216	SHIM	430 301	S07 S04
351 7764 <b>665</b> 1 351 77646651	DRIVE NOTOR ASM	HPC HPC	\$06		75726925	MOTOR	350-353	506
352 77646652	DRIVE MOTOR ASM DRIVE MOTOR ASM	HPC	S03 S06		75726924 75738402	MOTOR CAPACITOR	351-352 350-353	S06 S06
352 77646652	DRIVE MOTOR ASM	HPC	S03	556	75738480	CAPACITOR	351-352	506
353 77646053 353 77646653	DRIVE MCTOR ASH DRIVE MCTOR ASH	прс КРС	506 503		62121108 83435504	TERMINAL CONTACT	350-353	506
354 77646655	LRIVE MOTOR ASM	1.PC	503		16439600	CARACTTON DRACKET	350-353 350-353	
354 77646655 400 77581102	DRIVE NOTOR ASM CLAMSHELL	HPC HPC	506	563	77649003	AC CABLE AC CABLE DC CABLE DRIVE PULLEY	350-353	S06
402 77643700	CLAMSHELL ASM	HPC HPC	S03 S09		77647590 77648209	AC CONNECTOR BRACKET	350-353 350-353	506 506
402 7764370G	CLAMSHELL ASM	HFC	503	566	75896006	DRIVE PULLEY	350-353	506
405 77650869 410 77631402			\$03 503		83413405 93592202	SCRES	250-252	206
411 77631302	FRONT PAREL (WIDE)	HYC	503		10126104	WASHER	350-353 350-353	306 306
412 77631406 415 75681578	FRONT PAUEL (NARROW) FRONT PANEL (WIDE) FRONT PANEL (NARROW) SPRING	HPC HPC	S03		75882740	COVER	350-353 350-353 503	S06
420 75897201	BUTTCN	HPC	503		75882735 77658915	COVEH CYLINDER PISTON	503 503	S06 S06
421 75897206	EUTTON	HPC	503	578	75882725	ROLLER	503	506
430 77643501 430 77643501	FRONT PANEL ASM (HARNOW) FRONT PANEL ASK (NANKOW)	HPC HPC	507 503		77658910 92021009	SPRING LOWEL PIL CONNECTOR HOUSING RETAINING RING SOLENOID ASM SOLENOID NOUNT SCHEM	503	506
431 77643502	FRONT PANEL ASH (WIDE)	HPC	S07		75293954	CONNECTUR HOUSING	430-432	500
431 77643502 432 77643503	FRONT PANEL ASM (WILE) FRONT PANEL ASM (HARROW)	H P C H P C	S03 S07		00848201	RETAINING RING	430-432	S07
432 77643503	FRONT PANEL ASM (MARNOW) WRITE PROTECT	HFC	503	588	75882333 77636695	SOLENOIS ASM SOLENOID NOUNT	430 #30	\$07 \$07
440 75882037	WRITE PROTECT	HPC	508	589	10127102	SCHEW	430	SCT
440 75882037 450 75882185	WMITE PROTECT  INDEX DETECTOR KIT	HPC HPC	503 508		10127102 10127102	SCREW Screw	502	511
450 75862185	INDEX DETECTOR KIT	HPC HPC HPC HPC HPC HPC	503	590	75806502	WASHER	504 430	\$12 \$07
460 75889979 460 75889979	DOOR INTERLUCK KIT	NPC HPC	508 503		10125801	WASHER	430	507
470 77649000	TRACK 43 KIT	HPC	503		10125801 77648205	WASHER Cable	502 430-432	\$11 \$07
470 77649000	TRACK 43 KIT	HPC	508	593	77612981	DIOCE	430-432	507
471 75863336 471 75863336	ROUNTING KIT, SIDE	HPC HPC	513 50±		77610030 77646804	DOWEL PIN	430-432	S07
472 75894197	MOUNTING KIT, BUTTOM	H.PC	513		75899166	BUMPER Latch Spring	430-432 430-432	507 507
472 75694197 473 77649046	MUUNTING KIT, BOTTOM	hPC	S03 S08	597	92820166	SCREW	430-432	S07
473 77649046	TRACK 'O' DETECT ASM	HPG	503		10127166 75889295	SCREW TRAFF DETECTOR ASM	440 450	S08 S08
500 75863128	SASE (MACHINED)	301	505	601	75885407	INDEX DETECTOR ASM OPTICAL SWITCH	470	SOB
500 75883128 501 75881591	SPINDLE	301 301	S04 S04		77646590 94376903	SENSOR PLATE Screw	470	S08
502 77549278	ACTUATOR ASM	301	505		94376903	SCREW	470 473	308 S08
502 77649276 503 75882695	ACTUATUH ASM	301	511 504		77649007	TRACK 'O' OPTICAL SHITCH	473	S08
503 75882695	TRACK 43 KIT TRACK 43 KIT TRACK 43 KIT, SIDE MOUNTING KIT, SIDE MOUNTING KIT, SOTTOM MUNTING KIT, BOTTOM MUNTING KIT, BOTTOM TRACK "G" DETECT ASM TRACK "G" DETECT ASM GASE (MACHINED) BASE (KACHINED) SPINDLE ACTUATUR ASM LIFTER ASM LIFTER ASM STEPPER BRAKE ASM STEPPER BRAKE ASM BAIL ASM	301	506		77647995 75293955	TRACK 'O' BHACKET CONNECTOR HOUSING	473 473	508
504 77649030	STEPPER BRAKE ASM	301	505	609	77612011	CABLE TIE	473	S08 S08
504 77649030 505 75888675	STEPPER BRAKE ASM BAIL ASH	301 301	S12 S04		77612011 77612011	CABLE TIE	626	S 10
500 77647148	LUAD PLAIL ASM	301	S09		75881363	CABLE TIE Optical switch	700 440	S12 S08
506 77647148 507 75881326	LOAD PLATE ASN	301	504	615	75880751	SWITCH	400	508
508 75861275	CARRIAGE GUIDE RCD GUIDE RCD GUIDE PULLEY SPINDLE BALL BEAKING SPRING WAVE WASHER HOIGH ADAPTER EGVER STOP	301	\$05 \$05		75889215 10127169	NUT PLATE Screw	#60 #60	S08 S08
505 77646600	PULLEY SPINDLE	301	504	620	77647980	LOAD PLATE	506	509
510 77613697 511 93529005	SPRING WAVE WASHER	301	S04 S04		77649099 75882038	PIVOT DRACKET	506	509
512 77649255	HOTON ADAPTER	301	505	623	77647147	FOAM Spring	506 506	S09 S09
513 77646890 514 75868590	COVER STOP HINGE PIN	310 301	\$05 \$05		77646623	PIVOT PIN	506	S09
515 75888591	HINGE PIN	301	504		94376916 75881895	SCREW CONE ASM	506 402	509 509
51b 75888595	PIN	301	504	631	75882017	SPHING	402	509
517 75890210 518 75890211	HITCH PIN HITCH PIN	301 301	S04 S04	632 633	93564057 92033087	WASHER RETAINING RING	402	509
519 75888570	CLAMSHELL SPRING	301	304		93820782	SCREW	402 402	S09 S09
520 77649294 520 77649294	SOUND WRITE PROTECT ASK SOUND WRITE PROTECT ASK	301 301	504 \$10	635	93820248	SCREW	402	509
521 75293203	FLAT BELT	301	504		75881715 75881892	CONE BEARING	630 630	S09 S09
522 75883240	LATCH BLOCK EJECTOR'ASM	301	505	638	75888607	SHAFT	630	309
523 75889396 523 75889396	EJECTOR ASH	301 301	505 512		75888610 75861710	RETAINER Spacer	630	S09
524 77649284	UPPER HARRESS ASM	301	504	641	75882016	COMPRESSION SPRING	630 630	509 509
524 77649284 525 77649221	UPPER HARNESS ASA LOWEN HARNESS ASM	301 301	S 10 S 05		77610637	SCREW	630	S09
525 77649221	LOWER HARNESS ASM	301	\$10	646	77685730 75883075	UPPER CABLE UPPER GUIDE ASM	524 524	S 10 S 10
526 77648151 >27 75774732	WIRE GOIDE	301 301	S05 S05	647	75883181	UPPER GUIDE	646	S 10
528 75774736	PUSH IN CLIP PUSH IN CLIP	301	S05		75720001 92498024	PHOTO SOURCE Terminal	646 646	\$10 \$10
529 10127334	SCKEW	301	504	650	77631900	SOLENOID BRACKET	520	\$10
530 10125605 530 10125605	₩ASHER Washer	301 301	504 505	651 652	75882328 10126217	SOLENOID ASM Screw	520	510
530 10125605 530 10125605	WASHER, FLAT	472	\$13		10127114	SCREW	520 301	S10 S04
531 10125803 531 10125803	WASHER Washer	301 502	S04 S11		75889295	INDEX DETECTOR ASH	525	510
531 10125803	WASHER	520	510		77685725 77643425	LOWER CABLE CARRIAGE ASM	525 502	S10 S11
532 93592164	SCREW	502	S11	661	77649281	BAND ASM	502	S11
532 93592164 532 93592164	SCREW Screw	301 301	S05 S04		77649282 77649283	FOAM PAD Band Spring	502	\$11
533 15004800	SCREW	301	504	664	77646618	PULLEY	502 502	\$11 \$11
534 18862916 535 93592162	SCREW Screw	301 301	S05 S04	665	77646362	PULLEY CLIP	502	\$11
535 93592162	SCREW	301	505		77661198 09035006	BAND CLIP ROLL PIN	502 502	511 \$11
535 93592102	SCREW	503	506	668	53777900	TUN	502	SII
536 94277400 536 94277400	CABLE TIE Cable tie	301 473	508 508	668	53777900 75806503	NUT Washer	301	805
536 94277400	CABLE TIE .	520	\$10	675	77646670	UPPER AHM ASM	502 660	\$11 \$12
536 94277400 537 10126401	CABLE TIE Washer	524 301	\$10 \$04	676	77649035	LOWER ARN ASM	660	512
538 10125712	2CHEM	301	504		77646880 75880531	LOWER CARRIAGE Top cover	660 660	S12 S12
								· · -

77653522-B 8-15

## TOP-DOWN ASSEMBLY/COMPONENT PART LIST

ITEM	IDENT	NO.	DESCRIPTION	WHERE	USED .	SHEET
679	758861	185	ASH SPRING	666	0	\$12
			SPRING RETAINER	661	נ	\$12
681	101251	103	SCHE!	66	3	512
682	942774	+1ù	CABLE TIE	Óbi	0	512
683	101271	104	SCREM	661	J	\$12
684	758941	186	CABLE PROTECT	661	2	Sia
685	776434	137	LUAD PLATE LIFTEH	30	1	S04
690	758908	161	EJÉCION	52	3	511
691	758902	156	LATCH	52	j.	SII
692	758939	550	SPHING TORSION	52	3	511
693	005482	201	RETAINING KING	52	3	511
694	758619	575	EJECTOR SPRING	52		511
700	776490	125	STEPPER ASN	50	Ĭ,	S12
701	77646	195	BRAKE	50	4	<b>312</b>
702	776492	93	SPRING	50	4	\$12
705	776196	75	STEPPER MOTOR	7C	3	\$12
704	752939	954	CONHECTOR HOUSING	700	נ	S12
710	758830	001	POUNTING ACAPTER	47	ì	\$13
711	758816	507	ALAPTER	477	?	S13
	101271		SCHEN	47		513
713	101271	121	SCREW	47		513

### CROSS REFERENCE INDEX

ITEM IDENT NO	SHEET	ITÉM IDÉNT NO SHEET	ITEM IDENT NO SHEET
693 00848201	\$11	684 75894186 \$12	632 93564057 509
586 00848201	507	472 75894197 S13	542 93592158
667 09035006 681 10125103	\$11 \$12	472 75894197 503 566 75896006 506	535 93592162 504
530 10125605 530 10125605	S13 S05	420 75897201 S03 421 75897206 S03	535 93592162 S05 535 93592162 S06
530 10125605	504	596 75899166 507	532 93592164 \$04
538 10125712 591 10125801	504 507	400 77581102 \$03 594 77610030 \$07	532 93592164 805
591 10125801	511 504	642 77610637 \$69	568 93592202 \$06 541 93819248 \$05
531 10125803 531 10125803	511	609 77612011 \$12 609 77612011 \$08	635 93820248 509
531 10125803 569 10126104	\$10 \$06	609 77612011 S10	634 93820762 \$09 536 94277400 \$08
652 10126217	\$10	593 77612981 S07 510 77613697 S04	536 94277400 \$10
537 10126401 589 10127102	S04 S07	703 77619675 \$12 411 77631302 \$03	536 94277400 \$10 536 94277400 \$04
589 10127102	811	410 77631402 \$03	682 54277410 S12
589 10127102 683 10127104	S12 S12	412 77631406 SC3 650 77631900 S10	603 94376903 508
653 10127114 713 10127121	504 513	588 77636695 S07	625 94376916 \$09
712 10127131	S13	660 77643425	
598 10127166 617 10127169	508 508	430 77643501 S07 430 77643501 S03	
529 10127334	504	431 77643502 807	
533 15004800 562 16439600	504 506	431 77643502 S03 432 77643503 S07	
534 18862916	S05	432 77643503 503	
668 53777900 668 53777900	\$11 505	402 77643700	
560 62121108 521 75293203	S06 S04	665 77646362 S11 701 77646395 S12	
704 75293954	512	602 77646590 S08	
585 75293954 607 75293955	507 508	509 77646600	
648 75720001	\$10	624 77646623 SO9	
551 75726924 550 75726925	SC6 SO6	350 77646650 \$06 3 <b>5</b> 0 77646650 \$03	
555 75738402 556 75738480	506 506	351 77646651 \$06	
527 75774732	505	351 77646651 503 352 77646652 506	
528 75774736 590 75806502	S05 S07	352 77646652	
669 75806503	<b>S11</b>	353 77646653 503	
678 75880531 615 75880751	512 508	354 77646655 S06 354 77646655 SC3	
508 75881275	SQ5	675 77646670 \$12	
507 75861326 610 75881363	S05 S08	595 77646804 S07 677 77646880 S12	
694 75881575 415 75881578	511 503	513 ?7646890 S05	
501 75881591	504	623 77647147	
711 75881607 640 75881710	\$13 \$09	506 77647148 S04 620 77647980 S09	
636 75881715	809	564 77647990 SD6	
637 75881892 630 75881895	SC9 SO9	606 77647995 S08 526 77648151 S05	
641 75862016	509 509	592 77648205 S07	
631 75882017 440 75882037	SC3	565 77648209	
440 75882037 622 75882038	SC8 SO9	543 77648216 S04	
450 75882185	508	470 77649000 S03	
450 75862185 651 75882328	503 \$10	563 77649003	
587 75882333	507	301 77649014 S04	
578 75682725 576 75882735	506 306	301 77649014 S03 301 77649014 S05	
575 75882740	S06	700 77649025 S12	
503 75882895 503 75882895	506 504	504 77649030 S12 504 77649030 S05	
710 75883001 646 75883075	513 510	676 77649035 S12 473 77649046 S08	
500 75883128	504	473 77649046 503	
500 75883128 647 75883181	S05 S10	621 77649099	
522 75883240	S0\$	525 77649221 S05	
471 75883336 471 75883336	S03 S13	512 77649255 \$05 502 77649278 \$05	
601 75885407 519 75888570	508 504	502 77649278 S11	
514 75888590	305	661 77649281 \$11 662 77649282 \$11	
515 75888591 516 75888595	504 S04	663 77649263 511 524 776#9284 510	
638 75688607	509	524 77649284 504	
639 75888610 505 75888675	509 504	702 77649293	
679 75888785	S12	520 77649294 S10	
680 75888790 616 75889215	\$12 \$08	405 77650889	
USS 75869295	310	577 77658915 S06	
600 75889295 523 75889396	S08 S05	666 77661198 \$11 656 77685725 \$10	
523 75869396	S12	645 77685730 S10	
460 75889979 460 75889979	508 503	. 567 83413405 SD6 561 83435504 SO6	
517 75890210	S04	580 92021009 506	
518 75890211 691 75890856	S04 S11	633 92033087 509 649 92498024 510	
690 75890861 692 75893550	S11 S11	597 92820166 507 511 93529005 504	
->- 12033330		2004 C000A3CC4 ++ C	

8-17/8-18 77653522-B

## 9.1 INTRODUCTION

The following paragraphs contain the following wire lists: Upper-Harness Assembly; Lower-Harness Assembly; Stepper Motor; DC Harness; Sensor Assembly Track 43; Door-Lock-Solenoid Activity LED and Head Assemblies.

### 9.2 UPPER-HARNESS ASSEMBLY

9.2	UPPER	R-HARNESS ASSEMBLY		
Wire Col	lor	Origin	<u>Destination</u>	Approximate Length, Inches
Violet Gray Brown Yellow		S/S Index Anode D/S Index Cathode Common Door Switch N/O Door Switch	J6-1 J6-5 J6-3 J6-4	15 (381mm) 15 (381mm) 15 (381mm) 15 (381mm)
9.3	LOWE	R-HARNESS ASSEMBLY		Approximate
Wire Col	<u>lo</u> r	Origin	<u>Destination</u>	Length, Inches
Yellow Brown Orange Black		D/S Index Collector D/S Index Emitter S/S Index Collector S/S Index Emitter	J9-4 J9-5 J9-2 J9-3	13 (33) mm) 13 (330mm) 13 (330mm) 13 (330mm)
9.4	STEP	PER MOTOR		
Wire Col Orange Brown Yellow Red	<u>lor</u>	Origin ØA ØB ØC ØD	Destination  J3-1  J3-3  J3-5  J3-7	Approximate Length, Inches 8 (203mm) 8 (203mm) 8 (203mm) 8 (203mm)
9.5	DC H	ARNESS		
Wire Col	<u>lor</u>	Origin	Destination	Approximate Length, Inches
Red Black Orange Brown		J4-5 +5V J4-2 GND J4-1 +24V J4-3 +24V Return	J10-5 J10-6 J10-1 J10-2	8 (203.2mm) 8 (203.2mm) 8 (203.2mm) 8 (203.2mm)
9.6	SENS	OR ASSEMBLY TRACK 43, T	RACK OO HARNESS	ASSEMBLY

### 9.6 SENSOR ASSEMBLY TRACK 43, TRACK OO HARNESS ASSEMBLY

Wire Color	Origin	<u>Destination</u>	Approximate Length, Inches
Red	Current-Switch Collector	J8-3	7.3 (185.4mm)
White	Current-Switch Cathode	J8-6	7.3 (185.4mm)
Blue	Current-Switch Emitter	J8-4	7.3 (185.4mm)
Green	Current-Switch Anode	J8-5	7.3 (185.4mm)
Brown	Anode Track 0	J8-7	7.3 (229mm) 185.4
Yellow	Cathode Track 0	J8-8	7.3 (229mm) 185.4
Orange	Emitter Track 0	J8-10	7.3 (229mm) 185.4
Black	Collector Track 0	J8-9	7.3 (229mm) 185.4
77653520-A			9–1

## 9.7 DOOR-LOCK SOLENOID ACTIVITY LED

Wire Color	Origin	Destination	Approximate Length, Inches
Black	Door-Lock Solenoid+ Door-Lock Solenoid- Activity LED Anode Activity LED Cathode	J7-3	16 (406mm)
Black		J7-4	16 (406mm)
Brown		J7-2	16 (406mm)
Blue		J7-1	16 (406mm)

# 9.8 HEAD ASSEMBLIES

### Head 0

Wire Color	Origin	Destination
Black	Read/Write	J2-1
White	Read/Write	J2-2
White (large wire)	Shield	J2-3
Green	Erase+	J2-4
Red	Center Tap	J2-5
-	Key	J2-6
Yellow	Erase-	J2-7
	Hoad 1	

#### Head 1

Wire Color	Origin.	<u>Destination</u>
Plack	Read/Write	J2-8
White	Read/Write	J2-9
Red	Center Tap	J2-10
Green	Erase+	J2-11
White (large wire)	Shield	J2-12
-	Key	J2-13
Yellow	Erase-	J2-14

## 9.9 SOLENOID HARNESS ASSEMBLY

Wire Color	<u>Origin</u>	Destination	Approximate Length, Inches
Black	Head-Load Solenoid+	J5-1	9 (229mm)
Black	Head-Load Solenoid-	J5-2	9 (229mm)
Green	Write-Protect Anode	J5-5	9 (229mm)
Red	Write-Protect Collector	J5-7	9 (229mm)
Blue	Write-Protect Emitter	<b>J5-</b> 8	9 (229mm)
White	Write-Protect Cathode	J5-4	9 (229mm)

77653520-A