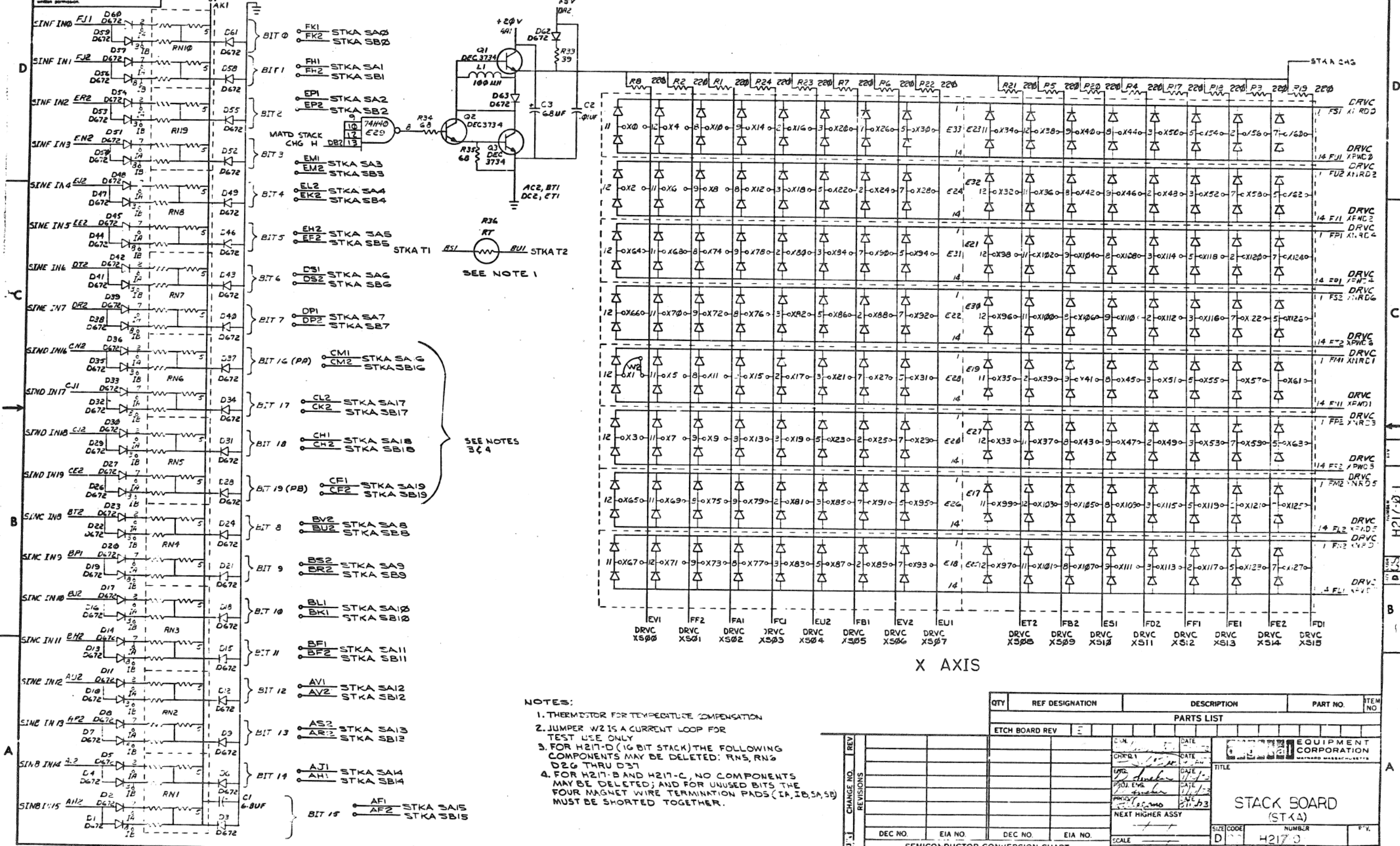




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SEE NOTE 1

SEE NOTES 3 & 4

- NOTES:
1. THERMISTOR FOR TEMPERATURE COMPENSATION
  2. JUMPER W2 IS A CURRENT LOOP FOR TEST USE ONLY
  3. FOR H217-D (16 BIT STACK) THE FOLLOWING COMPONENTS MAY BE DELETED: RNS, RN2, D26 THRU D37
  4. FOR H217-B AND H217-C, NO COMPONENTS MAY BE DELETED; AND FOR UNUSED BITS THE FOUR MAGNET WIRE TERMINATION PADS (IA, IB, SA, SB) MUST BE SHORTED TOGETHER.

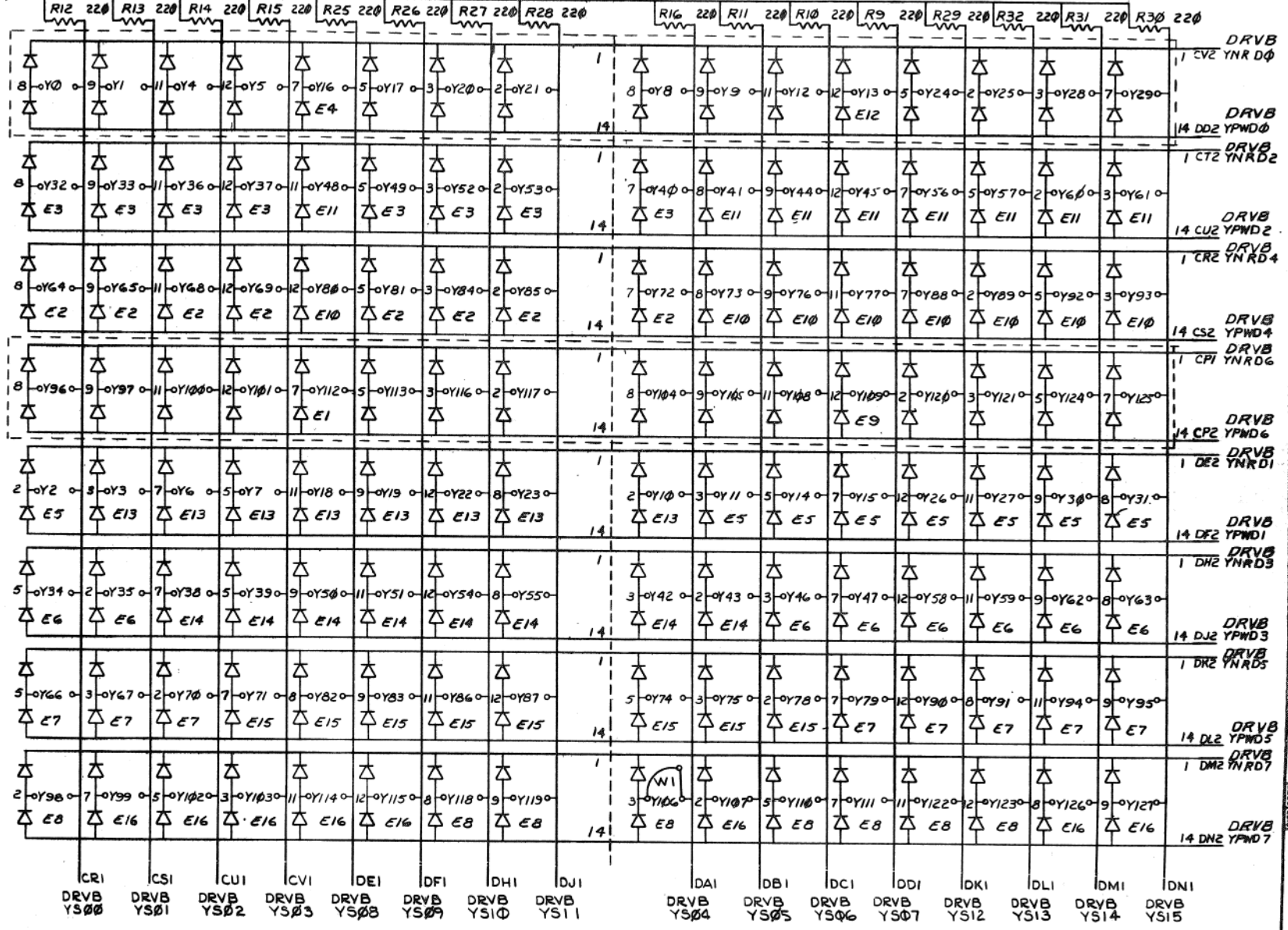
X AXIS

QTY	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
ETCH BOARD REV				
EQUIPMENT CORPORATION				
STACK BOARD (STKA)				
H217-D				
SEMICONDUCTOR CONVERSION CHART				
DEC NO.	EIA NO.	DEC NO.	EIA NO.	SCALE
				SHEET 2 OF 2

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1-0-212H CS 2

STKA CHG



Y AXIS

NOTE: JUMPER W1 IS A CURRENT LOOP FOR TEST USE ONLY

QTY	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
ETCH BOARD REV E				
CHK	CHANGE NO.	REV	DATE	BY
			7-10-72	W. MacIntyre
			8-1-73	J. S. [unclear]
			11-19-73	[unclear]
			11-16-73	[unclear]
			5-14-73	[unclear]
NEXT HIGHER ASSY				
SEMICONDUCTOR CONVERSION CHART				
DEC NO.	EIA NO.	DEC NO.	EIA NO.	TITLE
				STACK BOARD (STKB)
SCALE		DICS		NUMBER
3 OF 3		H217-01		REV.
SHEET		DIST.		

REV. H217-01