

DRAWING DIRECTORY

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CUSTOMER PRINT SET INDEX

THIS IS PRINT SET

- DRAWING DIRECTORY
- MODULE UTILIZATION
- TIMING DIAGRAM
- 16K UNIBUS TIMING
- 16K X-Y DRIVE
- 16K SENSE MEMORY
- MEMORY STACK (16K X 16)
- PARITY CONTROL
- BACK PLANE
- OPTION HARNESS
- PARTS LIST
- CUSTOMER ACCEPTANCE PROC.

SEQUENCE		SEQUENCE
	B-DD-MF11-U	
	D-MU-MF11-U-MU	
	D-TD-MF11-U-1	
	D-CS-M8293-0-1	
	D-CS-G235-0-1	
	D-CS-G114-0-1	
	D-CS-H217-0-1	
	D-CS-M7259-0-1	
	D-CS-5410345-0-1	
	E-IA-7009535-0-0	
	A-PL-MF11-U-0	
	A-SP-MF11-U-3	

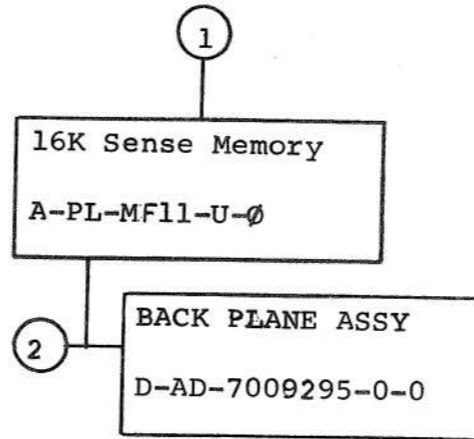
- MFG. PRINT SET
- ON-LINE TEST PROCEDURE A-SP-MF11-U-2
- MF11-U ENGINEERING SPECIFICATION A-SP-MF11-U-4

UNIT VARIATIONS		PRINT SET	
VAR	TITLE	MF11-U	
MF11-U	16K SENSE MEMORY	X	
MM11-U	16K SENSE MEMORY	X	
MF11-UP	16K SENSE MEMORY	X	
MM11-UP	16K SENSE MEMORY	X	

NOTE: TO INSTALL MF11-U OR MF11-UP IN 11/40 CPU WITH SERIAL NUMBER LESS THAN 6000 OR H960-D (OR-E) WITH SERIAL LESS THAN 7000 A FIELD MODIFICATION KIT (FM11-UA, -UB, OR -UC) IS REQUIRED.

DEC 16 (325) 1062-1A-R972

REV	A				USED ON OPTION/MODEL	DRN.	DATE	TITLE 16K SENSE MEMORY			
CHG. NO.	MF11-U-1					J. FLEMING	5/1/73				
DATE	12/73					CHK'D.	DATE	B DD	MF11-U		
						W. MAJOR	5/11/73				
						PROJ ENG.	DATE				
						D. SMELSER	5/15/73				
						PROD.	DATE				
						R. SHOOP	6/13/73				
						FIELD SERV.	DATE				
						WP RUBEY	11/8/74				
				SHEET 1 OF 3				DIST			



TITLE	SHEET	SIZE	CODE	NUMBER	REV
16K SENSE MEMORY	2 OF 3	B	DD	MF11-U	A

CUSTOMER PRINT SET		ELECTRICAL					CUSTOMER PRINT SET		MECHANICAL						
MF11-U	SET	FIND NO.	DRAWING NO.	REV	NO OF SHT	DESCRIPTION	OPTION NO./FILE DATE	MF11-U	SET	FIND NO.	DRAWING NO.	REV	NO OF SHT	DESCRIPTION	OPTION NO./FILE DATE
X		1	A-PL-MF11-U-0	A	1	16K SENSE MEMORY				1	A-PL-MF11-U-0	A	1	16K SENSE MEMORY	
X			D-CS-G114-0-1	#	9	16K SENSE INHIBIT		X			E-IA-7009535-0-0	#	1	OPTION HARNESS	
X			D-CS-G235-0-1	#	9	16K X-Y DRIVE									
X			D-CS-M8293-0-1	#	10	16K UNIBUS TIMING									
X			D-CS-H217-0-1	#	3	MEMORY STACK									
X			D-MU-MF11-U-MU	A	1	MODULE UTILIZATION									
X			D-TD-MF11-U-1	A	1	TIMING DIAGRAM									
X			D-CS-M7259-0-1	#	3	PARITY CONTROL									
X			A-SP-MF11-U-3	*	2	CUSTOMER ACCEPTANCE PROCEDURE									
	X		A-SP-MF11-U-2	*	6	ONLINE TEST PROCEDURE									
	X		A-SP-MF11-U-4	*	3	MF11-U ENG. SPEC.									
X		2	D-AD-7009295-0-0	#	1	BACK PLANE ASSY									
X			D-CS-5410345-0-1	#	1	32K X 16,18 MEMORY BACKPLANE									

CUSTOMER PRINT SET CODES
X = PRINT OF DOCUMENT INCLUDED IN PRINT SET
C = INCLUDES ALL PRINTS INDICATED ON DOCUMENT
S = CONFIDENTIAL AUTHORIZED SIGNATURE REQUIRED

TITLE
16K SENSE MEMORY

SHEET 3 OF 3

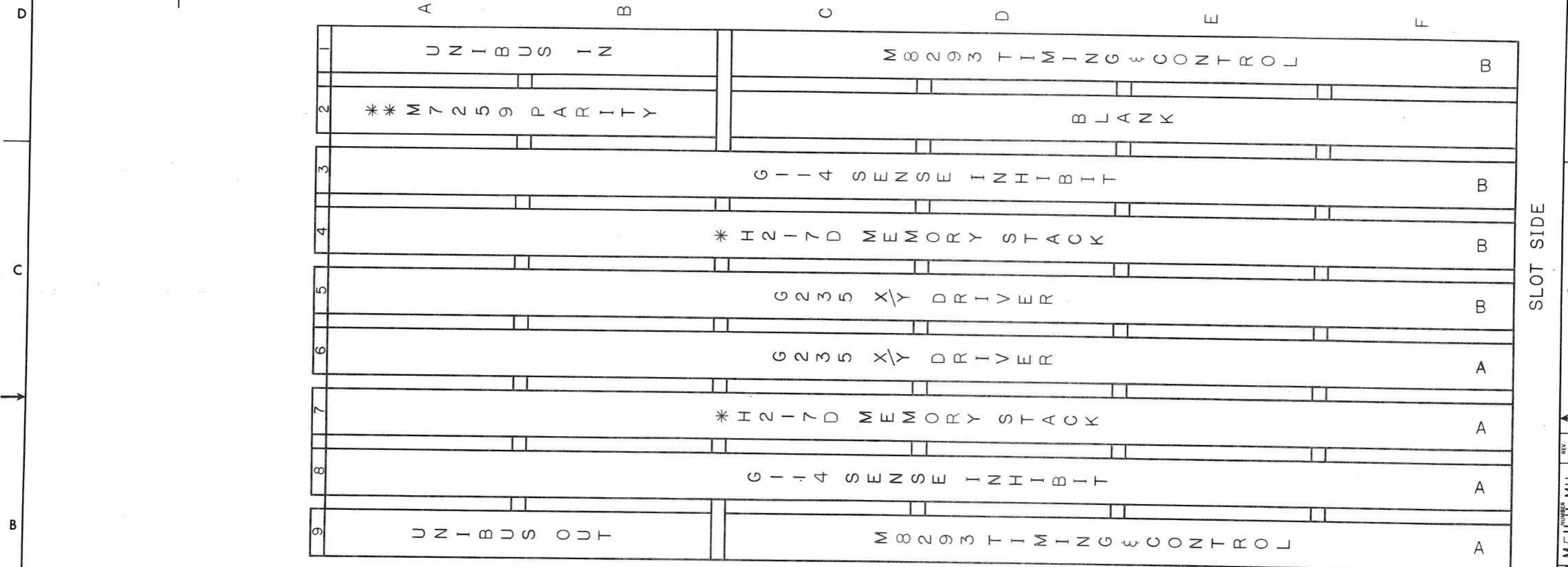
SIZE CODE
B DD

NUMBER
MF11-U

REV
A

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NOTES:
 * 1. FOR MFII-U (MEMORY WITH PARITY), USE H217C MEMORY STACK.
 ** 2. M7259 IS USED WITH MFII-U ONLY
 3. INSTALL 'A' MODULES FOR FIRST 16K, 'B' MODULES FOR 2ND 16K.



VIEWED FROM MODULE SIDE OF BACKPLANE

IC TYPE	GND	+5V

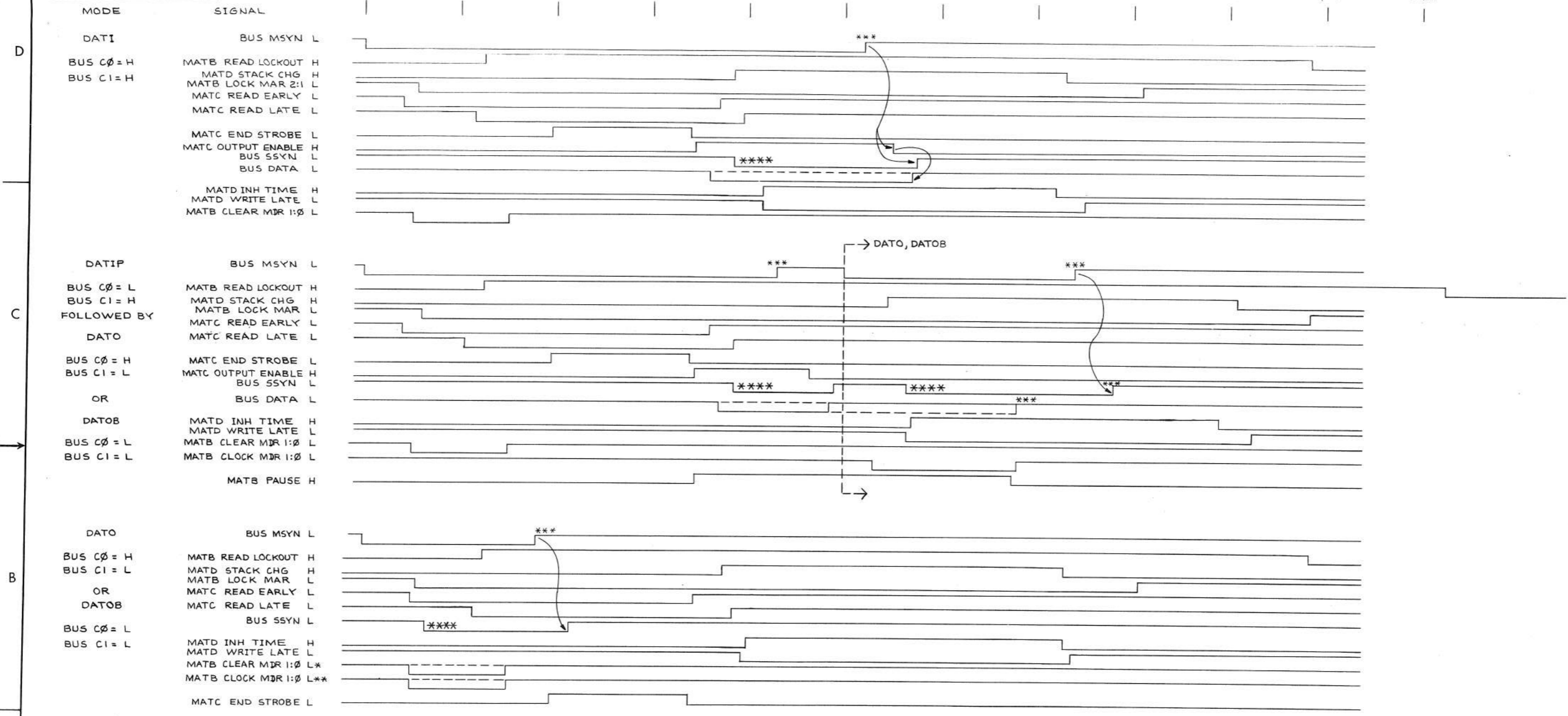
GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY. EXCEPTIONS ARE STATED ABOVE.

IC PIN LOCATIONS

FIRST USED ON OPTION MODEL		QTY	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
MFII-U						
ETCH BOARD REV		PARTS LIST				
DRN	DATE	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS				
CHK'D	DATE	MODULE UTILIZATION				
ENG	DATE	SIZE CODE	NUMBER	REV.		
PROJ. ENG.	DATE	DMU	MFII-U-MU	A		
PROD.	DATE	NEXT HIGHER ASSY				
	6/11/73	B-DD-MFII-U				
SEMICONDUCTOR CONVERSION CHART		SCALE	SHEET 1 OF 1			
DEC. NO.	EIA NO.	DEC. NO.	EIA NO.	DIST.		

BRUNING 40-522 16099
 DEC FORM NO. DRD-135A

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- NOTES:
- * IN THE DATOB MODE CLEAR MDR ONLY OCCURS IN THE BYTE NOT BEING ADDRESSED.
 - ** IN THE DATOB MODE CLOCK MDR ONLY OCCURS IN THE BYTE BEING ADDRESSED.
 - *** ACTUAL TIME DEPENDS ON BUS AND PROCESSOR DELAYS.
 - **** IN PARITY SYSTEMS BUS S5YNL WILL BE 20 NS LATER THAN SHOWN FOR DATO-DATOB BUS MODES AND 150 NS LATER FOR DATI-DATIP MODES.

REV.	CHG.	BY	DATE
A	1-3-73	P. DURANT	1-7-74

FIRST USED ON OPTION/MODEL MFII-U	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES	DRN. CHK'D.	DATE DATE	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
TOLERANCES	ENG. PROJ. ENG. PROD.	DATE DATE DATE	TITLE MFII-U TIMING DIAGRAM (UNIBUS INTERFACE)	
DECIMALS .xxx = .005 .xx = .02 .x = .1	ANGLES ° 0' 30"	REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY Y	SIZE CODE D TD	NUMBER MFII-U-1
MATERIAL	NEXT HIGHER ASSY. B-DD-MFII-U	SCALE - / -	SHEET 1 OF 1	REV. A

REV. A
NUMBER MFII-U-1
SIZE CODE DTD