PARTS REFERENCE

ITEM NO	DRAWING REFERENCE	DESCRIPTION		PART NUMBER	QUANTITY
1	E1,E7,E18,E27,E45,E54	8251B	I.C.	19 09594	6
2	E2,E5,E6,E8,E16,E17,E20,E21,E24,E37,E38	DEC 7400N	I.C.	19 05575	16
	E40,E44,E49,E52,E59				
3	E3,E4,E11,E14,E15,E22,E25,E34,E41,E43	DEC 7402N	I.C.	19 09004	14
	E46,E47,E50,E51				
4	E9,E10,E26,E57	DEC 7404N	I.C.	19 05578	4
5	E12,E29,E30,E36,E53,E58	DEC 7405N	I.C.	19 05580	6
6	E13,E32,E55,E60	DEC 7420N	I.C.	19 05577	4
7	E19,E31,E33,E48,E56	DEC 7410N	I.C.	19 05576	5
8	E23,E39	DEC 7453N	I.C.	19 05582	2
9	E28,E42,E61	DEC 8815A	I.C.	19 09713	3
10	E35	DEC 7460N	I.C.	19 05581	1
11	C1-C33, C35-C61	.01 MFD, 100V, 20%	CAP.	10 01610	60
12	C62-C64, C34	6.8MFD, 35V, 20%	CAP.	10 00067	4

NOTES:

1. PIN NOTATION THROUGHOUT IS ORDERED UPON MODULE PLACEMENT IN THE KAII PROCESSOR. MODULE REFERENCE ALONE IS OBTAINED BY DELETING THE NUMBER (SLOT LOCATION) AFTER THE FIRST LETTER, AND CONVERTING THE FIRST LETTER ACCORDING TO THE PIN NOMENCLATURE CHART AT RIGHT.

2. ALL SIGNALS THAT HAVE MODULE PINS ARE SO NOTED; MULTIPLE NOTATIONS OF THE SAME SIGNALS WITHIN A MODULE HAVE THE PIN NOTED ON EACH. AN INPUT SIGNAL IS NOTED ONLY ONCE PER SHEET UNLESS SEPERATE PINS ARE USED: MULTIPLE INPUTS ARE CONNECTED. MODULE OUTPUT SIGNALS ARE BROUGHT TO THE EXTREME RIGHT OF EACH SHEET.

3. PROCESSOR SIGNAL SOURCE NOTATION (K10-2, FOR EXAMPLE) IDENTIFIES THE SIGNAL SOURCE (PRINT AND MODULE). THE FIRST NUMBER AFTER THE K INDICATES THE MODULE PRINT SET, WHILE THE SECOND INDICATES THE SHEET WITHIN THE SET. IF ON A PRINT, THE FIRST NUMBER OF THE K PREFIXES COINCIDE FOR A SIGNAL NAME AND THE PRINT (SEE TITLE BLOCK), THE SIGNAL IS GENERATED ON THE MODULE. A DIFFERENCE IN THE FIRST NUMBER OF THE K PREFIXES INDICATES A SIGNAL GENERATED OFF THE MODULE. SIGNALS WITH A "BUS" PREFIX REPRESENT A "WIRED OR" SITUATION, AND MULTIPLE SOURCES FOR THE SIGNAL CAN EXIST.

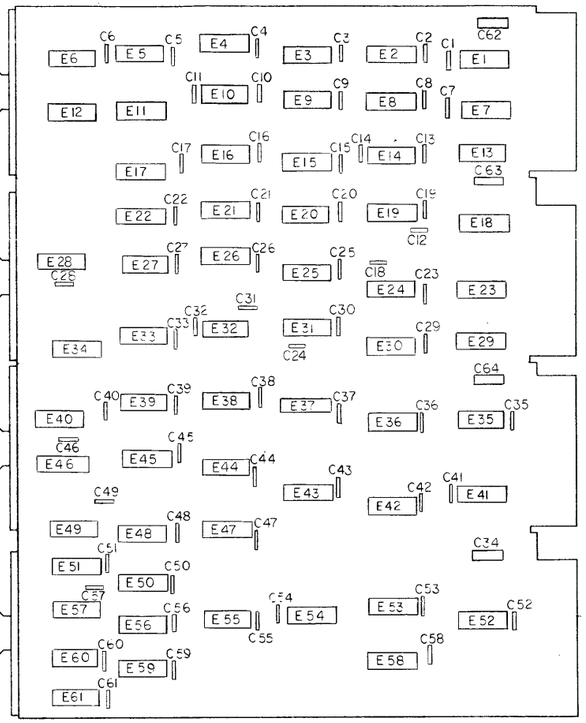
4. DETAILS ON COMPONENTS ARE NOTED IN THE PARTS REFERENCE, PLACEMENT IS NOTED IN THE COMPONENT PLACEMENT DIAGRAM.

5. GND AND +5V ARE USUALLY PIN 7 AND PIN 14, RESPECTIVELY. EXCEPTIONS ARE:

IC TYPE	GND	+5V
DEC 7481	PIN 10	PIN 4
DEC 7482	PIN 11	PIN 4
8251	PIN 8	PIN 16
DEC 8271	PIN 8	PIN 16
DEC 380	PIN 1	PIN 8
DEC 384	PIN 1	PIN 8

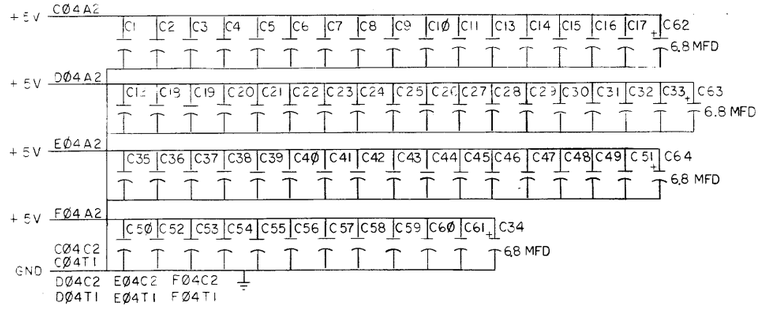
6. UNLESS OTHERWISE NOTED: RESISTANCE IS IN OHMS; CAPACITANCE IS IN PICOFARADS. CAPACITORS WITHOUT ANY NOTED VALUES ARE .01MFD

COMPONENT PLACEMENT



PIN NOMENCLATURE

MODULE	PROCESSOR
A	C
B	D
C	E
D	F



REVISIONS	REV	CHANGE NO	CHK

FIRST USED ON OPTION / MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP 11		PARTS LIST		

UNLESS OTHERWISE SPECIFIED
UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY √
REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL //
FINISH //

DRN.		DATE 2-4-70
CHK'D.		DATE 3/6/70
ENG.		DATE 3-6-70
PROJ. ENG.		DATE 3-6-70
PROD.		DATE

NEXT HIGHER ASSY: A-ML-KAII-Ø

SCALE //
SHEET 1 OF 4

digital EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS

TITLE: IR DECODE
M726 KIØ-1

SIZE	CODE	NUMBER	REV
D	CS	M726-Ø-1	A

ETCH REV. B
DIST. 1 Pier R

DEC FORM NO DRD 102A

K9-3 IRI5 (I)H — CØ4S2
K9-3 IRI4 (I)H — CØ4L2
K9-3 IRI3 (I)H — CØ4H1
K9-3 IRI2 (I)H — CØ4F1
K9-5 C (I)H — CØ4D2
K9-5 V (I)H — CØ4F2
K9-5 Z (I)H — CØ4T2
K9-5 N (I)H — CØ4A1
K9-3 IRI1 (I)L — CØ4N2
K9-3 IRIØ (I)L — CØ4P2
K9-3 IRØ9 (I)L — CØ4N1

E1 IRI4/12/5 DCDR 8251

K9-3 RSVD OPS H
KIØ-3 DEST MODE Ø L — CØ4E2
KIØ-2 IRI5 (I)L
K9-3 IRØ8 (I)H — CØ4B1
K9-4 IRØ7 (I)H — CØ4B2
K9-4 IRØ6 (I)H — CØ4U2
KIØ-2 IRØ8 (I) L
KIØ-2 (Z+C) H
KIØ-2 LESS H
KIØ-2 UNARY L
KIØ-2 IRI1 (I)H
KIØ-2 IRIØ (I)H
KIØ-2 IRØ9 (I)H
KIØ-2 IRI1/9 DCDR Ø L

KIØ-2 RSVD INSTR L
CØ4MI — KIØ-2 ADD L
CØ4M2 — KIØ-2 SUB L
CØ4E1 — KIØ-2 BINARY L
KIØ-2 BINARY BYTE OP H
KIØ-2 (ADD+SUB) H
CØ4DI — KIØ-2 JSR H
CØ4C1 — KIØ-2 JSR L
CØ4K2 — KIØ-2 BIS L
CØ4J2 — KIØ-2 BIC L
CØ4H2 — KIØ-2 BIT L
CØ4K1 — KIØ-2 CMP L
CØ4J1 — KIØ-2 MOV L
KIØ-2 RSVD ROT H
KIØ-2 TRAP L
KIØ-2 ROT L
KIØ-2 IRØ7 (I) L
KIØ-2 ROT RIGHT H
KIØ-2 EMT L
KIØ-2 ROT LEFT H
KIØ-2 IRØ6 (I)L
KIØ-2 SHF L
KIØ-2 SHF RIGHT H
KIØ-2 (ROT WD+SHF) H
CØ4L1 — KIØ-2 ROT/SHF L
DØ4H2 — KIØ-2 ROT/SHF R H
KIØ-2 ROT/SHF H
EØ4F2 — KIØ-2 ROT/SHF L H
KIØ-2 ROT BYTE H
KIØ-2 TRUE BR L
DØ4DI — KIØ-2 BRANCH H
KIØ-2 FALSE BR L
CØ4R2 — KIØ-2 BR L
KIØ-2 BR INSTR H
KIØ-2 TST L
DØ4CI — KIØ-2 SBC L
KIØ-2 ADC L
KIØ-2 NEG L
DØ4F2 — KIØ-2 DEC L
KIØ-2 INC L
KIØ-2 COM L
DØ4E2 — KIØ-2 CLR L

REVISIONS	REV.	CHANGE NO.	CHK

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP 11		PARTS LIST		

UNLESS OTHERWISE SPECIFIED
UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY
REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL
FINISH

DRN. K.Chilbrook DATE 11/25/69
CHK'D DATE 3/18/70
ENG. T.Clough DATE 3-10-70
PROJ. ENG. T.Clough DATE 3-10-70
PROD. DATE
NEXT HIGHER ASSY A-ML-KAII-Ø
SCALE
SHEET 2 OF 4

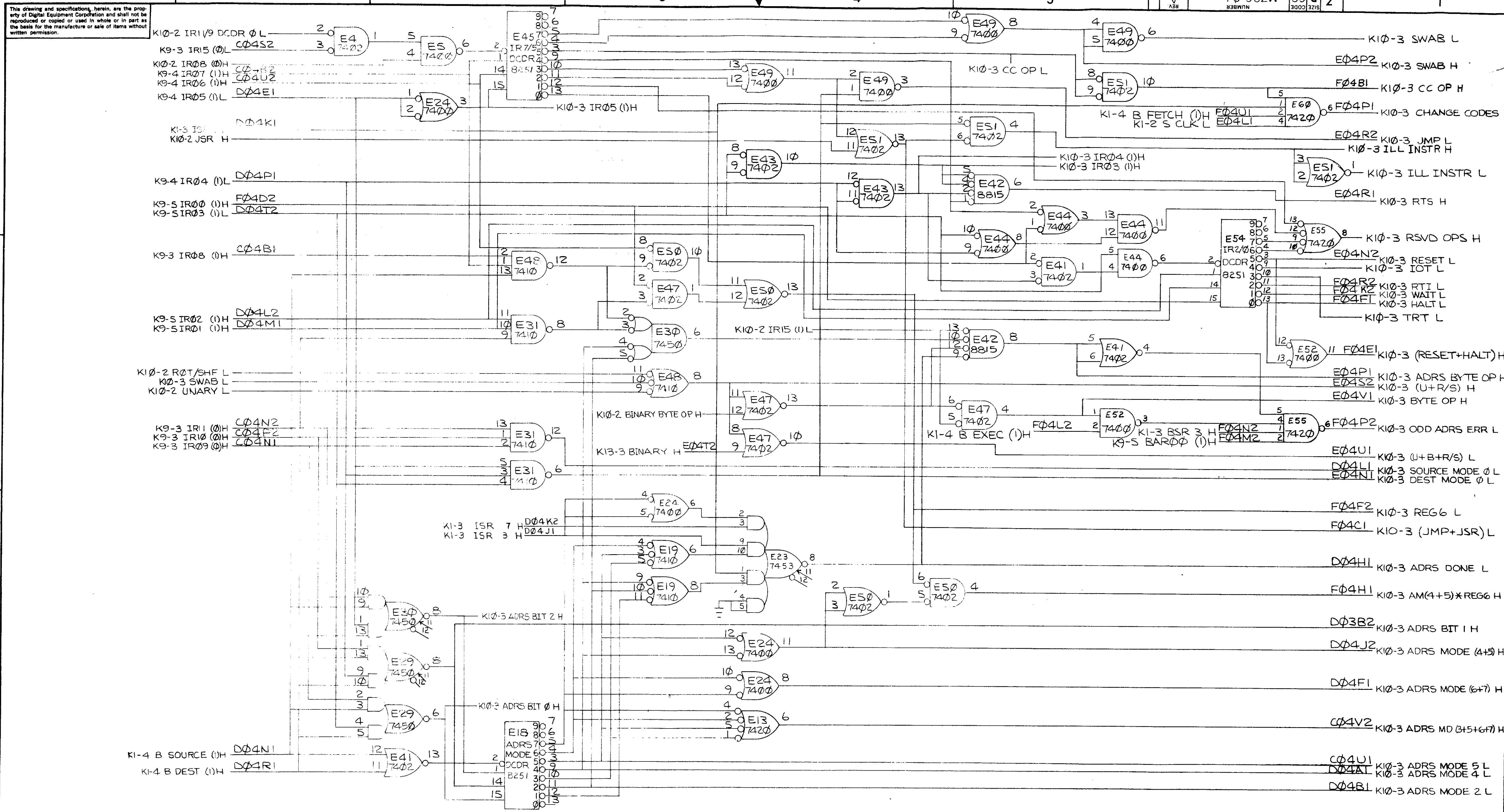
digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS

TITLE
IR DECODE
M726 KIØ-2

SIZE	CODE	NUMBER	REV.
D	CS	M726-0-1	A

DIST.

DEC FORM NO DRD 102A


KIØ-3 SWAB L
KIØ-3 SWAB H
KIØ-3 CC OP H
KIØ-3 CHANGE CODES L
KIØ-3 JMP L
KIØ-3 ILL INSTR H
KIØ-3 ILL INSTR L
KIØ-3 RTS H
KIØ-3 RSVD OPS H
KIØ-3 RESET L
KIØ-3 IOT L
KIØ-3 RTI L
KIØ-3 WAIT L
KIØ-3 HALT L
KIØ-3 TRT L
KIØ-3 (RESET+HALT) H
KIØ-3 ADRS BYTE OP H
KIØ-3 (U+R/S) H
KIØ-3 BYTE OP H
KIØ-3 ODD ADRS ERR L
KIØ-3 (U+B+R/S) L
KIØ-3 SOURCE MODE Ø L
KIØ-3 DEST MODE Ø L
KIØ-3 REG6 L
KIO-3 (JMP+JSR) L
KIØ-3 ADRS DONE L
KIØ-3 AM(4+5)*REG6 H
KIØ-3 ADRS BIT 1 H
KIØ-3 ADRS MODE (4+5) H
KIØ-3 ADRS MODE (6+7) H
KIØ-3 ADRS MD (3+5+6+7) H
KIØ-3 ADRS MODE 5 L
KIØ-3 ADRS MODE 4 L
KIØ-3 ADRS MODE 2 L
KIØ-3 IRØ5 (1)H
KIØ-3 CC OP L
KIØ-3 IRØ4 (1)H
KIØ-3 IRØ3 (1)H
KIØ-2 IR15 (1) L
KIØ-2 BINARY BYTE OP H
KI3-3 BINARY H
KIØ-3 ADRS BIT 2 H
KIØ-3 ADRS BIT Ø H
KI-4 B FETCH (1)H
KI-2 S CLK L
KI-4 B EXEC (1)H
KI-3 BSR 3 H
K9-5 BARØØ (1)H
KI-3 ISR 7 H
KI-3 ISR 3 H
KI-4 B SOURCE (1)H
KI-4 B DEST (1)H
KIØ-2 IR11/9 DCDR Ø L
K9-3 IR15 (Ø)L
KIØ-2 IRØ8 (Ø)H
K9-4 IRØ7 (1)H
K9-4 IRØ6 (1)H
K9-4 IRØ5 (1)L
KIØ-2 JSR H
K9-4 IRØ4 (1)L
K9-5 IRØØ (1)H
K9-5 IRØ3 (1)L
K9-3 IRØ8 (1)H
K9-5 IRØ2 (1)H
K9-5 IRØ1 (1)H
KIØ-2 RØT/SHF L
KIØ-3 SWAB L
KIØ-2 UNARY L
K9-3 IR11 (Ø)H
K9-3 IR1Ø (Ø)H
K9-3 IRØ9 (Ø)H

FIRST USED ON OPTION/MODEL: PDP 11	QTY.	DESCRIPTION	PART NO. / ITEM NO.

PARTS LIST

UNLESS OTHERWISE SPECIFIED — DIMENSION IN INCHES — TOLERANCES: DECIMALS ± .005, FRACTIONS ± 1/64, ANGLES ± 0°30' — FINAL SURFACE QUALITY — REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL — FINISH

DRN. R. Phillips, DATE 11/29
CHK'D, DATE 3/18/70
ENG., DATE 3-6-70
PROJ. ENG., DATE 3-6-70
PROD., DATE

NEXT HIGHER ASSY: A-ML-KA11-Ø
SCALE: NONE
SHEET 3 OF 4

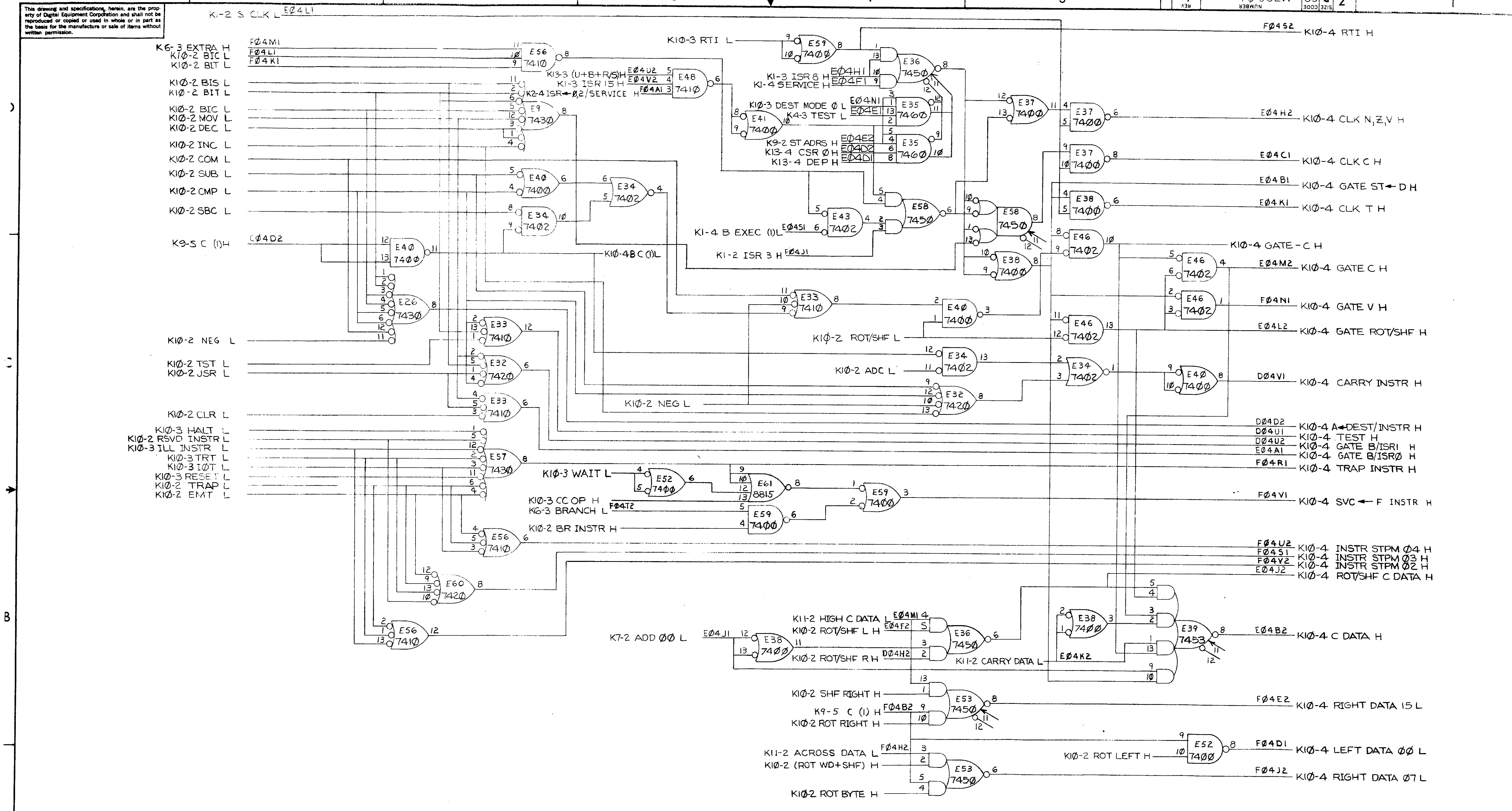
digital EQUIPMENT CORPORATION, MAYNARD, MASSACHUSETTS

TITLE: IR DECODE

M726 KIØ-3

SIZE D, CODE CS, NUMBER M726-Ø-1, REV. A

DEC FORM NO DRD 102A



K1-2 S CLK L
K6-3 EXTRA H
K1Ø-2 BIC L
K1Ø-2 BIT L
K1Ø-2 BIS L
K1Ø-2 BIT L
K1Ø-2 BIC L
K1Ø-2 MOV L
K1Ø-2 DEC L
K1Ø-2 INC L
K1Ø-2 COM L
K1Ø-2 SUB L
K1Ø-2 CMP L
K1Ø-2 SBC L
K9-5 C (1)H
K1Ø-2 NEG L
K1Ø-2 TST L
K1Ø-2 JSR L
K1Ø-2 CLR L
K1Ø-3 HALT L
K1Ø-2 RSVD INSTR L
K1Ø-3 ILL INSTR L
K1Ø-3 TRT L
K1Ø-3 IØT L
K1Ø-3 RESET L
K1Ø-2 TRAP L
K1Ø-2 EMT L
K1Ø-3 RTI L
K1-3 ISR 8 H
K1-4 SERVICE H
K1Ø-3 DEST MODE Ø L
K4-3 TEST L
K9-2 ST ADRS H
K13-4 CSR Ø H
K13-4 DEP H
K1-4 B EXEC (1)L
K1-2 ISR 3 H
K1Ø-4 B C (1)L
K1Ø-2 ROT/SHF L
K1Ø-2 ADC L
K1Ø-2 NEG L
K1Ø-3 WAIT L
K1Ø-3 CC OP H
K6-3 BRANCH L
K1Ø-2 BR INSTR H
K7-2 ADD ØØ L
K11-2 HIGH C DATA L
K1Ø-2 ROT/SHF L H
K1Ø-2 ROT/SHF R H
K11-2 CARRY DATA L
K1Ø-2 SHF RIGHT H
K9-5 C (1) H
K1Ø-2 ROT RIGHT H
K11-2 ACROSS DATA L
K1Ø-2 (ROT WD+SHF) H
K1Ø-2 ROT BYTE H
K1Ø-2 ROT LEFT H
K1Ø-4 RTI H
K1Ø-4 CLK N,Z,V H
K1Ø-4 CLK C H
K1Ø-4 GATE ST← D H
K1Ø-4 CLK T H
K1Ø-4 GATE - C H
K1Ø-4 GATE C H
K1Ø-4 GATE V H
K1Ø-4 GATE ROT/SHF H
K1Ø-4 CARRY INSTR H
K1Ø-4 A←DEST/INSTR H
K1Ø-4 TEST H
K1Ø-4 GATE B/ISR1 H
K1Ø-4 GATE B/ISRØ H
K1Ø-4 TRAP INSTR H
K1Ø-4 SVC←F INSTR H
K1Ø-4 INSTR STPM Ø4 H
K1Ø-4 INSTR STPM Ø3 H
K1Ø-4 INSTR STPM Ø2 H
K1Ø-4 ROT/SHF C DATA H
K1Ø-4 C DATA H
K1Ø-4 RIGHT DATA 15 L
K1Ø-4 LEFT DATA ØØ L
K1Ø-4 RIGHT DATA Ø7 L

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.
PDP 11		PARTS LIST	

UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY
REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL —
FINISH —

DRN. DATE 12/2/69
CHK'D. DATE 3/18/70
ENG. DATE 3-6-70
PROJ. ENG. DATE 3-6-70
PROD. DATE

NEXT HIGHER ASSY: A-ML-KA11-Ø

digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS

TITLE
IR DECODE
M726 K1Ø-4

SIZE D CODE CS NUMBER M726-Ø-1 REV. A
SCALE NONE
SHEET 4 OF 4
DIST.

DEC FORM NO. DRD 102A