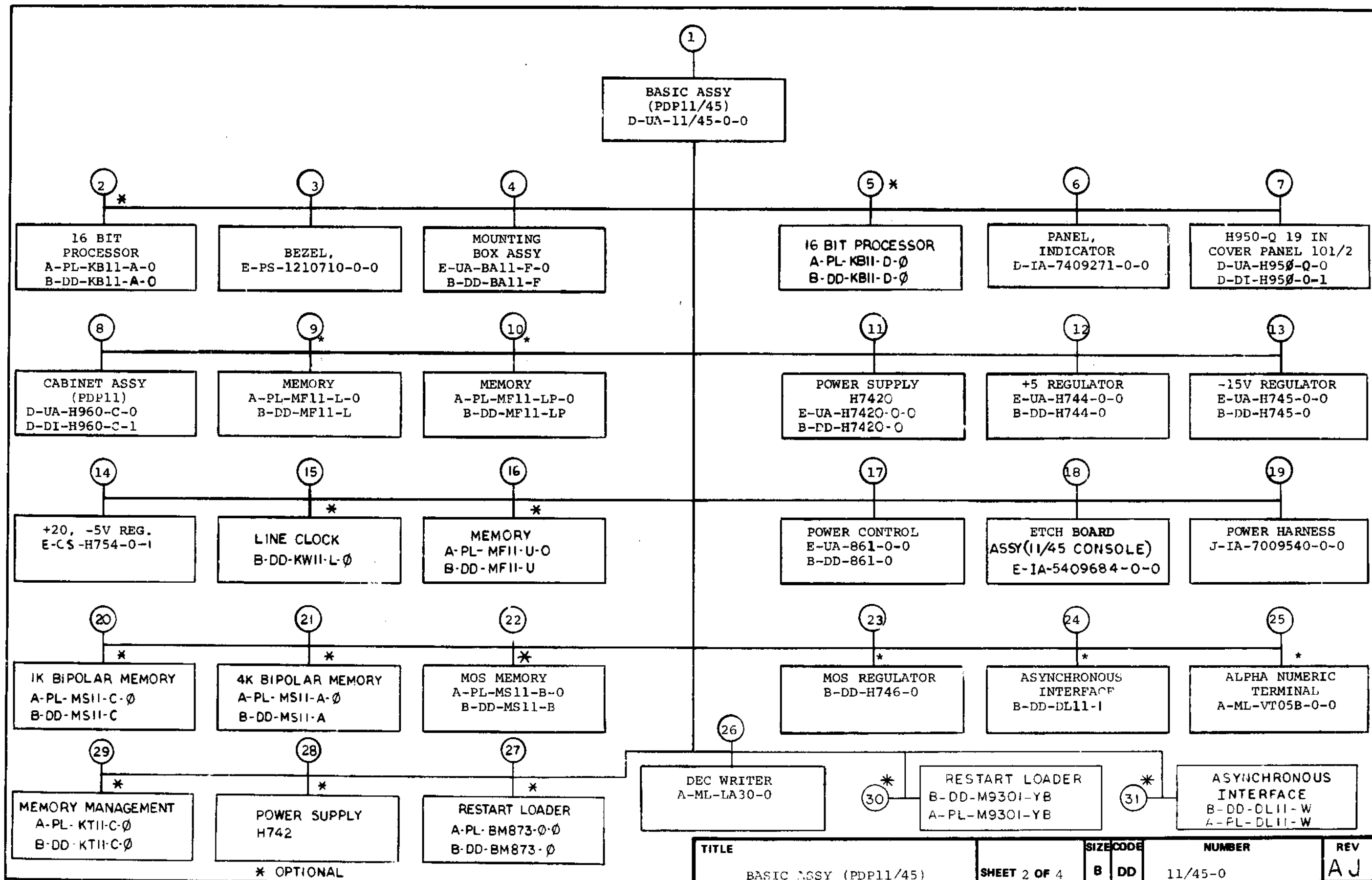


**PDP-11/45
system
engineering drawings**

digital equipment corporation · maynard, massachusetts



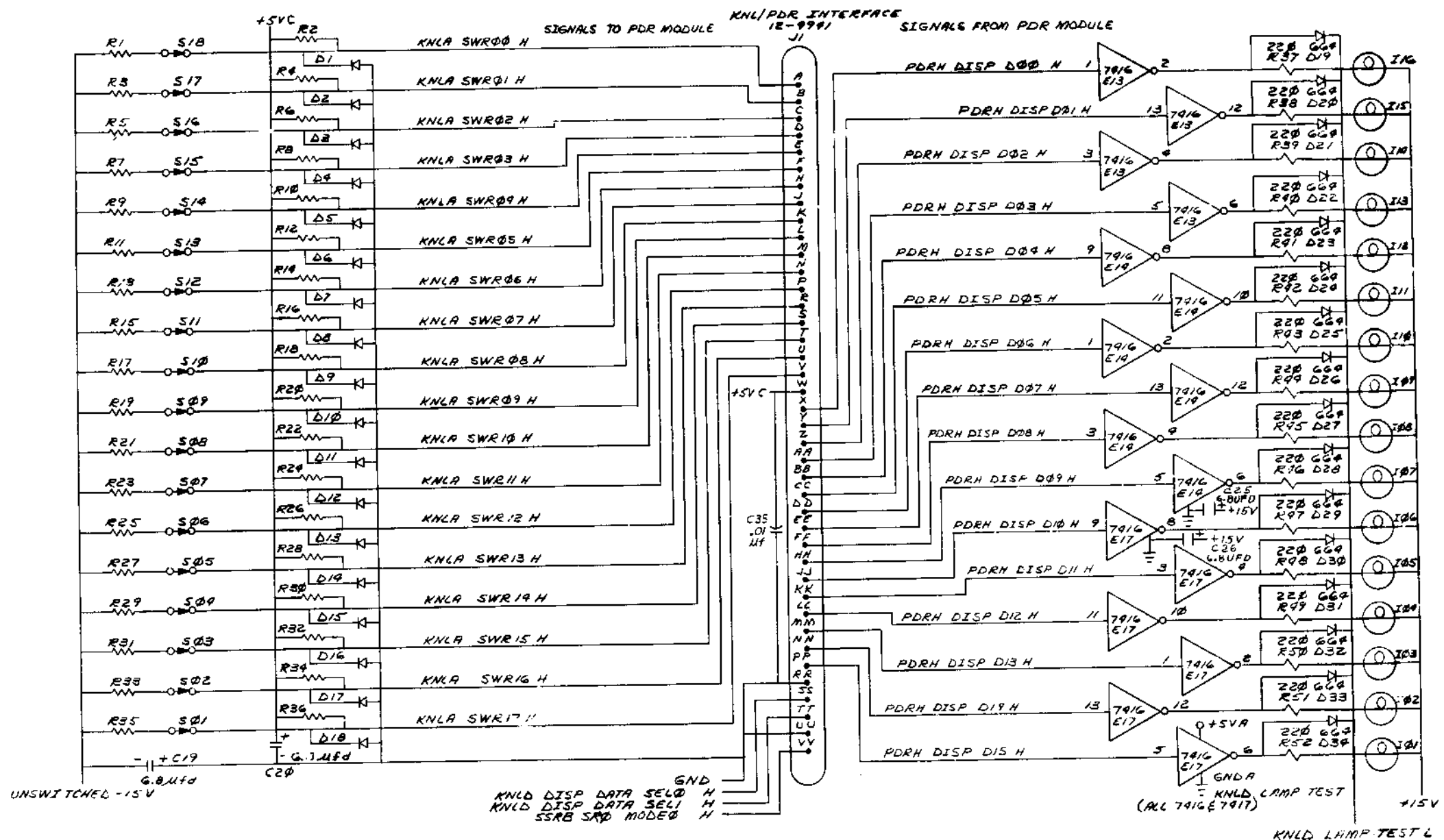


TITLE	SHEET	SIZE	CODE	NUMBER	REV
BASIC ASSY (PDP11/45)	2 OF 4	B	DD	11/45-0	AJ

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"SWITCH REGISTER" SWITCHES SHOWN IN LOGICAL "0" STATE (DOWN)

- NOTES:
- CONTROL LOGIC AND DISPLAY GROUNDS MUST BE KEPT SEPARATE
 - ALL RESISTORS ARE 2.2K UNLESS MARKED OTHERWISE
 - ALL UNMARKED DIODES ARE D664

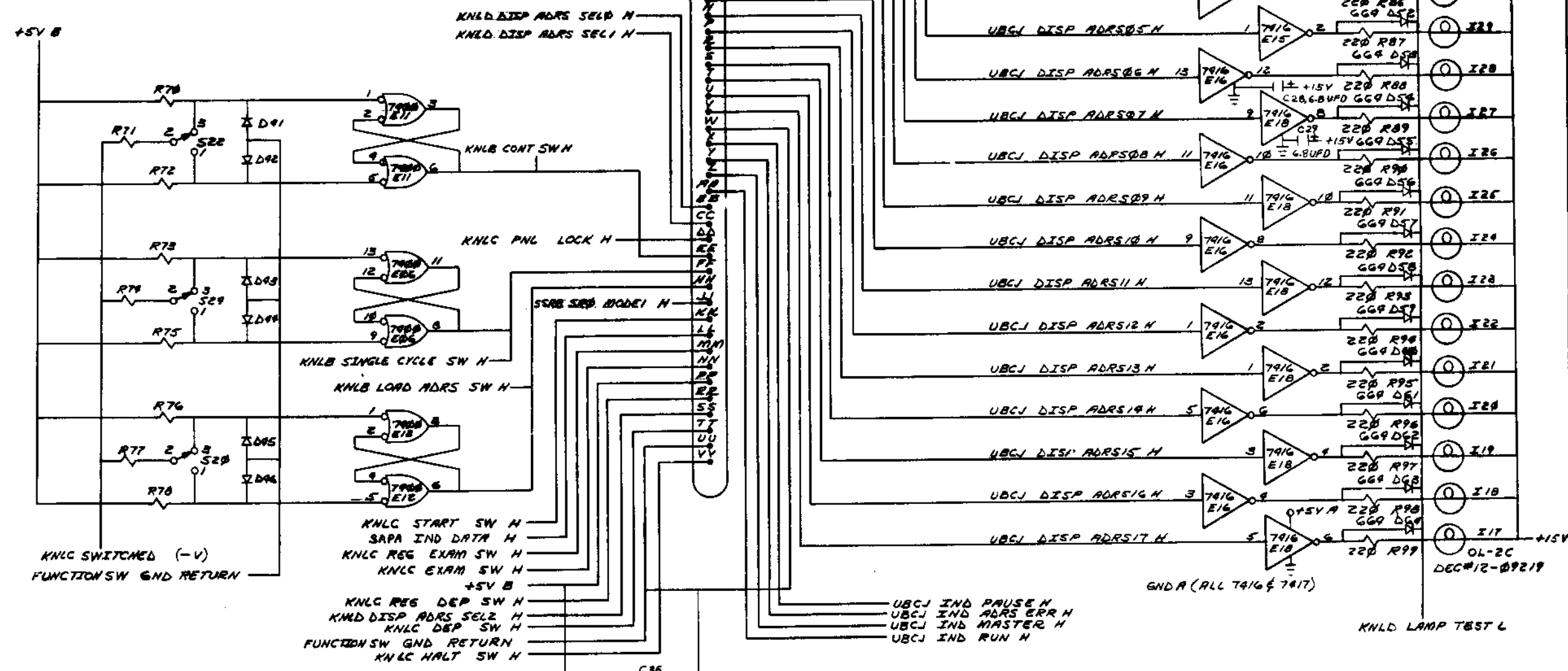


REV	DATE	BY	CHKD	DESCRIPTION
1	11-14-74	J. SWANSON		INITIAL DESIGN
2	12-11-74	J. SWANSON		REVISED FOR MANUFACTURE
3	1-10-75	J. SWANSON		REVISED FOR MANUFACTURE
4	2-10-75	J. SWANSON		REVISED FOR MANUFACTURE
5	2-10-75	J. SWANSON		REVISED FOR MANUFACTURE
6	2-10-75	J. SWANSON		REVISED FOR MANUFACTURE
7	2-10-75	J. SWANSON		REVISED FOR MANUFACTURE
8	2-10-75	J. SWANSON		REVISED FOR MANUFACTURE

FIRST USED ON OPTION/MODEL	QTY	DESCRIPTION	PART NO.	ITEM NO.
11/45				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED: DIMENSION IN INCHES TOLERANCES		DATE	DIGITAL EQUIPMENT CORPORATION	
DECIMALS	ANGLE	DATE	TITLE	
.XX - .006	10° 30'	12/17/74	11/45 CONSOLE BOARD	
XX - .02		1/15/75	KNLA	
K - .1		2/10/75	REV. H	
REMOVE BLARS AND BREA. SHARP CORNERS SURFACE QUANTITY		PROD. DATE	NUMBER	
MATERIAL		DATE	DCC 5409684-0-1	
NEXT HIGHER ASSY		SCALE		
B-DD-11/45-0		SHEET 1 OF 4		
FINISH		DIST		

The drawing and specifications shall be the basis for the procurement of material or work or in part in the form for the manufacturer or any of them unless otherwise specified.

NOTES:
 CONTROL SWITCHES ARE SHOWN IN THEIR NORMAL OR NON OPERATIVE POSITION. SWITCHES WITH ARROWS POINTING UP ARE PUSHED DOWN TO OPERATE THOSE WITH ARROWS DOWN ARE LIFTED TO OPERATE.
 CONTROL SWITCHES 525 THRU 528, 519 THRU 522 ARE MOMENTARY SPST.
 ALL TURNS ON SWITCHES 1 THRU 8 ARE TO BE CONNECTED TO +5V B AND FUNCTION SW GND RETURN.

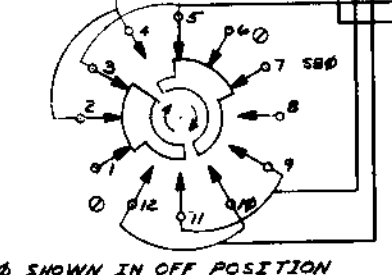
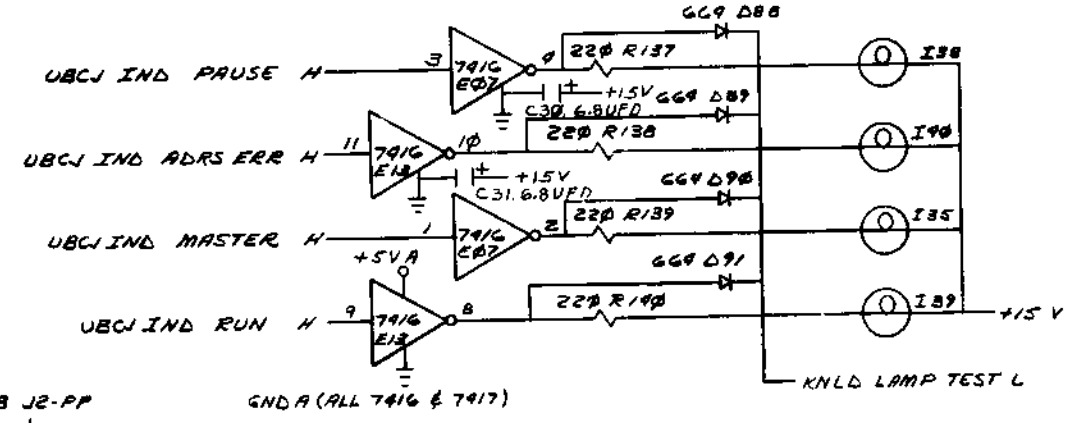
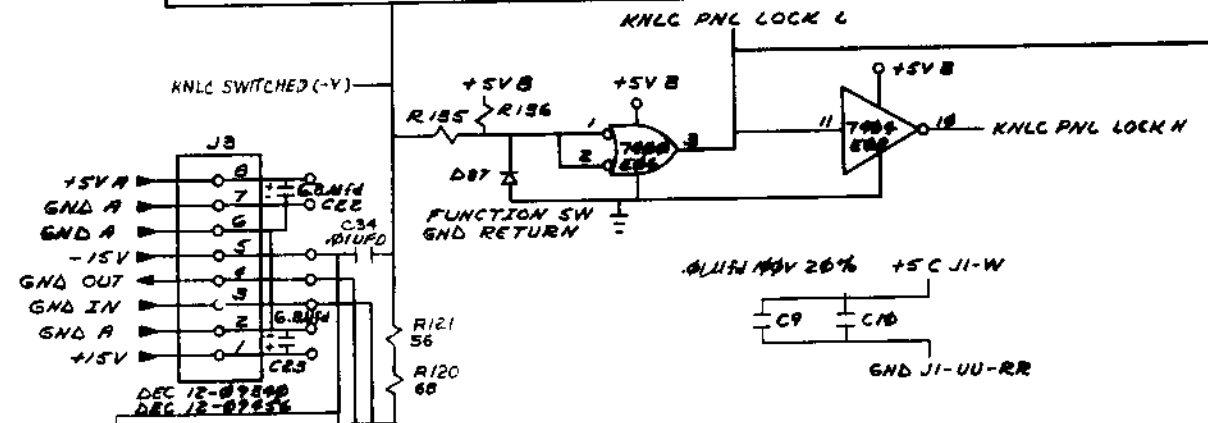
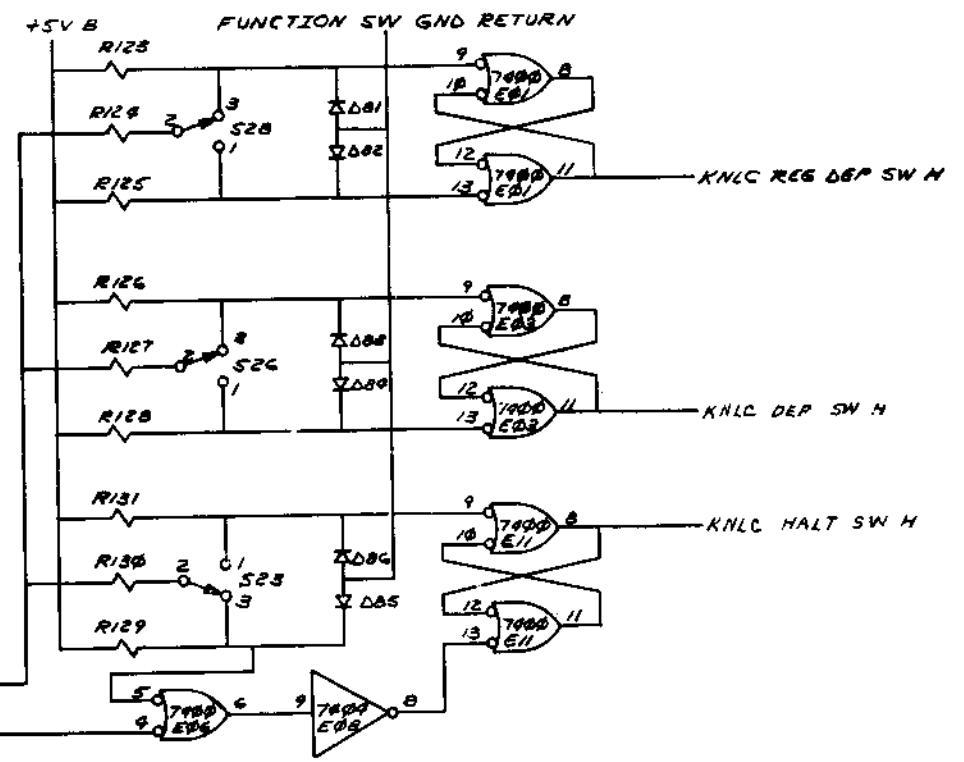
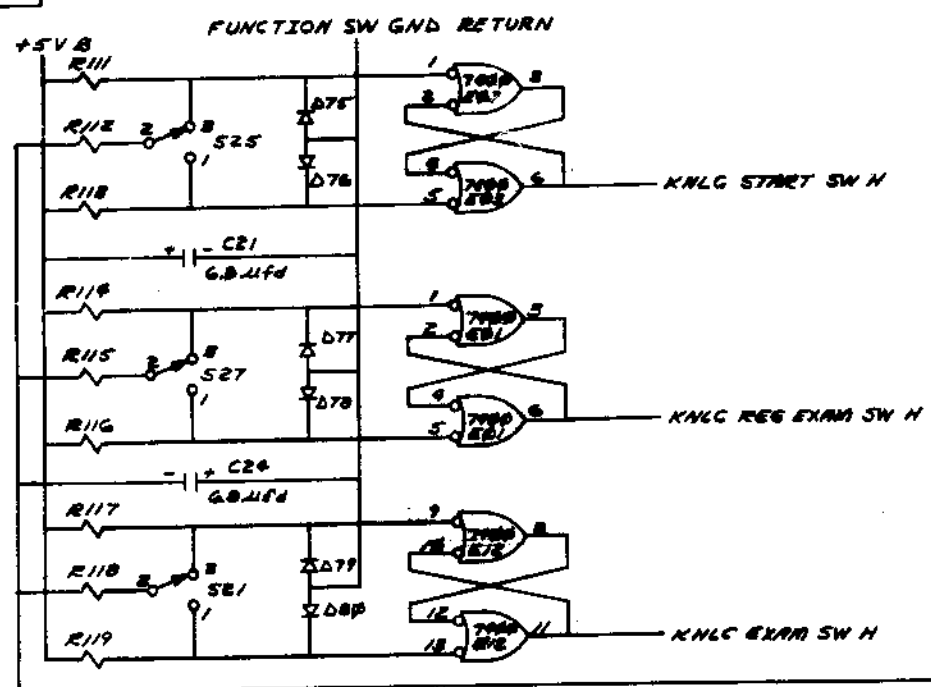


FIRST USED ON OPTION/MODEL	QTY	DESCRIPTION	PART NO.
11145			
PARTS LIST			
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES	DRN <i>Calder</i>	DATE 12-24-71	EQUIPMENT CORPORATION 11/45 CONSOLE BOARD
DECIMALS ANGLES	CHK <i>Calder</i>	DATE 1/17/72	
.XX - .006 .XX - .02 .X - .1		DATE 3/27/71	KNLB
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	PROJ. <i>Calder</i>	DATE 3/27/71	
MATERIAL	NEXT HIGHER ASSY.	DATE 8-30-72	REV. N
FINISH	B-00-11145-0	SCALE NONE	
	SHEET 2 OF 4	DIST.	

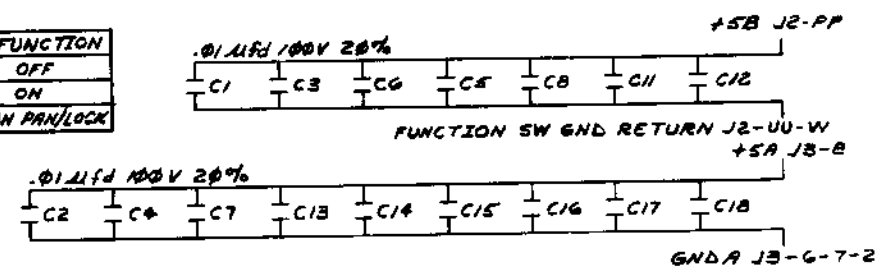
REV. 10-22-70
 CHANGE NO.
 DDC FORM NO. DD FORM 10-4

DCS 5409684-0-1

Indicate from the top of the page the revision and date of the drawing. Do not use the same number for a revision of the drawing as for a revision of the equipment or parts of same without the approval of the design engineer.



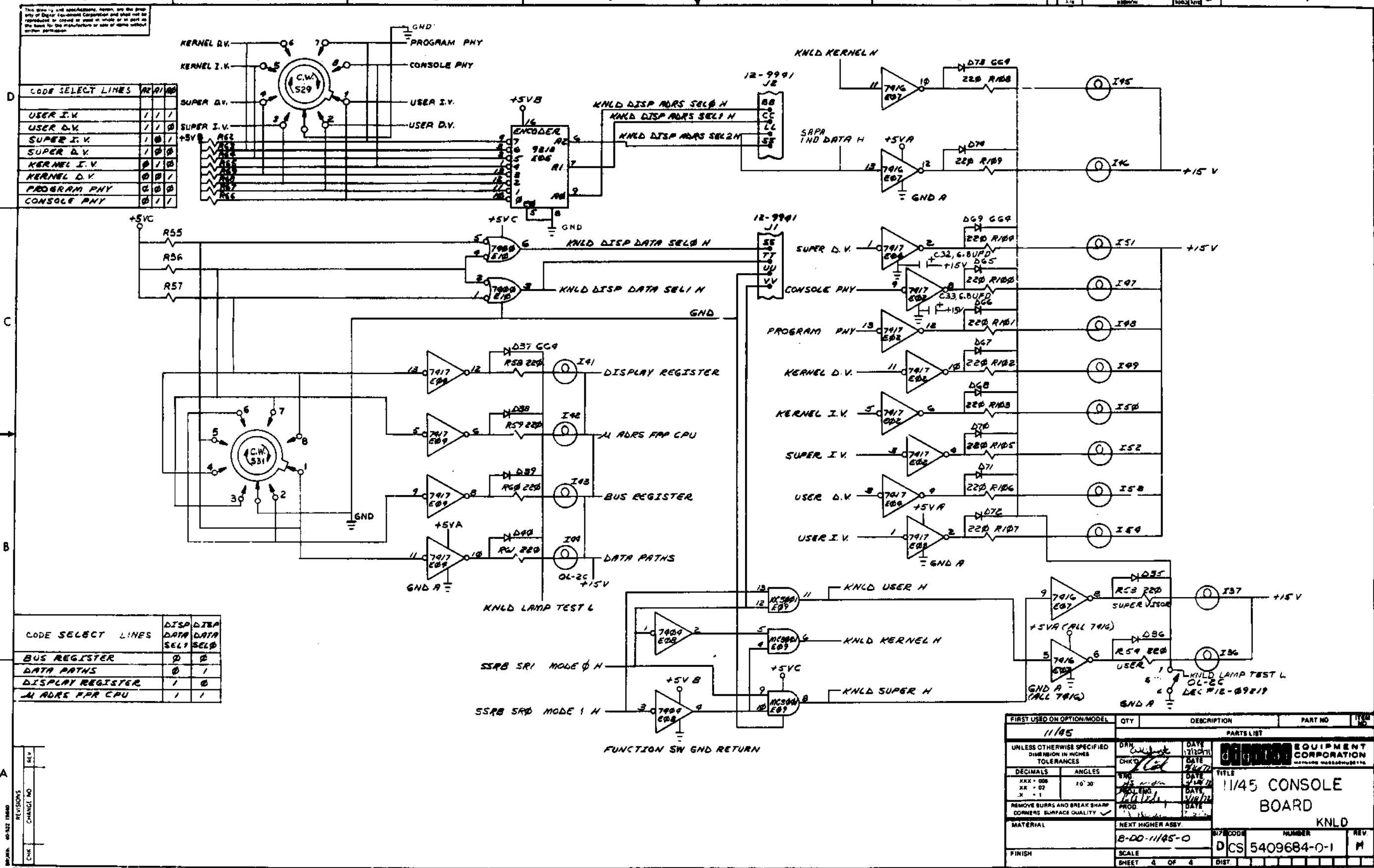
POSITION	POLE	FUNCTION
POS 1	2, 3	OFF
POS 2	1, 5, 8, 9	ON
POS 3	1, 12	ON PAN/LOCK



FIRST USED ON OPTION/MODEL	QTY	DESCRIPTION	PART NO.	ITEM NO.	
11/45					
PARTS LIST					
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES		DATE	digital EQUIPMENT CORPORATION		
DECIMALS	ANGLES	DATE	11/45 CONSOLE BOARD		
.XXX - .006	10' 30'	3/25/72	KNLC		
.XX - .02		8/28/72	REV. 4		
X - .1		3-30-72	B-DD-11/45-0		
REMOVE VRRS AND BR. AX SHARP CORNER SURFACE QUALITY			DCS 5409684-0-1		
MATERIAL	NEXT HIGHER ASSY.	SCALE	SHEET 3 OF 4		
	B-DD-11/45-0				
FINISH					

REV. 4
CHANGE NO.
DATE

DCS 5409684-0-1

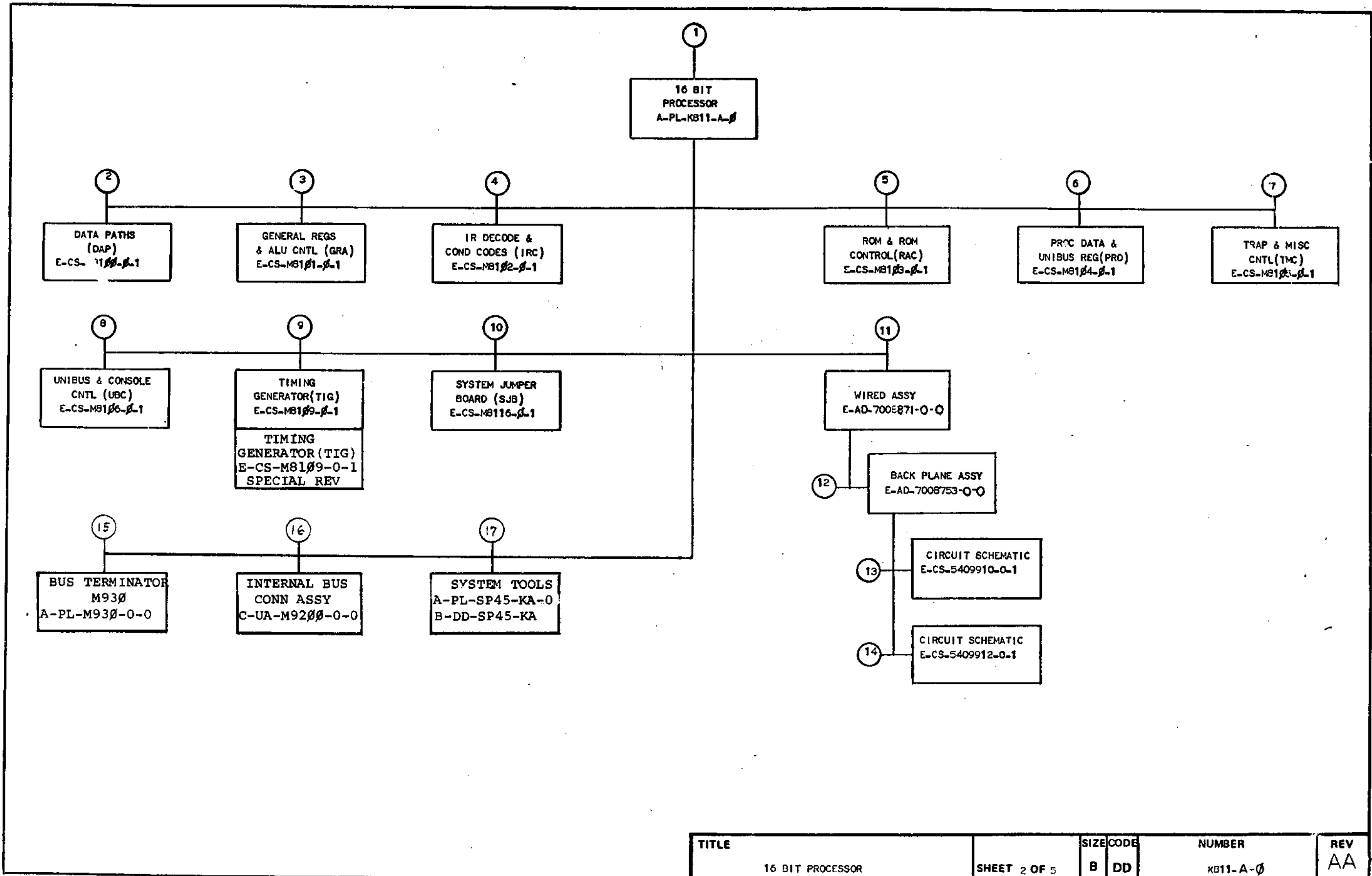


CODE SELECT LINES	AV	AV	AV
USER I.V.	1	1	1
USER D.V.	1	1	0
SUPER I.V.	1	0	1
SUPER D.V.	1	0	0
KERNEL I.V.	0	1	0
KERNEL D.V.	0	0	1
PROGRAM PNY	0	0	0
CONSOLE PNY	0	1	1

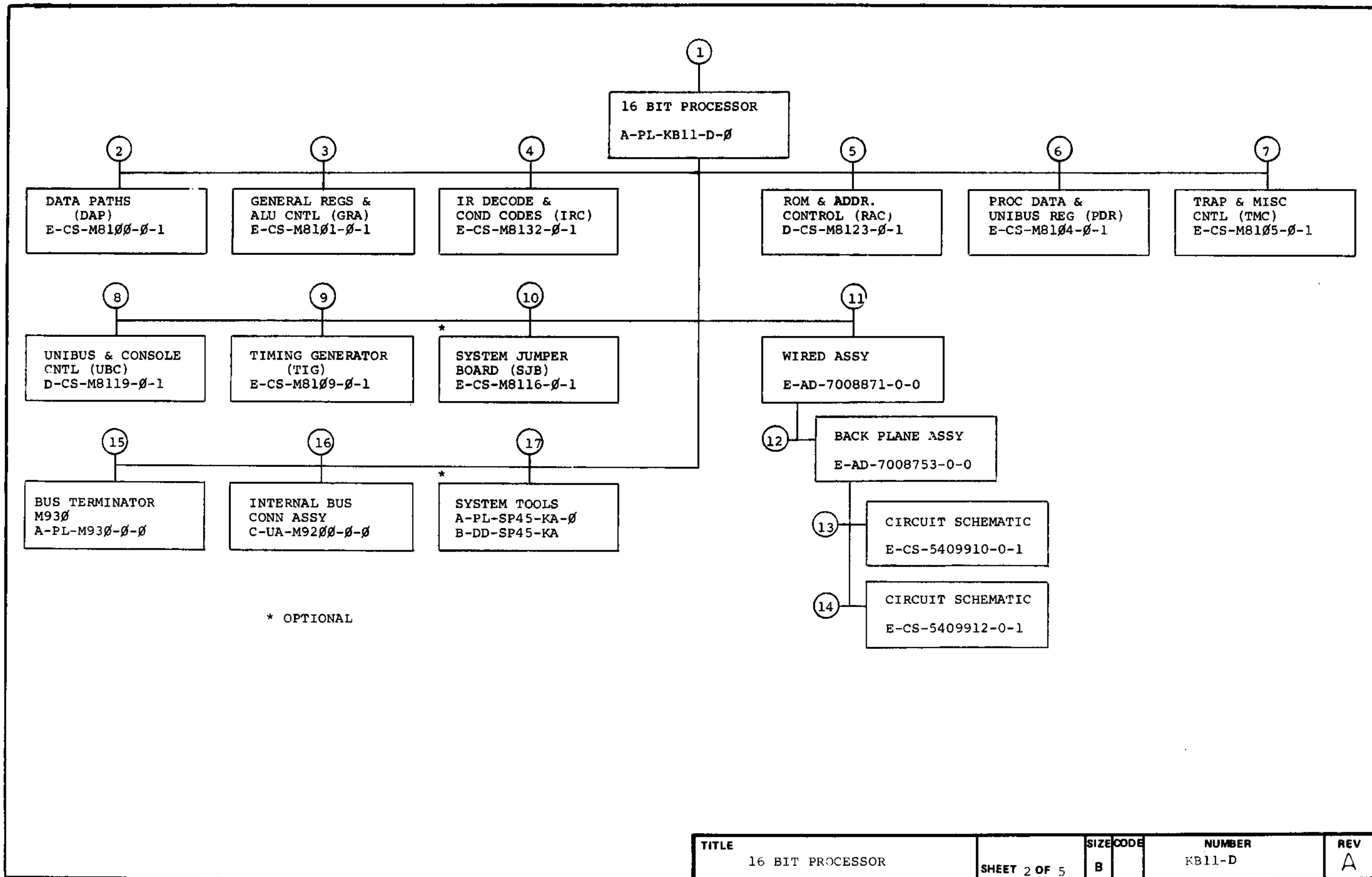
CODE SELECT LINES	DISP DATA SEL1	DATA DATA SEL0
BUS REGISTER	0	0
DATA PATHS	0	1
DISPLAY REGISTER	1	0
M ADRS FAP CPU	1	1

REV	CHG	NO

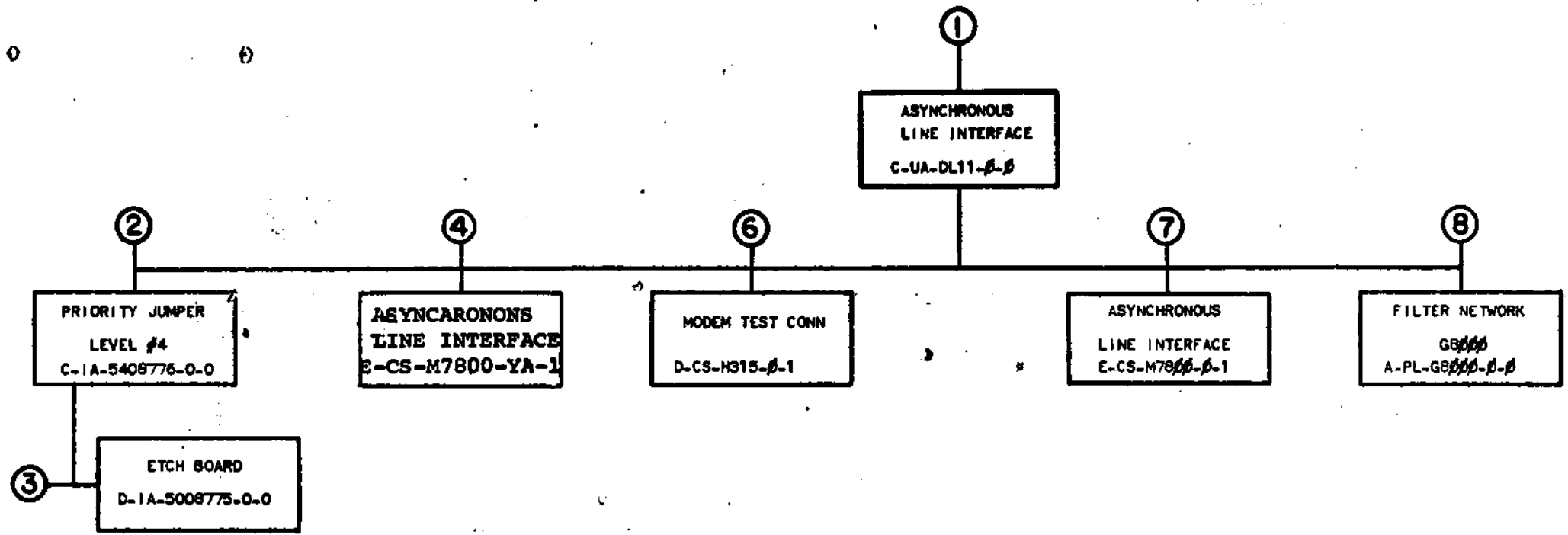
FIRST USED ON OPTION/MODEL	QTY	DESCRIPTION	PART NO	TITLE
11/85				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES	DATE 12/27/71	DATE 1/14/72	EQUIPMENT CORPORATION	
DECIMALS ANGLES	CHK'D JEL	DATE 1/14/72	TITLE 11/45 CONSOLE BOARD	
XXX - 000 XX - 02 X - 1	ENG JEL	DATE 1/14/72	MATERIAL	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	PROD JEL	DATE 2/2/72	NEXT HIGHER ASSY. B-00-11/45-0	
FINISH	SCALE	SHEET 4 OF 4	DIST	REV M



TITLE	SHEET	SIZE	CODE	NUMBER	REV
16 BIT PROCESSOR	2 OF 5	B	DD	KB11-A-0	AA

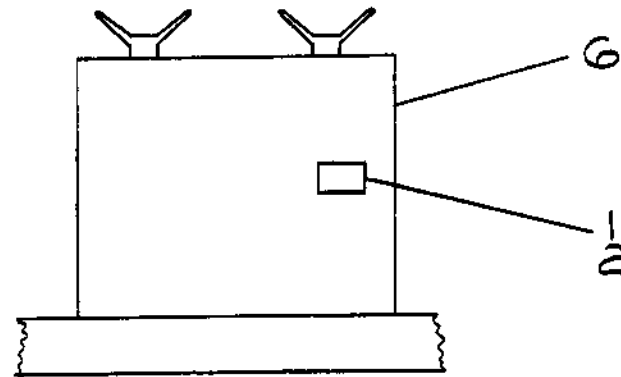


TITLE	SHEET	SIZE	CODE	NUMBER	REV
16 BIT PROCESSOR	2 OF 5	B		KB11-D	A



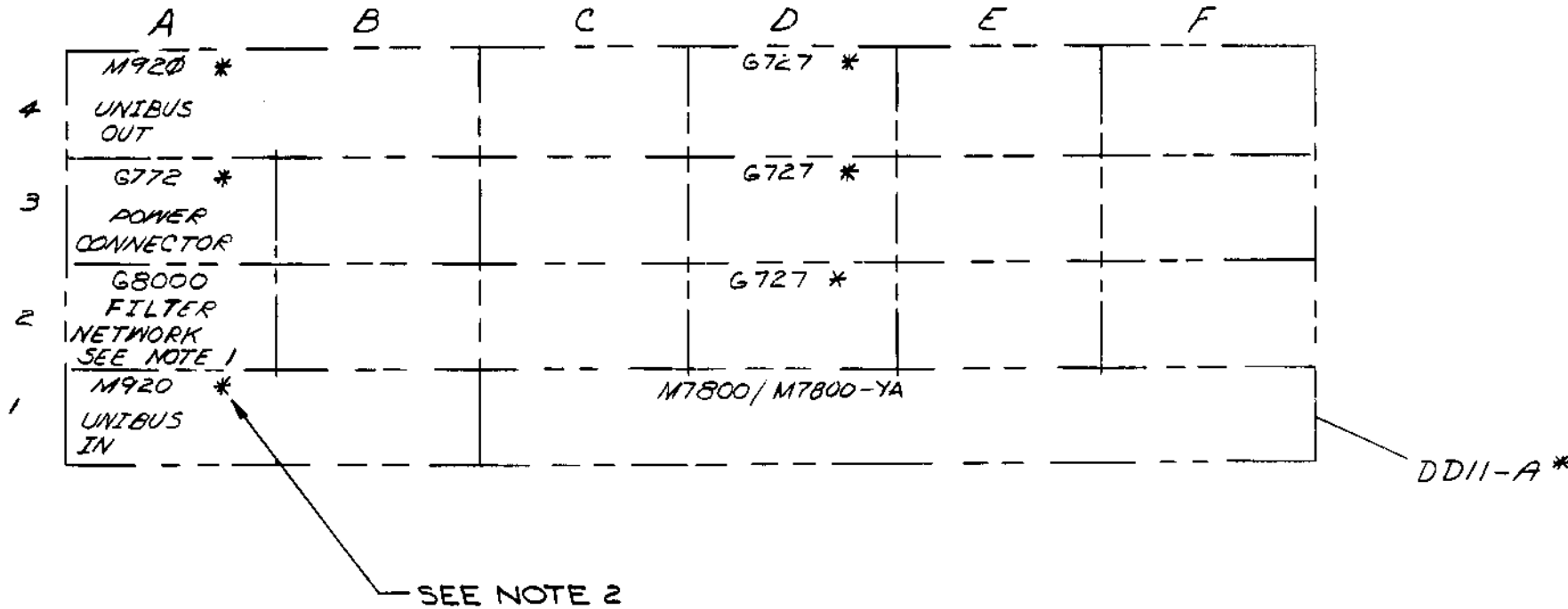
TITLE	ASYNCHRONOUS LINE INTERFACE	SHEET 2 OF 3	SIZE CODE B DD	NUMBER DL11-Ø	REV K
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1972



NOTES:

- G 8000 IS REQUIRED ONLY IN PDP 11 SYSTEMS WHERE +15V IS NOT AVAILABLE. THE INSTALLATION REQUIRES 2 WIRES TO BE ADDED.
A03V2-A02V2
A02N2-CXXUI
WHERE (XX) IS THE SLOT NUMBER CONTAINING THE DLII.
- ITEMS INDICATED WITH ASTERICK (*) ARE SHOWN FOR REFERENCE ONLY AND ARE NOT PART OF THIS UNIT.



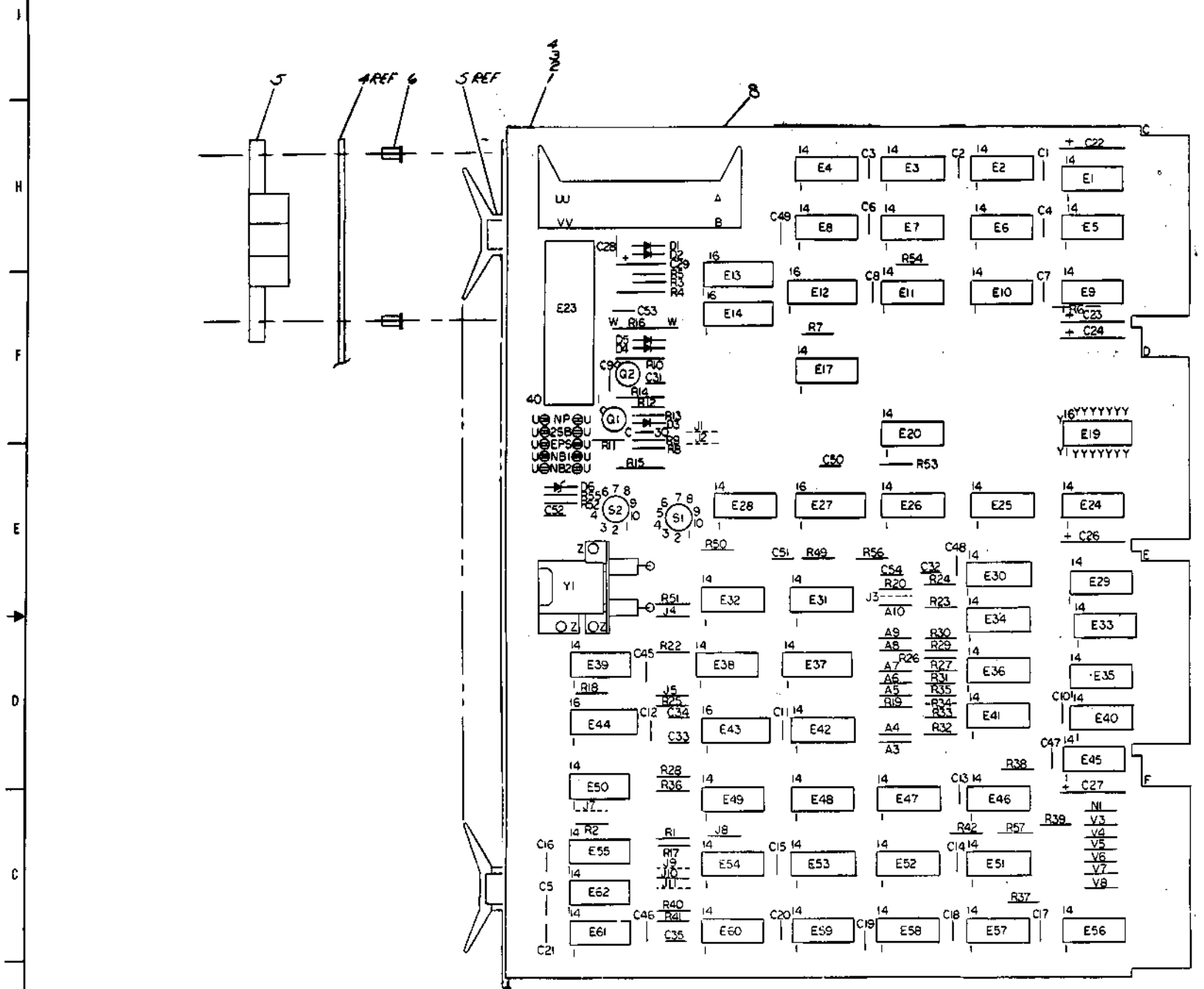
REV.	CHG. NO.	REV.
A	DLII-00001	
B	DLII-00002	
C	DLII-00005	
D	DLII-00006	
E	DLII-00008	
F	DLII-00009	

F. JANSON
 J. JANSON
 L. CONDON
 J. MCINTYRE

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP-11		PARTS LIST		
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES	DRN. <i>M. Condon</i>	DATE 3/10/72	digital EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS	
DECIMALS .XXX = .005 .XX = .02 .X = .1	CHK'D. <i>J. Janon</i>	DATE 2-24-72		
ANGLES ±0° 30'	ENG. <i>P. E. Janon</i>	DATE 5-11-72	TITLE ASYNCHRONOUS LINE INTERFACE	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ✓	PROJ. ENGR. <i>P. E. Janon</i>	DATE 5-11-72		
MATERIAL	RROD. <i>J. McIntyre</i>	DATE 5-15-72	SIZE CODE	NUMBER
FINISH	NEXT HIGHER ASSY.		CUA	DLII-0-0
	B-00-DLII-0		REV.	F
	SCALE NONE		DIS:	
	SHEET 1 OF 1			

DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS				QUANTITY / VARIATION																			
PARTS LIST																							
MADE BY M. PIERCE		CHECKED J. FERGUSON		SECTION																			
DATE 4/27/72		DATE 4/27/72		1																			
ENG P. E. JANSON		PROD <i>J. Mc Jolye</i>		ISSUED SECT.																			
DATE 5/11/72		DATE 5/15/72		1																			
ITEM NO.	DWG NO. / PART NO.	DESCRIPTION		DL11-A	DL11-B	DL11-C	DL11-D	DL11-E															
1	C-IA-5408776-0-0	PRIORITY JUMPER LEVEL #4		1	1	1	1	1															
3	D-UA-BC05C-25	CABLE MODEM BC05C		-	1	-	1	1															
4	D-IA-7008360-0-0	CABLE ASSEMBLY (KL8E)		1	-	1	-	-															
5	D-CS-H315-0-1	MODEM TEST CONNECTOR		-	-	-	-	A/R	See Note 2														
6	E-CS-M7800-0-1	ASYNCHRONOUS LINE INTERFACE		-	1	-	1	1															
7	A-PL-G8000-0-0	FILTER NETWORK		A/R A/R A/R See Note 1																			
8		CRYSTAL		A/RA/RA/RA/RA/R See Note 3																			
9	E-CS-M7800-YA-1	ASYNCHRONOUS LINE INTERFACE		1	-	1	-	-															
10	9008269	TRANSPARENT VINYL TAPE		A/R																			
NOTES:																							
1. G8000 IS REQUIRED ONLY IN PDP11 SYSTEMS WHERE +15V IS NOT AVAILABLE. ONE PER DD11-A																							
2. ONE H315 PER PDP11 SYSTEM																							
3. CRYSTAL FREQUENCY DEFINED BY CUSTOMER SPECIFIED BAUD RATE OR BY THE DOCUMENTATION OF AN OPTION WHICH USES THE DL11.																							
4. APPLY TAPE TO TOP SURFACES OF CRYSTAL AND MOUNTING BRACKETS TO INSULATE FROM ADJACENT MODULES.																							
5. PRIORIY LEVELS 5, 6, or 7 MAY BE SPECIFIED BY THE CUSTOMER OR THE DOCUMENTATION OF AN OPTION WHICH USES THE DL11.																							
TITLE		ASYNCHRONOUS LINE INTERFACE		ASSY NO.		C-UA-DL11-0-0		SIZE CODE		A PL		NUMBER		DL11-0-0		REV.		F		ECO NO.		DL11-00009	
SHEET 1 OF 1				DIST.																			

Detailed Equipment Description

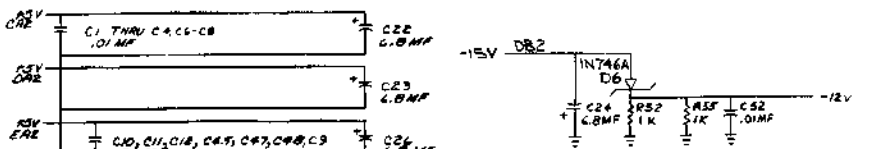


NOTES:
 1.) PIN NOTATION THROUGHOUT IS ORDERED UPON MODULE PLACEMENT IN THE SYSTEM UNIT. MODULE REFERENCE ALONE IS OBTAINED BY CONVERTING THE FIRST LETTER ACCORDING TO THE PIN NOMENCLATURE CHART AT THE LEFT.
 2.) JUMPERS TO BE USED AT CONNECTIONS A3-A10, J4-J5, J8, J10, J3-16, AND #1.
 3.) LETTERS ENCLOSED IN PARENTHESIS REFER TO PINS ON THE BERG CONNECTOR. EXAMPLE: (X).
 4.) DEC 8640S WERE PHASE 1 AND 200 REPLACEMENTS ANY 200 FAILURES SHOULD BE REPLACED BY 8640-1, EXCEPT E28. E28 MUST BE REPLACED WITH A 7380 CHIP.

PIN NOMENCLATURE
MODULE SYSTEM UNIT

QTY	REF DESIGNATION	DESCRIPTION	PART NO.
1	E28	WASHERS INTERNAL TOOTH (DEC 7380)	191039C
10	J1 THRU J19	JUMPER INSULATED	7300101
1	R5	R25 750Ω 1/4W 5%	7301401
1	R28	R28 300Ω 1/4W 5%	7300301
1	R30	R30 100Ω 1/4W 5%	7300101
1	R31	R31 100Ω 1/4W 5%	7300101
1	R32	R32 100Ω 1/4W 5%	7300101
1	R33	R33 100Ω 1/4W 5%	7300101
1	R34	R34 100Ω 1/4W 5%	7300101
1	R35	R35 100Ω 1/4W 5%	7300101
1	R36	R36 100Ω 1/4W 5%	7300101
1	R37	R37 100Ω 1/4W 5%	7300101
1	R38	R38 100Ω 1/4W 5%	7300101
1	R39	R39 100Ω 1/4W 5%	7300101
1	R40	R40 100Ω 1/4W 5%	7300101
1	R41	R41 100Ω 1/4W 5%	7300101
1	R42	R42 100Ω 1/4W 5%	7300101
1	R43	R43 100Ω 1/4W 5%	7300101
1	R44	R44 100Ω 1/4W 5%	7300101
1	R45	R45 100Ω 1/4W 5%	7300101
1	R46	R46 100Ω 1/4W 5%	7300101
1	R47	R47 100Ω 1/4W 5%	7300101
1	R48	R48 100Ω 1/4W 5%	7300101
1	R49	R49 100Ω 1/4W 5%	7300101
1	R50	R50 100Ω 1/4W 5%	7300101
1	R51	R51 100Ω 1/4W 5%	7300101
1	R52	R52 100Ω 1/4W 5%	7300101
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1	R55	R55 100Ω 1/4W 5%	7300101
1	R56	R56 100Ω 1/4W 5%	7300101
1	R57	R57 100Ω 1/4W 5%	7300101
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1	R59	R59 100Ω 1/4W 5%	7300101
1	R60	R60 100Ω 1/4W 5%	7300101
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1	R62	R62 100Ω 1/4W 5%	7300101
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1	R66	R66 100Ω 1/4W 5%	7300101
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1	R132	R132 100Ω 1/4W 5%	7300101
1	R133	R133 100Ω 1/4W 5%	7300101
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1	R149	R149 100Ω 1/4W 5%	7300101
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1	R151	R151 100Ω 1/4W 5%	7300101
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1	R153	R153 100Ω 1/4W 5%	7300101
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1	R163	R163 100Ω 1/4W 5%	7300101
1	R164	R164 100Ω 1/4W 5%	7300101
1	R165	R165 100Ω 1/4W 5%	7300101
1	R166	R166 100Ω 1/4W 5%	7300101
1	R167	R167 100Ω 1/4W 5%	7300101
1	R168	R168 100Ω 1/4W 5%	7300101
1	R169	R169 100Ω 1/4W 5%	7300101
1	R170	R170 100Ω 1/4W 5%	7300101
1	R171	R171 100Ω 1/4W 5%	7300101
1	R172	R172 100Ω 1/4W 5%	7300101
1	R173	R173 100Ω 1/4W 5%	7300101
1	R174	R174 100Ω 1/4W 5%	7300101
1	R175	R175 100Ω 1/4W 5%	7300101
1	R176	R176 100Ω 1/4W 5%	7300101
1	R177	R177 100Ω 1/4W 5%	7300101
1	R178	R178 100Ω 1/4W 5%	7300101
1	R179	R179 100Ω 1/4W 5%	7300101
1	R180	R180 100Ω 1/4W 5%	7300101
1	R181	R181 100Ω 1/4W 5%	7300101
1	R182	R182 100Ω 1/4W 5%	7300101
1	R183	R183 100Ω 1/4W 5%	7300101
1	R184	R184 100Ω 1/4W 5%	7300101
1	R185	R185 100Ω 1/4W 5%	7300101
1	R186	R186 100Ω 1/4W 5%	7300101
1	R187	R187 100Ω 1/4W 5%	7300101
1	R188	R188 100Ω 1/4W 5%	7300101
1	R189	R189 100Ω 1/4W 5%	7300101
1	R190	R190 100Ω 1/4W 5%	7300101
1	R191	R191 100Ω 1/4W 5%	7300101
1	R192	R192 100Ω 1/4W 5%	7300101
1	R193	R193 100Ω 1/4W 5%	7300101
1	R194	R194 100Ω 1/4W 5%	7300101
1	R195	R195 100Ω 1/4W 5%	7300101
1	R196	R196 100Ω 1/4W 5%	7300101
1	R197	R197 100Ω 1/4W 5%	7300101
1	R198	R198 100Ω 1/4W 5%	7300101
1	R199	R199 100Ω 1/4W 5%	7300101
1	R200	R200 100Ω 1/4W 5%	7300101

DEC 7411	8	16	—	—
DEC 7380	1	8	—	—
DEC UNIT	3	1	—	2
DEC 7417	8	16	—	—
DEC 82,1	8	16	—	—
DEC 7442	8	16	—	—
DEC 34F	1	8	—	—
DEC 7493	10	5	—	—
DEC 7492	10	5	—	—
DEC 7453	8	16	—	—
DEC 8640	1	8	—	—
DEC 7490	10	5	—	—
DEC 7413	8	16	—	—

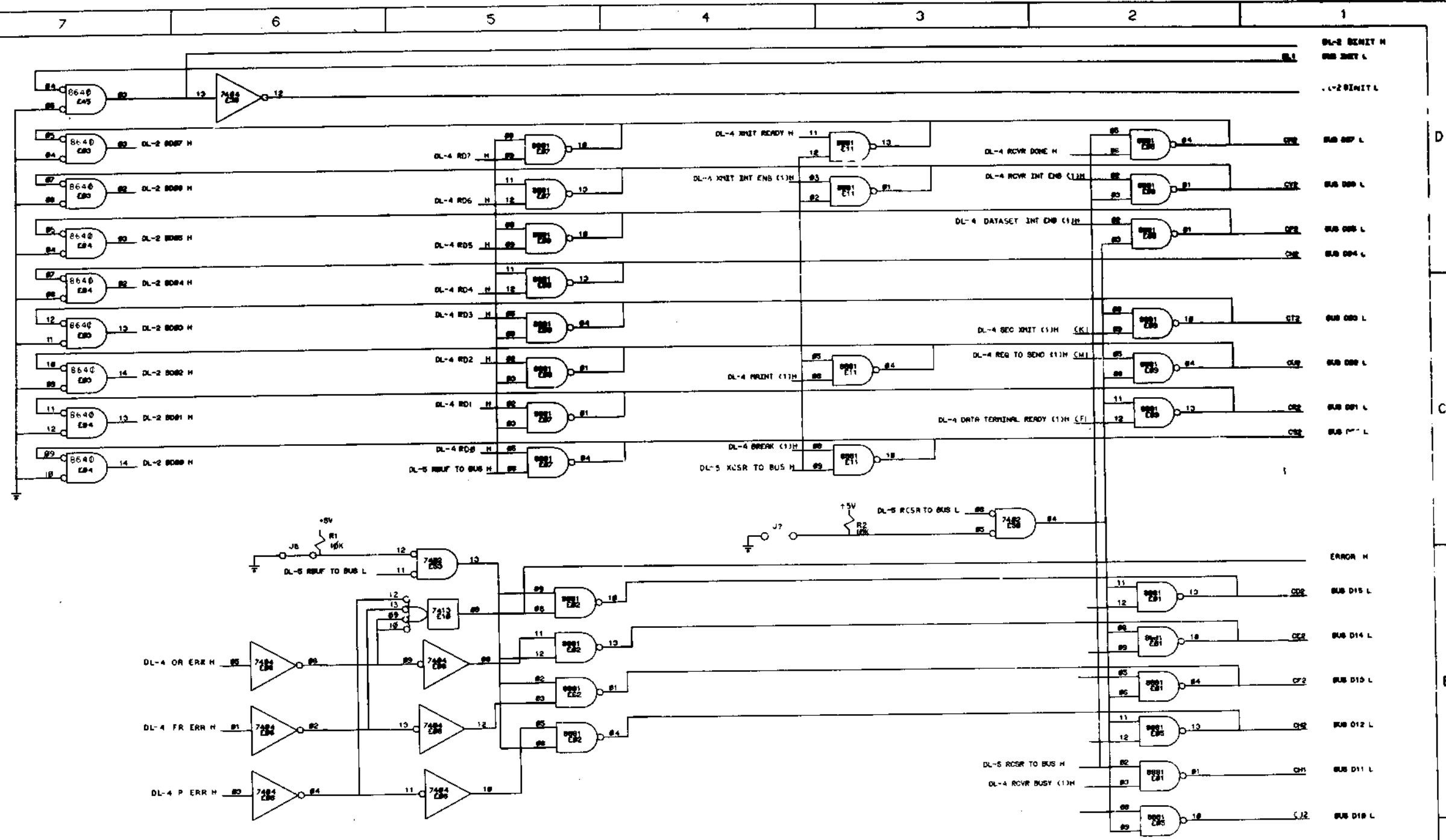


QTY	REF DESIGNATION	DESCRIPTION	PART NO.
1	C22	100Ω 1/4W 5%	7300101
1	C23	100Ω 1/4W 5%	7300101
1	C24	100Ω 1/4W 5%	7300101
1	C25	100Ω 1/4W 5%	7300101
1	C26	100Ω 1/4W 5%	7300101
1	C27	100Ω 1/4W 5%	7300101

QTY	REF DESIGNATION	DESCRIPTION	PART NO.
1	R52	R52 100Ω 1/4W 5%	7300101
1	R53	R53 100Ω 1/4W 5%	7300101
1	R54	R54 100Ω 1/4W 5%	7300101
1	R55	R55 100Ω 1/4W 5%	7300101
1	R56	R56 100Ω 1/4W 5%	7300101
1	R57	R57 100Ω 1/4W 5%	7300101

This drawing and specifications, when used in conjunction with the drawings and specifications of the equipment to which they apply, shall be the basis for the manufacture of the equipment. All dimensions shall be in inches unless otherwise specified.

DIGITAL EQUIPMENT CORPORATION



REV.	DESCRIPTION
1	ORIGINAL
2	REVISED TO ADD 7481 OR GATES
3	REVISED TO ADD 7482 DECODERS
4	REVISED TO ADD 7483 DECODERS
5	REVISED TO ADD 7484 DECODERS
6	REVISED TO ADD 7485 DECODERS
7	REVISED TO ADD 7486 DECODERS
8	REVISED TO ADD 7487 DECODERS

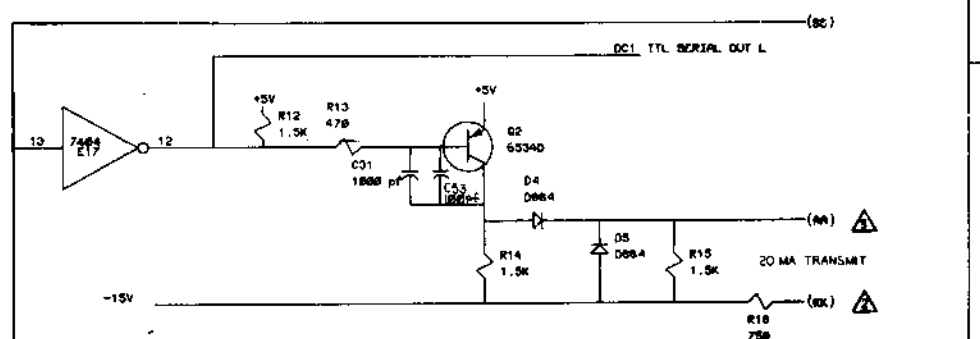
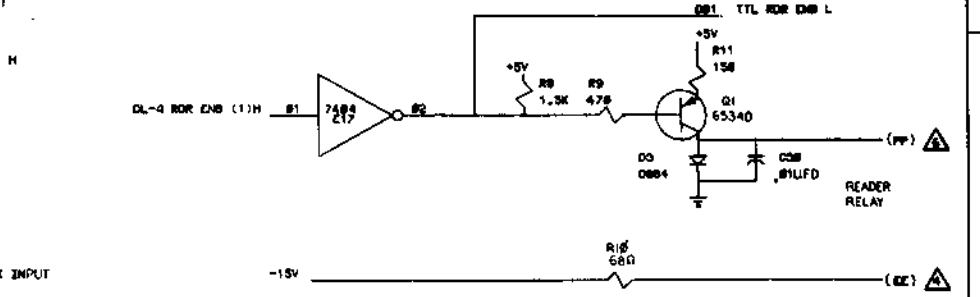
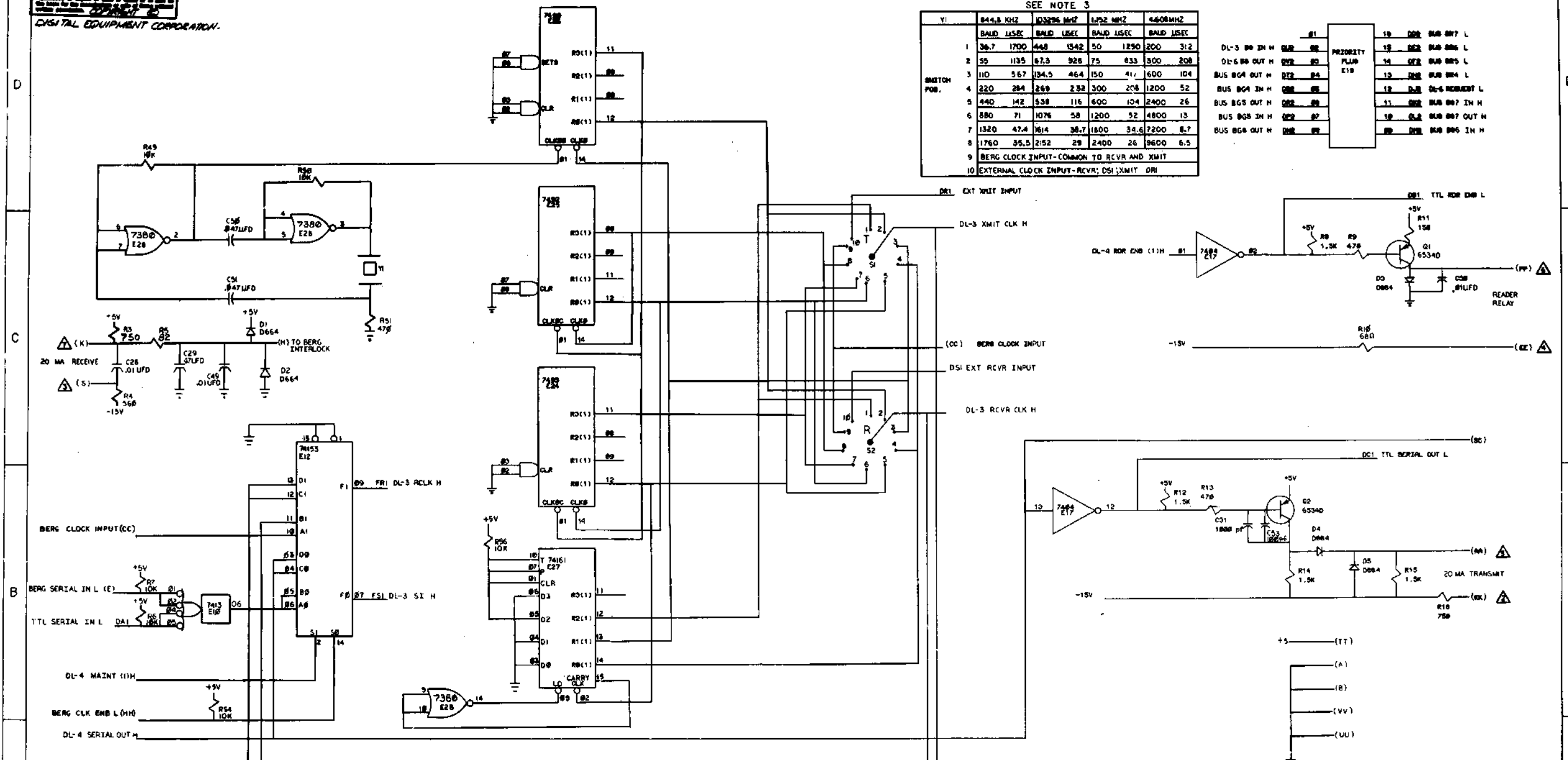
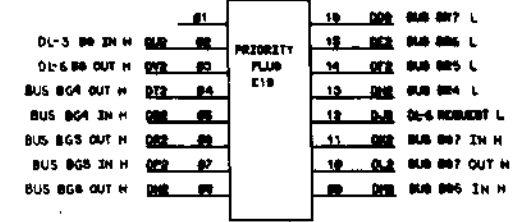
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.
DL11			
UNLESS OTHERWISE SPECIFIED DIMENSIONS IN INCHES			
TOLERANCES			
DECIMALS	ANGLES		
.XX - .99	± .005		
.X - .1	± .01		
REMOVE BURRS AND DE-BURR SHARP CORNERS SURFACE QUALITY			
MATERIAL			
FINISH			
EQUIPMENT CORPORATION			
TITLE ASYNCHRONOUS LINE INTERFACE (BUS RECEIVERS & DRIVERS) DL-2			
D CS		M7800-YA-1	

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DIGITAL EQUIPMENT CORPORATION

SEE NOTE 3

VI	844.8 KHZ		103296 MHz		1.952 MHz		4.608MHz	
	BAUD	USEC	BAUD	USEC	BAUD	USEC	BAUD	USEC
1	36.7	1700	448	1542	50	1250	200	312
2	55	1135	67.3	926	75	833	300	208
3	110	567	134.5	464	150	417	600	104
4	220	284	269	232	300	208	1200	52
5	440	142	538	116	600	104	2400	26
6	880	71	1076	58	1200	52	4800	13
7	1320	47.4	1614	38.7	1800	34.6	7200	8.7
8	1760	35.5	2152	29	2400	26	9600	6.5
9	BERG CLOCK INPUT - COMMON TO RCVR AND XMIT							
10	EXTERNAL CLOCK INPUT - RCVR; DSI; XMIT ORI							



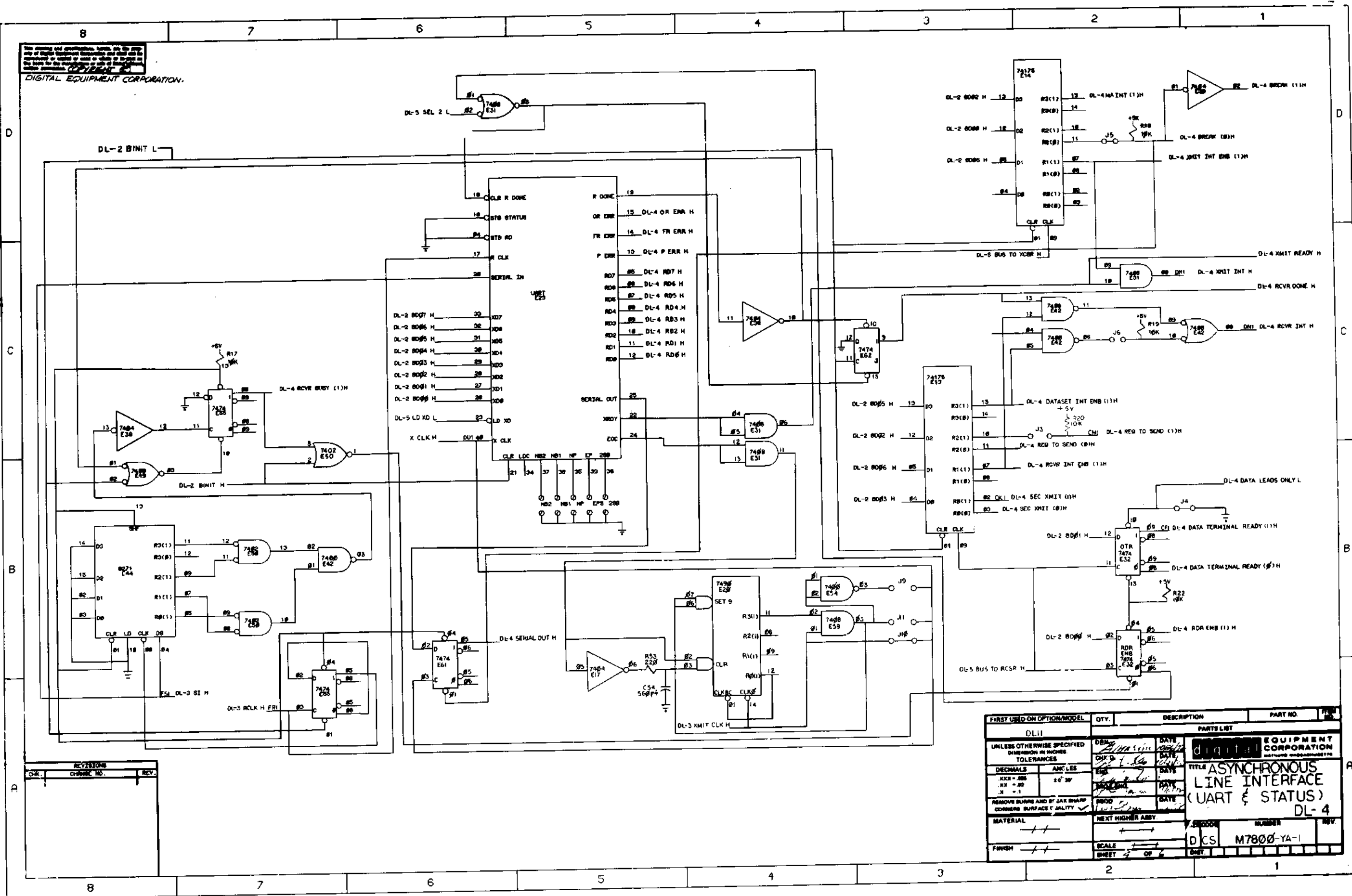
REV.	CHG. NO.	REV.

NOTES:
 1. LETTERS ENCLOSED IN PARENTHESIS REFER TO PINS ON THE BERG CONNECTOR. EXAMPLE: (X)
 2. NUMBERS WITHIN TRIANGLES REFER TO PINS ON THE FEMALE MATE-N-LOCK CONNECTOR WHEN USING THE 7008360 CABLE. THIS CABLE ALSO CONNECTS BERG PINS H TO E.
 3. ALTHOUGH THE ABOVE TABLE INCLUDES ONLY THE STANDARD DL11 CRYSTALS OTHER VALUES MAY BE SPECIFIED BY THE CUSTOMER OR BY OTHER DOCUMENTATION OF AN OPTION WHICH USES THE DL11.

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.
DL11			
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES			
DECIMALS	ANGLES	EQUIPMENT CORPORATION	
.XXX - .005	±0° 30'	TITLE ASYNCHRONOUS LINE INTERFACE (CLOCK & CURRENT LOOPS) DL-3	
.XX - .01		D CS M7800-YA-1	
.X - .1			
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY			
MATERIAL	NEXT NUMBER ASSY.	REVISION	NUMBER
FINISH	SCALE	DATE	REV.

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REV.	CHG.	DATE	BY

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.
DL11			
UNLESS OTHERWISE SPECIFIED DIMENSIONS IN INCHES TOLERANCES			
DECIMALS			
ANGLES			
REMOVE BURRS AND DE-BURR CORNER SURFACE QUALITY			
MATERIAL			
FINISH			

DATE	BY	DATE	BY	DATE	BY

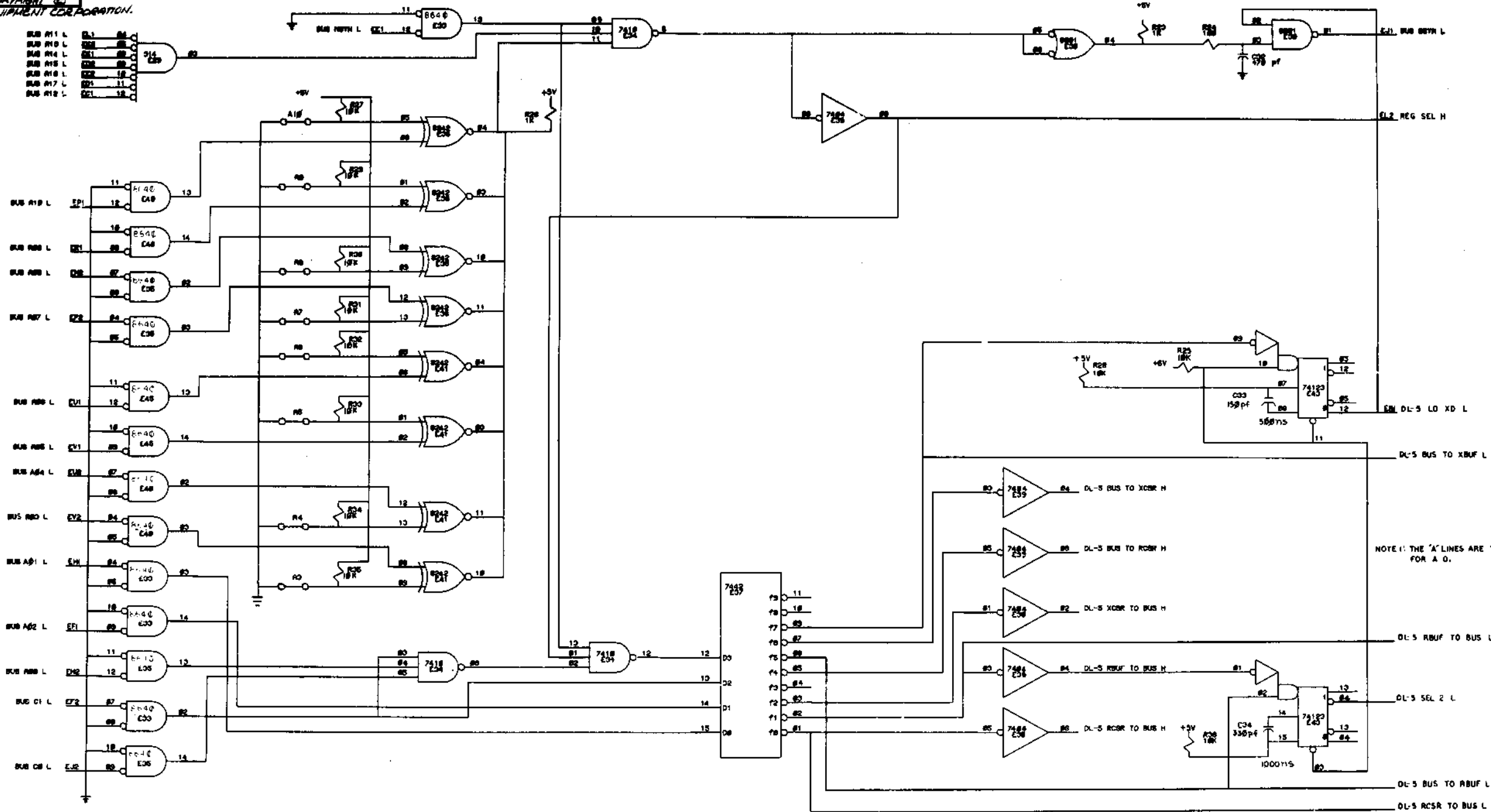
TITLE	NUMBER	REV.
ASYNCHRONOUS LINE INTERFACE (UART & STATUS)	DL-4	
DCS	M7800-YA-1	

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DIGITAL EQUIPMENT CORPORATION.

- BUS A11 L C11 18
- BUS A10 L C21 18
- BUS A14 L C21 18
- BUS A15 L C21 18
- BUS A16 L C21 18
- BUS A17 L C21 18
- BUS A18 L C21 18

- BUS A19 L E11 18
- BUS A20 L E21 18
- BUS A21 L E21 18
- BUS A22 L E21 18
- BUS A23 L E21 18
- BUS A24 L E21 18
- BUS A25 L E21 18
- BUS A26 L E21 18
- BUS A27 L E21 18
- BUS A28 L E21 18
- BUS A29 L E21 18
- BUS A30 L E21 18
- BUS A31 L E21 18
- BUS A32 L E21 18
- BUS A33 L E21 18
- BUS A34 L E21 18
- BUS A35 L E21 18
- BUS A36 L E21 18
- BUS A37 L E21 18
- BUS A38 L E21 18
- BUS A39 L E21 18
- BUS A40 L E21 18
- BUS A41 L E21 18
- BUS A42 L E21 18
- BUS A43 L E21 18
- BUS A44 L E21 18
- BUS A45 L E21 18
- BUS A46 L E21 18
- BUS A47 L E21 18
- BUS A48 L E21 18
- BUS A49 L E21 18
- BUS A50 L E21 18
- BUS A51 L E21 18
- BUS A52 L E21 18
- BUS A53 L E21 18
- BUS A54 L E21 18
- BUS A55 L E21 18
- BUS A56 L E21 18
- BUS A57 L E21 18
- BUS A58 L E21 18
- BUS A59 L E21 18
- BUS A60 L E21 18
- BUS A61 L E21 18
- BUS A62 L E21 18
- BUS A63 L E21 18
- BUS A64 L E21 18
- BUS A65 L E21 18
- BUS A66 L E21 18
- BUS A67 L E21 18
- BUS A68 L E21 18
- BUS A69 L E21 18
- BUS A70 L E21 18
- BUS A71 L E21 18
- BUS A72 L E21 18
- BUS A73 L E21 18
- BUS A74 L E21 18
- BUS A75 L E21 18
- BUS A76 L E21 18
- BUS A77 L E21 18
- BUS A78 L E21 18
- BUS A79 L E21 18
- BUS A80 L E21 18
- BUS A81 L E21 18
- BUS A82 L E21 18
- BUS A83 L E21 18
- BUS A84 L E21 18
- BUS A85 L E21 18
- BUS A86 L E21 18
- BUS A87 L E21 18
- BUS A88 L E21 18
- BUS A89 L E21 18
- BUS A90 L E21 18
- BUS A91 L E21 18
- BUS A92 L E21 18
- BUS A93 L E21 18
- BUS A94 L E21 18
- BUS A95 L E21 18
- BUS A96 L E21 18
- BUS A97 L E21 18
- BUS A98 L E21 18
- BUS A99 L E21 18
- BUS A100 L E21 18



REV.	DATE	BY	CHK.	CONTR. NO.	REV.

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	REV.
DL11				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES				
DECIMALS				
ANGLES				
XXX = .000				
XX = .01				
X = .1				
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY				
MATERIAL		NEXT HIGHER ARMY		
FINISH				
TITLE: ASYNCHRONOUS LINE INTERFACE (ADDRESS SELECTION) DL-5				
D/C S		M7800-14-1		
SHEET 5 OF 6				

DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS			LEGEND		QUANTITY/VARIATION											
ACCESSORY LIST			D	DOCUMENT	DL11-A	DL11-B	DL11-C	DL11-D	DL11-E	KIT CHECK	BY	DATE	INSTALLATION CHECK	BY	DATE	
MADE BY	E. Pellegrini	CHECKED	<i>P. Janson</i>	SECTION	PA	PAPER TAPE ASCII										
DATE	June 26, 1972	DATE	8-8-72		PB	PAPER TAPE BINARY										
ENG	Paul Janson	PROD	<i>J. Janson</i>	ISSUED SECT.	PM	PAPER TAPE READ-IN-MODE										
DATE	June 26, 1972	DATE	8-8-72													
ITEM NO.	DWG NO./PART NO.	DESCRIPTION				DL11-A	DL11-B	DL11-C	DL11-D	DL11-E						
1	M7800	ASYNCHRONOUS LINE INTERFACE (EIA)				-	1	-	1	1						
2	G8000	FILTER NETWORK				0	A/R	0	A/R	0						
3	M7800-YA	ASYNCHRONOUS LINE INTERFACE (CURRENT LOOP)				1	0	1	0	0						
4	5408776	PRIORITY JUMPER LEVEL #4				1	1	1	1	1						
5	BC05-C-25	MODEM CABLE				0	1	0	1	1						
6	7008360	TTY CABLE				1	0	1	0	0						
7	-	CRYSTAL				1	1	1	1	1						
8	-	DL11 ENGINEERING DRAWINGS				1	1	1	1	1						
9	DEC-11-HDLAA-A-D	DL11 ASYNCHRONOUS LINE INTERFACE MANUAL				1	1	1	1	1						
10	LIBKIT-11-KL11-04	KL11 MAINDEC				1	1	0	0	0						
11	LIBKIT-11-DL11C-A-K	DL11 MAINDEC				0	0	1	1	0						
12	LIBKIT-11-DL11E-A-K	DL11 MAINDEC				0	0	0	0	1						
13	H315	MODEM TEST CONNECTOR				0	0	0	0	A/R						
NOTES: 1. G8000 IS REQUIRED ONLY IN PDP-11 SYSTEMS WHERE +15V IS NOT AVAILABLE. ONE PER DD11-A.																
2. CRYSTAL FREQUENCY DEFINED BY CUSTOMER SPECIFIED BAUD RATE.																
3. ONE H315 PER PDPII SYSTEM																
4. INSURE THAT TRANSPARENT VINYL TAPE HAS BEEN APPLIED TO THE TOP SURFACE OF THE CRYSTAL AND MOUNTING BRACKET.																
TITLE			ASSY. NO.	SIZE CODE	NUMBER			REV.	ECO NO							
DL11 CHECK LIST				A AL	DL11-0-5			C	DL11-00005							
			SHEET 1 OF 1	DIST.												

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

TITLE DL11 INSTALLATION PROCEDURE DATE 6-23-72

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE
C	CHANGE PER ECO	DL11-4	JANSON	3/73	<i>[Signature]</i>	4/73
D	CHANGE PER ECO	DL11-5	CONDON	7/73	<i>[Signature]</i>	
E	CHANGE PER ECO	DL11-7	CONDON	8/74	<i>[Signature]</i>	
F	CHANGE PER ECO	DL11-8	CONDON	4-75	<i>[Signature]</i>	

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ENGINEERING SPECIFICATION

TITLE DL11 INSTALLATION PROCEDURE

DL11 INSTALLATION PROCEDURE:
Installation of the M7800 module or its variation as a DL11-A through DL11-E option consists of the following preparations:

- Jumper insertion/deletion for selection of operation mode (A, B, C, D, or E).
 - Register address assignment.
 - Vector address assignment.
 - Priority assignment.
 - Special NPR jumper insertion/deletion.
 - Selection of data format (data bits, stop bits, parity).
 - Selection of crystal for baud rate.
 - Installation of 68000 in systems where +15v is not available.
 - Filter capacitor selection for high baud rate current-loop.
- A. OPERATION MODE:
The following describes the jumpers associated with controlling the mode of operation (A, B, C, D, or E):
- Ties EIA driver to REQUEST-TO-SEND lead (pin 4) of dataset cable. IN for DL11-B, D, and E; Goes not affect DL11-A and C. Drawing DL-7.
 - Ties EIA driver, normally used for the REQUEST-TO-SEND lead, to FORCE BUSY lead (pin 25) for use with Bell 103E. This is a customer option. If not specified, jumper is OUT for all DL11's. Drawing DL-7.
 - When inserted, allows REQUEST-TO-SEND lead (pin 4) to be controlled by bit 2 of the receiver status register. OUT for DL11-B and D; IN for DL11-E; does not affect DL11-A and C. Drawing DL-4.
 - When inserted, forces "DATA LEADS ONLY" mode of EIA operation. Turns DATA TERMINAL READY (pin 20) and REQUEST-TO-SEND (pin 4) on. IN for DL11-B and D; OUT for DL11-E; does not affect DL11-A and C. Drawing DL-4.
 - When inserted, allows the BREAK bit to function. OUT for DL11-A and B; IN for DL11-C, D, and E. Drawing DL-4.
 - When inserted, allows DSET INT to cause interrupts. OUT for DL11-A, B, C and D; IN for DL11-E. Drawing DL-4.
 - When inserted, allows dataset control bits to be read as part of the receiver status register.

DEC FORM NO. 108
DRA 108

APPD	SIZE	CODE	NUMBER	REV
<i>[Signature]</i>	A	SP	DL11-0-2	F

SHEET 2 OF 9

ENGINEERING SPECIFICATION

TITLE DL11 INSTALLATION PROCEDURE

J7. (cont)
OUT for DL11-A, B, C and D; IN for DL11-E. Drawing DL-2.

J8. When inserted, allows error bits to be read as part of the receiver data register. OUT for DL11-A and B; IN for DL11-C, D and E. Drawing DL-2.

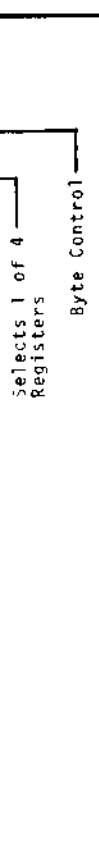
Summary of mode control jumpers:

JUMPER	A	B	C	D	E	DRAWING
J1	IN	IN	IN	IN	IN	DL-7
J2	OUT	OUT	OUT	OUT	OUT	DL-4
J3	*	*	*	*	*	DL-4
J4	*	IN	IN	IN	IN	DL-4
J5	OUT	OUT	IN	IN	IN	DL-4
J6	OUT	OUT	OUT	OUT	IN	DL-4
J7	OUT	OUT	OUT	IN	IN	DL-2
J8	OUT	OUT	IN	IN	IN	DL-2

* = don't care

B. REGISTER ADDRESS ASSIGNMENTS:

The DL11 can respond to addresses with the following format:



Bits 10 through 3 are controlled by jumpers A10 to A3. A jumper inserted indicates a zero.

For the DL11-A and B used as the console device, address 777560 is assigned. For additional units, assign 776XX0, where XX=62 for the first additional unit and XX=67 for the 16th unit.

For the DL11-C, D and E assign address 77XXX0, where XXX=561 for the first line, and XXX=617 for the 31st line. Assign all C's first, then D's, and then E's.

DEC FORM NO. 108
DRA 108

APPD	SIZE	CODE	NUMBER	REV
<i>[Signature]</i>	A	SP	DL11-0-2	F

SHEET 3 OF 9

ENGINEERING SPECIFICATION

TITLE DL11 INSTALLATION PROCEDURE

C. VECTOR ADDRESS ASSIGNMENT:
Jumpers V8 through V3 control the interrupt vector. A jumper inserted provides a vector bit of one. Vectors can be produced in the form XX0 and XX4 where XX ranges from 00 to 77.

For the DL11-A and B used as a console device the vector address is 060/064. For additional units vectors are floating.

For the DL11-C, D, and E vector addresses are floating. Assign all C's first, then D's then E's.

D. PRIORITY ASSIGNMENT:
Interrupt priority is established by inserting a "priority plug" in the socket at IC location E19. For DL11-A, B, C, D and E use level 4, for the standard assignment or level 5-7 as specified by the customer or the documentation of an option which uses the DL11.

SUMMARY OF REGISTER, VECTOR AND PRIORITY ASSIGNMENTS:

ADDRESS	VECTOR	PRIORITY
DL11-A, B CONSOLE	777560 777562 777564 777566	60/64 BR4
DL11-A, B ADDITIONAL UNITS	776XX0 776XX2 776XX4 776XX6	FLOATING BR4
Where XX= 50 for line #1 and XX= 67 for line #16		
DL11-C, D, E	77XXX0 77XXX2 77XXX4 77XXX6	Floating 4
Where XXX= 561 for line #1 and XXX= 617 for line #31		

DEC FORM NO. 108
DRA 108

APPD	SIZE	CODE	NUMBER	REV
<i>[Signature]</i>	A	SP	DL11-0-2	F

SHEET 4 OF 9

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE DL11 INSTALLATION PROCEDURE

E. SPECIAL NPR JUMPER:

Jumper N1, shown on drawing DL-6, controls the response of the interrupt circuit to an NPR request. The jumper should normally be IN, except for 11/28 and 11/15 systems without the N111 option.

F. SELECTION OF DATA FORMAT:

1. Data Bits

Split lug pairs N82 and N81 control the number of data bits in the serial character as follows:

N82	N81	# OF DATA BITS
OUT	OUT	8
OUT	IN	7
IN	OUT	6
IN	IN	5

2. Parity

Parity is controlled by split lug pairs NP and EPS as follows:

NP	EPS	PARITY
OUT	OUT	OFF
OUT	IN	OFF
IN	OUT	EVEN
IN	IN	ODD

3. Stop Bits

Split lug pair 2S8 and jumpers J9, J10 and J11 control the number of stop bits in the serial character as follows:

2S8	J9	J10	J11	# OF STOP BITS
OUT	OUT	IN	OUT	2
IN	OUT	IN	OUT	1
IN	OUT	OUT	IN	1.5 for TI, GI, and SCM UARTS
IN	IN	OUT	OUT	1.5 for MD UARTS

G. CRYSTAL SELECTION:

The clocking scheme of the DL11 consists of a single crystal oscillator feeding a divider network, with two 10-position switches tapping various points to feed into the UART's

SIZE	CODE	NUMBER	REV
A	SP	DL11-0-2	F

DSC FORM NO DEC 14-1981-1027-41376
DIA 108

SHEET 5 OF 9

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE DL11 INSTALLATION PROCEDURE

M. 68000 INSTALLATION:

For DL11-B, D, and E a positive voltage is required between 9 and 15 volts to operate the EIA drivers. For PDP-11/20 and PDP-11/15 systems with the H720 power supply, a 68000 module must be installed to provide this voltage. Using a filter network, this module converts the full-wave rectified +48V signal to a positive DC voltage.

1. Install 68000 into slot A02 of DD11-A.
 2. Wire A03V2 to A02Y2.
 3. Wire A02Z2 to CXXU1 where XX is the slot location of the M7800.
- Refer to diagram 1.

I. FILTER CAPACITOR SELECTION:

For DL11-A's and DL11-C's, which operate with 20ma current loops, capacitors are used to filter the receive line and slow the switching time of the transmit line. To avoid excessive distortion above 150 baud, the capacitance in each of these two circuits must be reduced. This is accomplished by clipping C29 (.47 mfd) and C31 (1000 pF), both shown on drawing DL-3.

J. DL11-B,D,E in Systems with +15V available using DD11-A:
There is a special situation of using a DD11-A to mount a DL11-B, D, or E in systems with +15V available. These systems have +15V available and it appears at pin A03V2 of the DD11-A when using power harness such as 7009177, 7008855, or 7008909. In this situation, no 68000 is necessary, and +15V can be wired directly from A03V2 to CXXU1, where XX is the slot number of the DL11.
NOTE: this does not apply to DL11-A or C or DD11-B.

K. When using the DL11-B,D,E in an 11/05 processor pin CXXU1 has +15V available on it so no 68000 or no jumpers are required.

SIZE	CODE	NUMBER	REV
A	SP	DL11-0-2	F

DSC FORM NO DEC 14-1981-1027-41376
DIA 108

SHEET 7 OF 9

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE DL11 INSTALLATION PROCEDURE

6. Cont

transmitter and receiver sections. Thus, for a given crystal frequency, 8 baud rates are independently selectable for transmit and receive. The two additional switch positions select external clocks.

POSITION	SPEED GROUP	CRYSTAL (MHZ)			
		1	2	3	4
1*	23040	36.7	44.8	50	70
2	15360	55	67.3	75	300
3	7680	110	134.5	150	600
4	3840	220	269	300	1200
5	1920	440	538	600	2400
6	960	880	1076	1200	4800
7	640	1320	1614	1800	7200
8	480	1760	2152	2400	9600

*Most counter-clock wise position.

To determine a crystal frequency for a non-standard baud rate, pick the position of the closest baud rate in the 1.152MHz column, and then multiply the non-standard baud rate by the factor for that position. For example, if the customer specifies 1050 baud, this is closest to 1200 baud, position 6.

$$1050 \times 960 = 1008000 = 1.008\text{MHZ.}$$

The crystal frequency should not fall outside the range of the standard DLI1 crystals. Although the above table includes only the standard DLI1 crystals other values may be specified by the customer or by other documentation of an option which uses the DL11.

DSC part number for the standard crystals are as follows:

044.8 KHZ	18-10245-1*
1.03296 MHZ	18-05501-6
1.152 MHZ	18-05501-5
4.608 MHZ	18-05501-7

*Use A or C cut crystals only. Do not use crystals marked NE-60.

When ordering a special crystal, refer to purchase specification 18-05501 for crystal specification.

Insure that transparent vinyl tape (9008269) is applied to the top surfaces of the crystal and mounting brackets to insulate from adjacent modules.

SIZE	CODE	NUMBER	REV
A	SP	DL11-0-2	F

DSC FORM NO DEC 14-1981-1027-41376
DIA 108

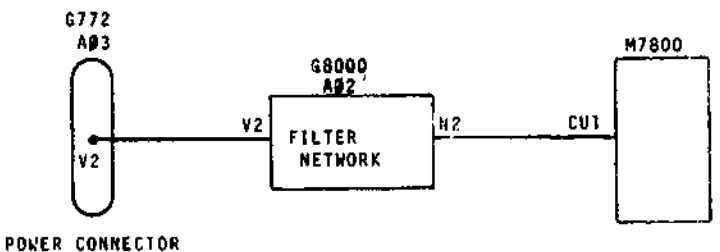
SHEET 6 OF 9

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE DL11 INSTALLATION PROCEDURE

DIAGRAM 1. 68000 INSTALLATION

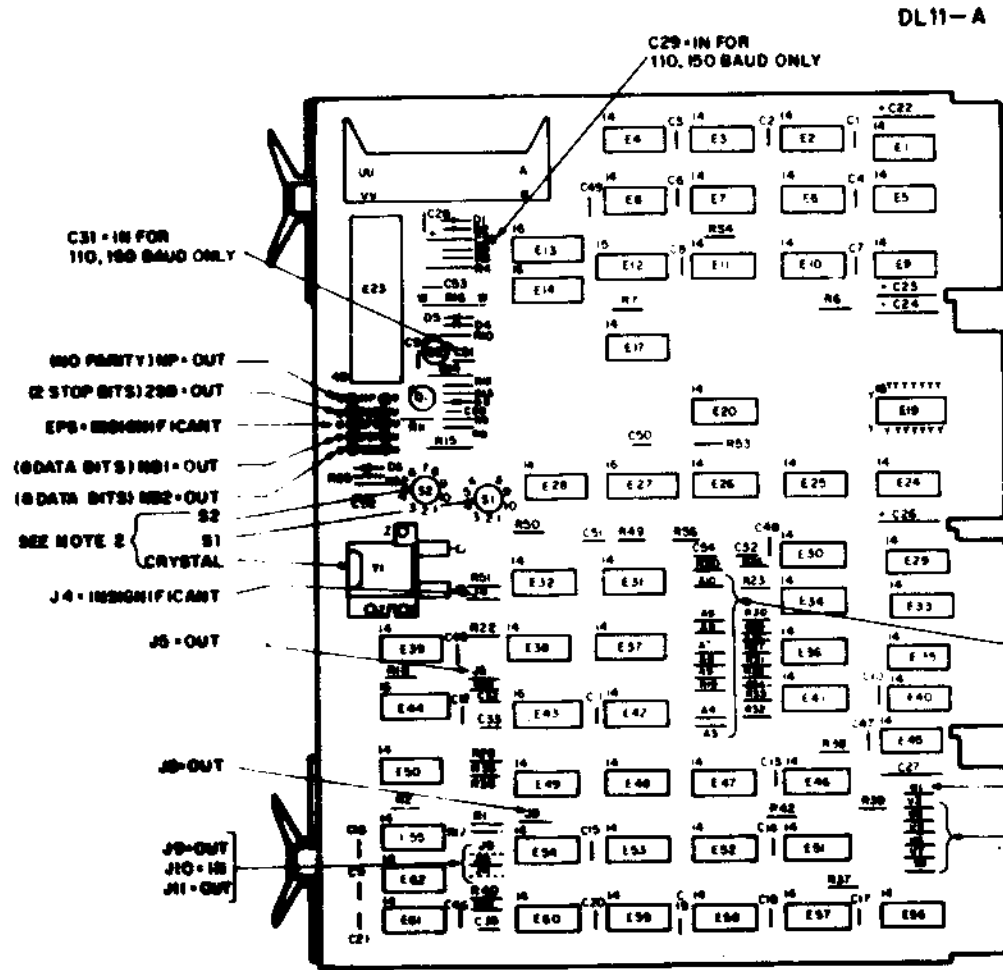


SIZE	CODE	NUMBER	REV
A	SP	DL11-0-2	F

DSC FORM NO DEC 14-1981-1027-41376
DIA 108

SHEET 8 OF 9

TITLE DL11 INSTALLATION PROCEDURE



DL11-A

NOTES:

1. For further information on the DL11-A configuration or the installation of DL11-B, DL11-C, DL11-D or DL11-E refer to:
 - a. DL11 Asynchronous Line Interface Manual
 - b. ASP-DL11-0-2 (DL11 installation procedure) in the DL11 Engineering Drawings.
- 2.

SPEED GROUP	BAUD RATE			
	1	2	3	4
CRYSTAL FREQ (KHZ)	944.8K	1.01296M	1.152M	1.608M
ST. S2 POS.				
1	36.7	44.8	50	200
2	55	67.3	75	300
3	110	134.5	150	600
4	220	269	300	1200
5	440	538	600	2400
6	880	1076	1200	4800
7	1320	1614	1800	7200
8	1760	2152	2400	9600

Position 1 is most counter-clockwise position

ADDRESS

NI (EXCEPT FOR 11/20 & 11/15 SYSTEMS WITHOUT KHI OPTION)

VECTOR ADDRESS

(1-2454)

REV F

NUMBER DL11-0-2

SIZE CODE SP A

SHEET 9 OF 9



CUSTOMER PRINT SET						CUSTOMER PRINT SET								
	MFG. SET	FIND NO.	DRAWING NO.	REV	NO OF SHEETS	OPTION NO./FILE DATE		MFG. SET	FIND NO.	DRAWING NO.	REV	NO OF SHEETS	DESCRIPTION	OPTION NO./FILE DATE
X		1	A-PL-BM873-0-0	A	1	RESTART LOADER (PL)		X	7	K-CS-M873-0-28	9	9	ROM LISTING (FOR 23225A2)	
	X		A-SP-BM873-0-1		16	BM873 ENG. SPECIFICATION		X		K-CS-M873-0-29	9	9	ROM LISTING (FOR 23226A2)	
	X		A-SP-BM873-0-2		1	BM873 TEST PROCEDURE		X		K-CS-M873-0-30	9	9	ROM LISTING (FOR 23227A2)	
X			A-AL-BM873-0-3		1	BM873 ACCESSORY LIST		X		K-CS-M873-0-31	9	9	ROM LISTING (FOR 23228A2)	
							X	X		K-CS-M873-0-32	1	1	WORD LISTING OF ROM CONTENTS (FOR BM873-WA)	
X	X	2	D-CS-M873-0-1	#	4	RESTART LOADER		X	8	A-PS-23338A2-0-0	3	3	ROM LISTING (FOR 23338A2)	
			K-CO-M873-0-4		1	X-Y COORDINATE HOLE LOCATION		X		A-PS-23339A2-0-0	3	3	ROM LISTING (FOR 23339A2)	
			D-AH-M873-0-5		1	ASSY/DRILLING HOLE LAYOUT		X		A-PS-23340A2-0-0	3	3	ROM LISTING (FOR 23340A2)	
	X		B-MH-M873-0-6		1	MODULE ECO HISTORY		X		A-PS-23341A2-0-0	3	3	ROM LISTING (FOR 23341A2)	
	X		K-RL-M873-0-7	#	9	ROM LISTING (FOR 23044A2)		X		B-CS-M873-0-33	1	1	WORD LISTING OF ROM CONTENTS (FOR BM873-YG)	
	X		K-RL-M873-0-8	#	9	ROM LISTING (FOR 23045A2)	X	X						
X	X		B-AP-M873-0-9	#	1	WORD LISTING OF ROM CONTENTS (FOR M873-YA)			9	A-PS-23327A2-0-0	3	3	ROM LISTING (FOR 23327A2)	
	X		K-RL-M873-0-10		9	ROM LISTING (FOR 23089A2)		X		A-PS-23328A2-0-0	3	3	ROM LISTING (FOR 23328A2)	
	X		K-RL-M873-0-11		9	ROM LISTING (FOR 23090A2)		X		A-PS-23329A2-0-0	3	3	ROM LISTING (FOR 23329A2)	
	X		K-RL-M873-0-12		9	ROM LISTING (FOR 23091A2)		X		A-PS-23330A2-0-0	3	3	ROM LISTING (FOR 23330A2)	
	X		K-RL-M873-0-13		9	ROM LISTING (FOR 23092A2)	X	X		B-CS-M873-0-34	1	1	WORD LISTING OF ROM CONTENTS (FOR BM873-YH)	
X	X		B-AP-M873-0-14		1	WORD LISTING OF ROM CONTENTS (FOR M873-YB)								
	X	4	K-RL-M873-0-7		9	ROM LISTING (FOR 23044A2)								
	X		K-RL-M873-0-8		9	ROM LISTING (FOR 23045A2)								
	X		K-CS-M873-0-15		9	ROM LISTING (FOR 23109A2)								
	X		K-CS-M873-0-16		9	ROM LISTING (FOR 23110A2)								
X	X		B-CS-M873-0-17		1	WORD LISTING OF ROM CONTENTS (FOR M873YC)								
	X	5	K-CS-M873-0-18		9	ROM LISTING (FOR 23095A2)								
	X		K-CS-M873-0-19		9	ROM LISTING (FOR 23096A2)								
	X		K-CS-M873-0-20		9	ROM LISTING (FOR 23097A2)								
	X		K-CS-M873-0-21		9	ROM LISTING (FOR 23098A2)								
X	X		B-CS-M873-0-22		1	WORD LISTING OF ROM CONTENTS (FOR M873 YD)								
	X	6	K-CS-M873-0-23		9	ROM LISTING (FOR 23215A2)								
	X		K-CS-M873-0-24		9	ROM LISTING (FOR 23216A2)								
	X		K-CS-M873-0-25		9	ROM LISTING (FOR 23217A2)								
	X		K-CS-M873-0-26		9	ROM LISTING (FOR 23218A2)								
	X		B-CS-M873-0-27		1	WORD LISTING OF ROM CONTENTS (FOR M873-YF)								

CUSTOMER PRINT SET CODES
X = PRINT OF DOCUMENT INCLUDED IN PRINT SET
C = INCLUDES ALL PRINTS INDICATED ON DOCUMENT
S = CONFIDENTIAL AUTHORIZED SIGNATURE REQUIRED

TITLE: RESTART LOADER
SHEET 2 OF 2
SIZE CODE: B DD
NUMBER: BM873-0
REV: F

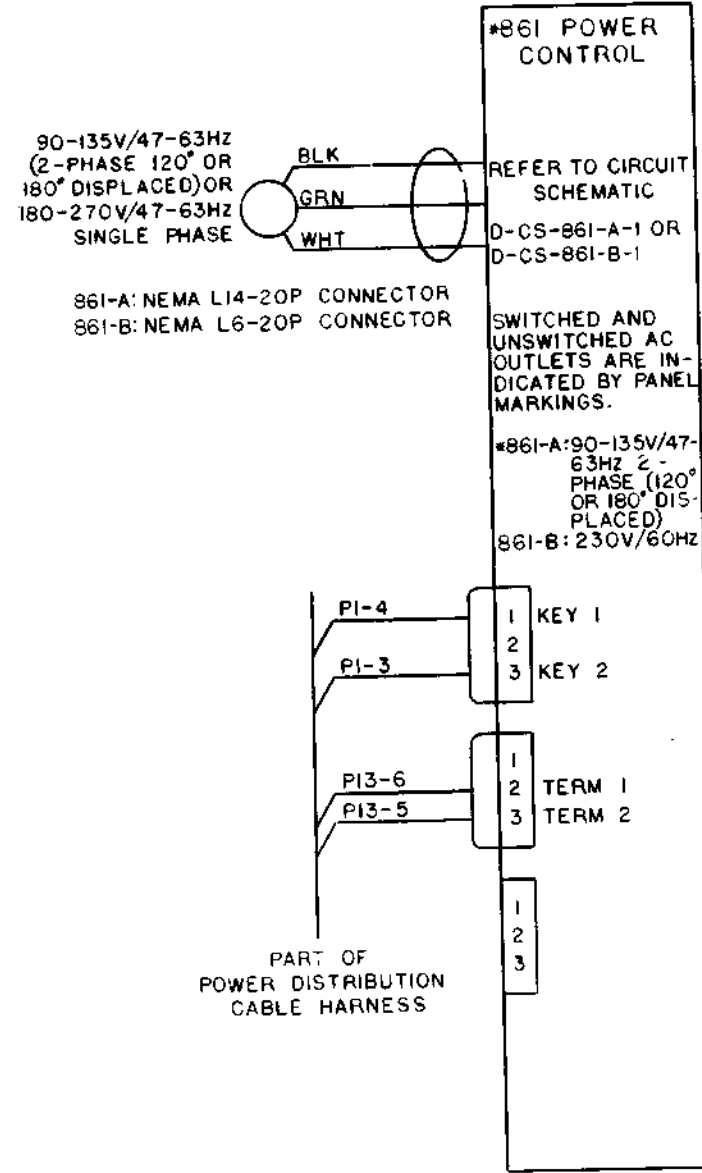
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NOTE

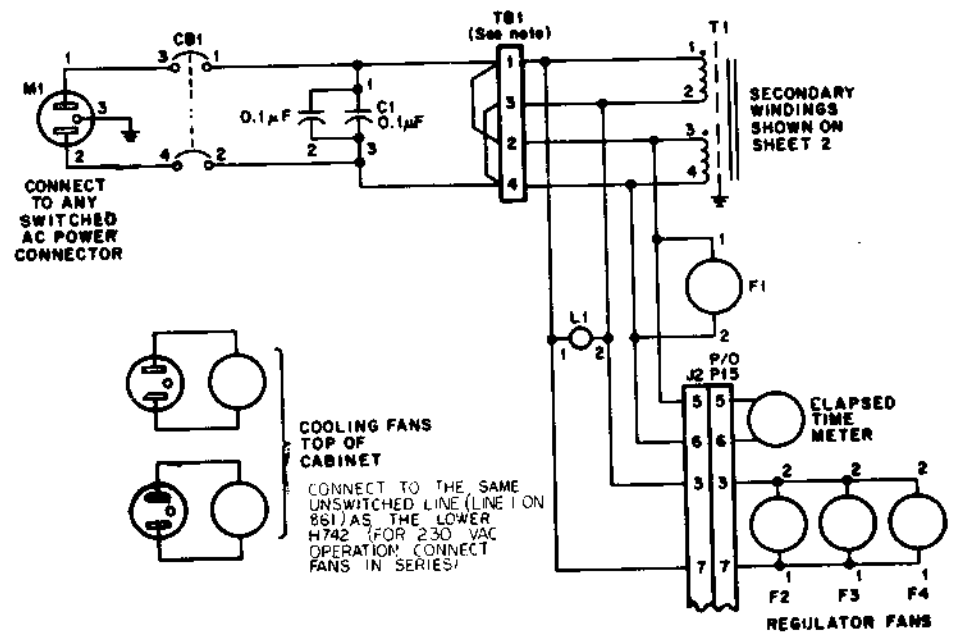
1. FOR 11/45 SYSTEMS BUILT WITH 860 POWER CONTROLS, REFER TO D-IC-11/45-0-1 REV. A DRAWING.

TABLE I
POWER SYSTEM - MAJOR ECO SUMMARY

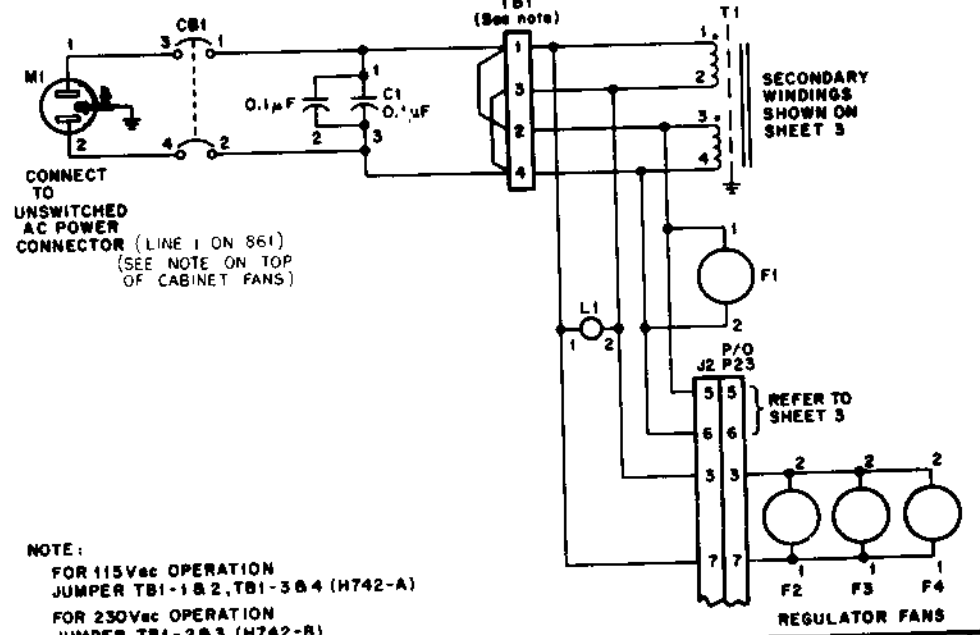
ECO#	D-IC-11/45-0-1	DESCRIPTION	REV CHANGES (PROBLEM-SOLUTION)
FROM REV	TO REV		
11/45-00031	A	B	REPLACED OBSOLETE 860 POWER CONTROL WITH 861 POWER CONTROL. (D-IC-11/45-0-1 REV. A DOCUMENTS MACHINES WITH 860 POWER CONTROL.)
11/45-00054	C	D	POWER DISTRIBUTION REDESIGNED TO ACCOMMODATE H754 REGULATOR (420V/3-5V) FOR 16K MEMORY. MAIN POWER HARNESS CHANGED FROM 700B784 TO 7009540. SYSTEM UNIT POWER DISTRIBUTION MOVED FROM BACK OF CPU BOX TO TOP REAR OF CPU BOX. SYSTEM UNIT CONNECTORS CHANGED FROM FLAT 8-PIN CONNECTOR TO 15-PIN AND 6-PIN RECTANGULAR CONNECTOR-PAIR. MACHINES WITH THIS ECO HAVE SERIAL NO'S ≥ 2000.
11/45-00057	D	E	7009540 HARNESS REVISED TO DISTRIBUTE -15V TO SYSTEM UNITS WHEN H754 REGULATOR IS INSTALLED FOR 16K MEMORY. P45/J45 (FLAT 4-PIN CONNECTORS) ADDED TO HARNESS NEAR P7. (AFFECTS ONLY MACHINES WITH S/N ≥ 2000).
11/45-00060	E	F	+5V FROM SLOT D H744 REWIRED TO LOWER VOLTAGE DROPS TO SYSTEM UNITS.
11/45-00061	F	H	CPU HARNESS MODIFIED TO ACCOMMODATE SECOND H746 MOS REGULATOR. ADD P30 TO 7009540 HARNESS (NEAR P29 MACHINE WITH S/N ≥ 2000). IF S/N < 2000 P30 OF THE 7009784 HARNESS IS REWIRED TO DISTRIBUTE MOS VOLTAGE FROM AN H746 IN SLOT L OF THE LOWER H742. KB11-A ECO# MUST BE INSTALLED AT SAME TIME



UPPER H742 POWER SUPPLY
REFER TO D-CS-H742-0-1



LOWER H742 POWER SUPPLY



NOTE:
FOR 115V_{ac} OPERATION
JUMPER TB1-1 & 2, TB1-3 & 4 (H742-A)
FOR 230V_{ac} OPERATION
JUMPER TB1-2 & 3 (H742-B)

TABLE 2

MEMORY CONFIGURATION	REGULATOR SLOTS				JUMPERS			FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
	H	J	K	L	+5V2 (P5-7/8)	+5V1 (P5-3/8)	+5V3 (P6-7/8)					
BIPOLAR ONLY								11/45				
1-2K	H743	X	X	X	IN	IN	IN					
3-4K	H744	X	X	X	IN	IN	IN					
5-6K	H744	X	X	X	IN	IN	IN					
7-8K	H744	X	X	X	IN	IN	IN					
MOS. ONLY												
4-16K	H746	H744	X	X	IN	IN	IN					
16-32K	H746	H744	X	H746	IN	IN	IN					
MIXED-MOS/BIPOLAR												
4-16K MOS; 1-2K BIPOLAR	H746	H744	X	X	IN	X	IN					
4-16K MOS; 3-4K BIPOLAR	H746	H744	X	H744	IN	X	X					

REVISIONS

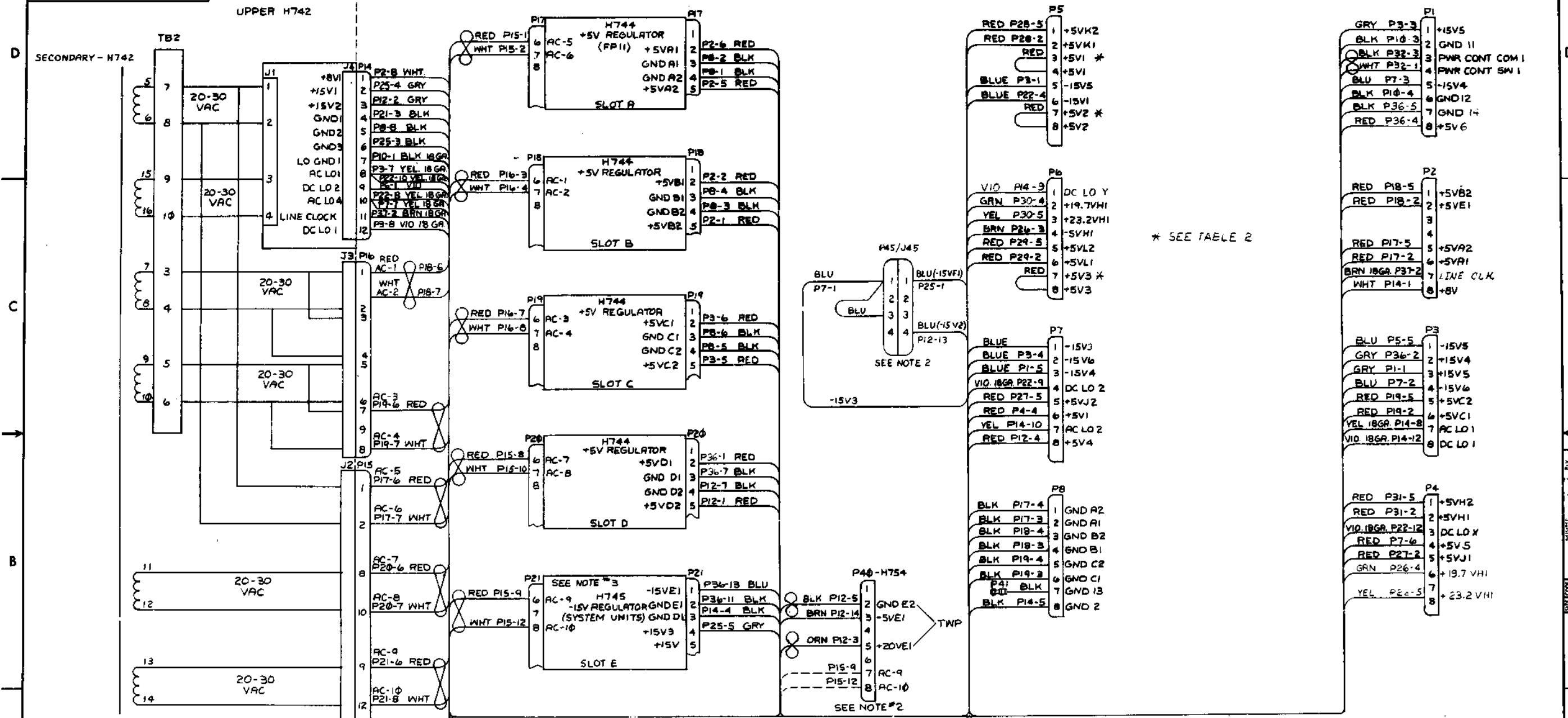
REV.	CHANGE NO.	DATE	BY	CHKD.
A	11/45-00031	11/74	SWANSON	
B	11/45-00047	11/74	MINOR	
C	11/45-00052	11/74	BOEN	
D	11/45-00054	11/74	BOEN	
E	11/45-00057	11/74	BOEN	
F	11/45-00060	11/74	BOEN	
G	11/45-00061	11/74	BOEN	
H	11/45-00061	11/74	BOEN	
I	11/45-00061	11/74	BOEN	

digital EQUIPMENT CORPORATION

POWER SYSTEMS CONFIGURATION

DIC 11/45-0-1

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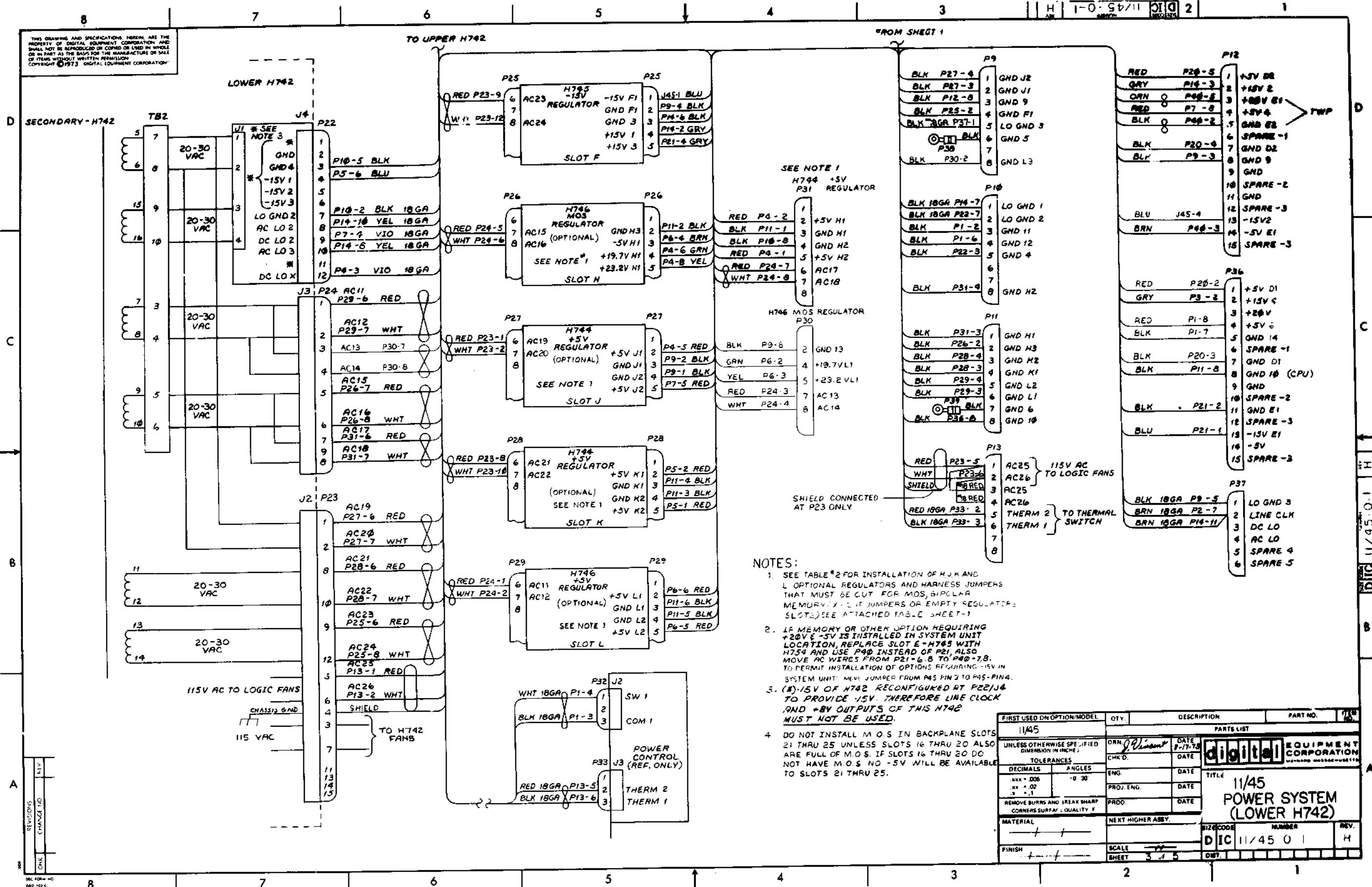


* SEE TABLE 2

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
11/45				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES				
TOLERANCES				
DECIMALS	ANGLES	ENG.	DATE	
.XX = .005	±0° 30'	PROJ. ENG.	DATE	
.XX = .02		PROD.	DATE	
.X = .1				
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY 1				
MATERIAL		NEXT HIGHER ASSY.		
FINISH		SCALE NONE		
		SHEET 2 OF 5		
		D I C 11/45-0-1		
		REV. H		

REV.	CHANGE NO.
1	
2	
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	

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- NOTES:
- SEE TABLE #2 FOR INSTALLATION OF H, J, K AND L OPTIONAL REGULATORS AND HARNESS JUMPERS THAT MUST BE CUT FOR MOS, BIPLAR MEMORY, L1 JUMPERS OR EMPTY REGULATOR SLOTS. SEE ATTACHED TABLE SHEET-1.
 - IF MEMORY OR OTHER OPTION REQUIRING +20V E -5V IS INSTALLED IN SYSTEM UNIT LOCATION, REPLACE SLOT E -H743 WITH H754 AND USE P40 INSTEAD OF P21. ALSO MOVE AC WIRES FROM P21-6, 8 TO P40-7, 8. TO PERMIT INSTALLATION OF OPTIONS REQUIRING -15V IN SYSTEM UNIT, MOVE JUMPER FROM P45 PIN 3 TO P45-PIN 4.
 - (X) -15V OF H742 RECONFIGURED AT P22/J4 TO PROVIDE -15V. THEREFORE LINE CLOCK AND +5V OUTPUTS OF THIS H742 MUST NOT BE USED.
 - DO NOT INSTALL M.O.S IN BACKPLANE SLOTS 21 THRU 25 UNLESS SLOTS 16 THRU 20 ALSO ARE FULL OF M.O.S. IF SLOTS 16 THRU 20 DO NOT HAVE M.O.S NO -5V WILL BE AVAILABLE TO SLOTS 21 THRU 25.

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.
11/45			
PARTS LIST			
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES	DRN. <i>J. Wilson</i>	DATE 8-17-78	digital EQUIPMENT CORPORATION
TOLERANCES	CHK'D.	DATE	
DECIMALS	ANGLES	DATE	TITLE 11/45 POWER SYSTEM (LOWER H742)
.XXX ± .006	° 0 30	DATE	
.XX ± .02		PROJ. ENG. DATE	SIZE CODE D I C 11/45 0 1
.X ± .1		DATE	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY V	PRD.	DATE	NUMBER 11/45 0 1
MATERIAL	NEXT HIGHER ASSY.		
FINISH	SCALE		REV. H
	SHEET 3 OF 5		

REVISIONS	DATE	BY
1		
2		
3		

DEC FORM NO 040 102-C

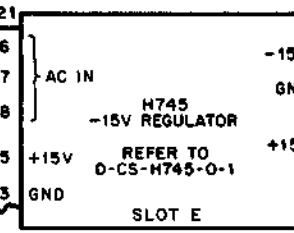
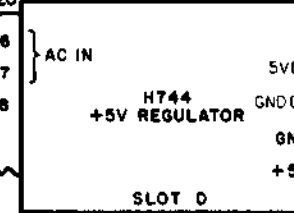
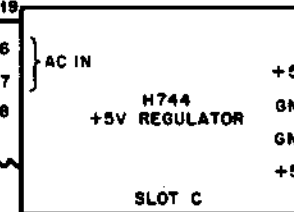
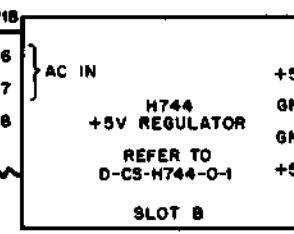
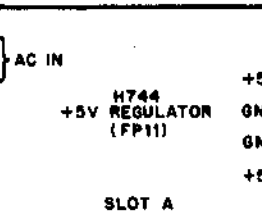
D I C 11/45-0-1 H

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UPPER H742 POWER SUPPLY

T1
SECONDARY
UPPER
H742
POWER
SUPPLY

POWER CONTROL
UPPER H742
POWER SUPPLY
REFER TO
C-CS-5409730-0-1



7008784 OLD POWER DISTRIBUTION CABLE HARNESS

CUT JUMPERS

P5 7-8	P5 3-4	P6 7-8
BIPOLAR ONLY		
IF MORE THAN 2K OF BIPOLAR	IF MORE THAN 4K OF BIPOLAR	IF MORE THAN 8K OF BIPOLAR
NEVER CUT	ALWAYS CUT	IF MORE THAN 2K OF BIPOLAR

- P1
- 1 +15V
 - 2 GND
 - 3 860 GND IN
 - 4 860 GND OUT
 - 5 -15V
 - 6 GND
 - 7 GND
 - 8 +5V
- TO CONSOLE CONNECTOR J3

- P2
- 1 +5V
 - 2 +5V
 - 3
 - 4
 - 5 +5V
 - 6 +5V
 - 7 CLOCK
 - 8 +8V

- P3
- 1 -15V
 - 2 +15V
 - 3 +15V
 - 4 DC LO 2
 - 5 +5V
 - 6 +5V
 - 7 AC LO 1
 - 8 DC LO 1

- P4
- 1 +5V
 - 2 +5V
 - 3 DC LO X
 - 4 +5V
 - 5 +5V
 - 6 +19.7V HI
 - 7
 - 8 +23.2V HI

- P5
- 1 +5V
 - 2 +5V
 - 3 +5V
 - 4 -15V
 - 5 -15V
 - 6 -15V
 - 7 +5V
 - 8
- P6
- 1 +19.7V
 - 2 +23.2V
 - 3
 - 4 -5V
 - 5 +5V
 - 6 +5V
 - 7 +5V
 - 8
- P7
- 1 -15V
 - 2 -15V
 - 3 -15V
 - 4 DC LO 2
 - 5 +5V
 - 6 +5V
 - 7 AC LO 2
 - 8 +5V
- P8
- 1 GND
 - 2
 - 3
 - 4
 - 5
 - 6
 - 7
 - 8

ELAPSED TIME METER

THIS SHEET APPLIES TO MACHINES WITH SERIAL NUMBERS LESS THAN 2000.

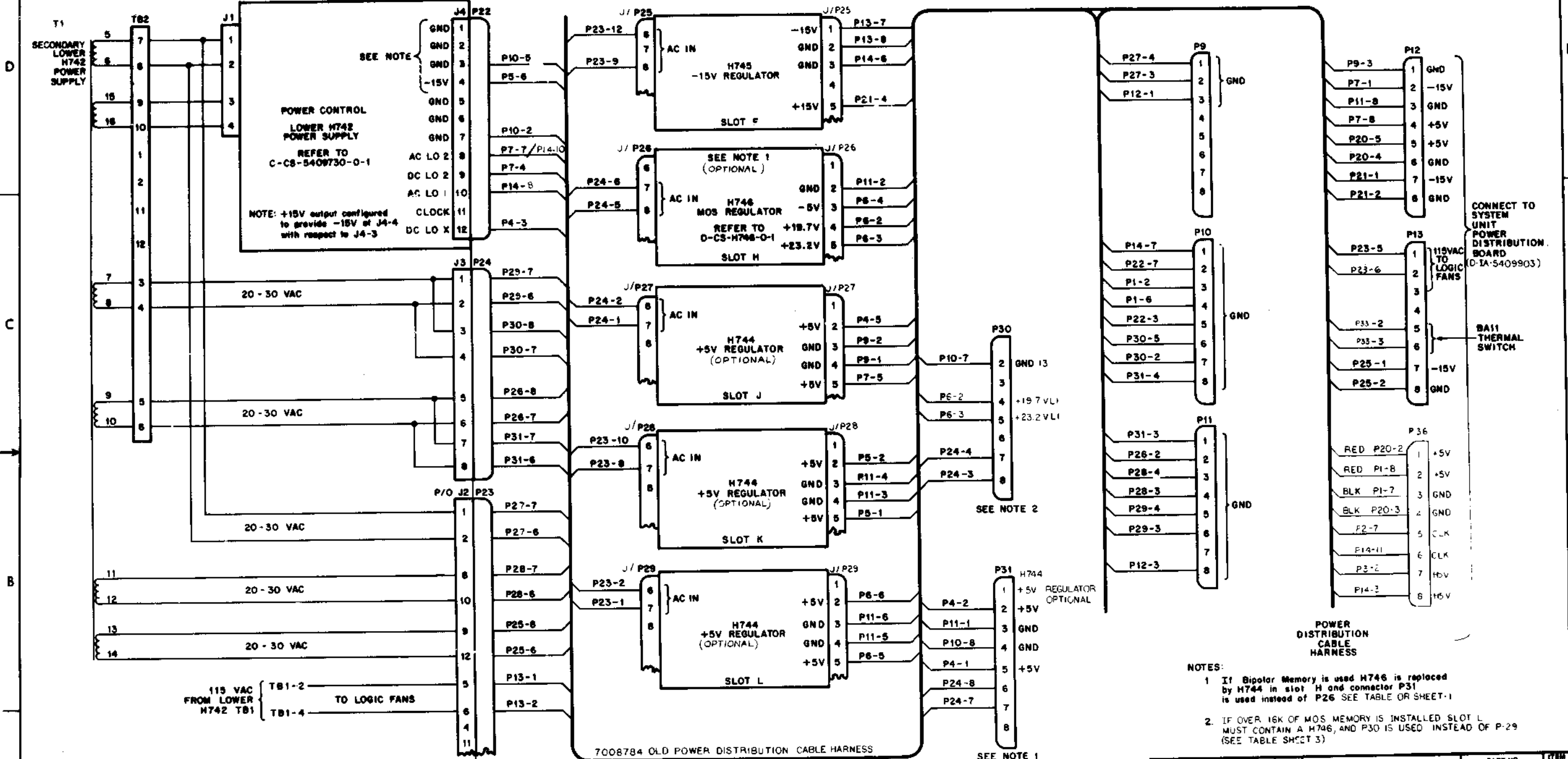
FIRST USED ON OPTION/MODEL 11/45	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES	DRN	DATE	DIGITAL EQUIPMENT CORPORATION	
DECIMALS	CHKD	DATE	TITLE	
ANGLES	ENG	DATE	OLD POWER SYSTEMS CONFIGURATION	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	PROD	DATE	SIZE CODE NUMBER REV.	
MATERIAL	NEXT HIGHER ASSY.	DATE	D1011/45-0-1 H	
FINISH	SCALE	DATE	SHEET 4 OF 5	

REVISIONS

NO.	DATE	DESCRIPTION
1		

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LOWER H742 POWER SUPPLY



SEE NOTE

POWER CONTROL
LOWER H742
POWER SUPPLY
REFER TO
C-CS-5409730-0-1

NOTE: +15V output configured to provide -15V at J4-4 with respect to J4-3

SEE NOTE 1
(OPTIONAL)

H746
MOS REGULATOR
REFER TO
D-CS-H746-0-1

SLOT H

H744
+5V REGULATOR
(OPTIONAL)

SLOT J

H744
+5V REGULATOR
(OPTIONAL)

SLOT K

H744
+5V REGULATOR
(OPTIONAL)

SLOT L

7008784 OLD POWER DISTRIBUTION CABLE HARNESS

- NOTES:
- 1 IF Bipolar Memory is used H746 is replaced by H744 in slot H and connector P31 is used instead of P26 SEE TABLE OR SHEET-1
 - 2 IF OVER 16K OF MOS MEMORY IS INSTALLED SLOT L MUST CONTAIN A H746, AND P30 IS USED INSTEAD OF P-29 (SEE TABLE SHEET 3)

THIS SHEET APPLIES TO MACHINES WITH SERIAL NUMBERS LESS THAN 2000

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
11/45		PARTS LIST		
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES		DATE	EQUIPMENT CORPORATION	
DECIMALS .005	ANGLES .0° 30'	DATE	TITLE	
XXX - .005	XX - .02	DATE	OLD POWER SYSTEMS CONFIGURATION	
X - .1		DATE		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		DATE		
MATERIAL	NEXT HIGHER ASSY.	DATE		
FINISH	SCALE	DATE		
	SHEET	DATE		

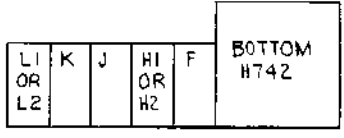
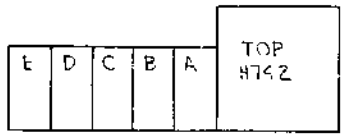
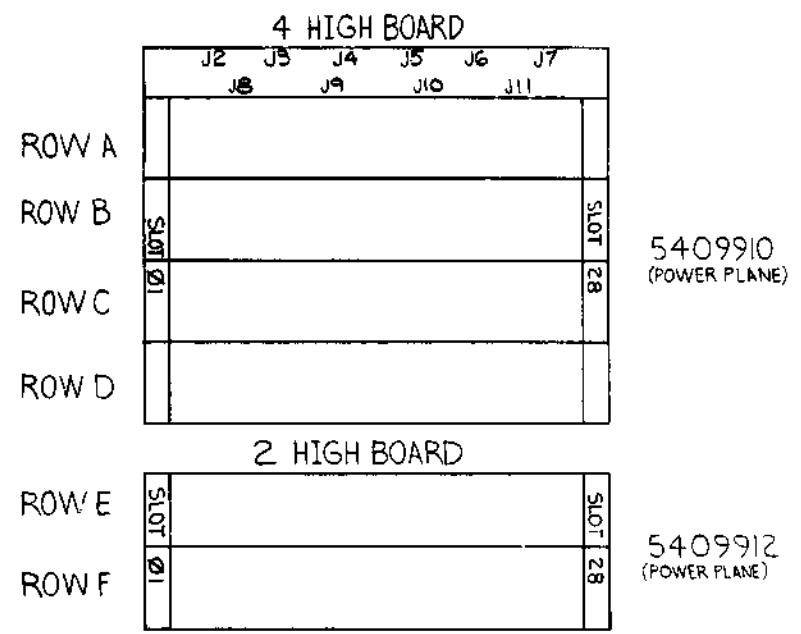
REV.	
CHG.	
NO.	

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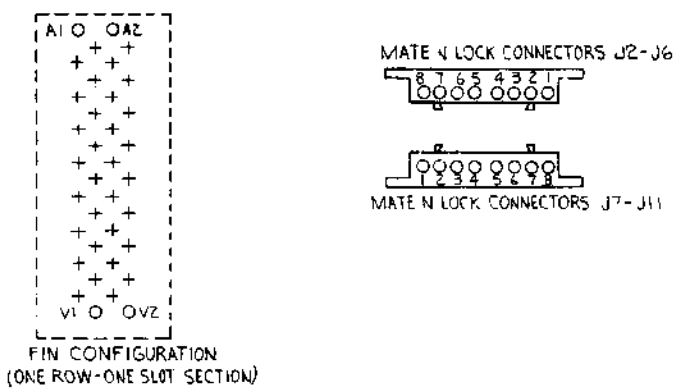
VOLTAGE	REGULATOR LOCATION	PIN	ROW	SLOT	MATE N LOCK CONNECTION	WIRE WRAP PIN
+5V	A	A2, V1	A-F	2-5	J2-5,6	
	B	A2		16-9	J2-3,4	
	B	V1		6-9	J2-3,4	
	C	A2, V1		10-15	J3-5,6	
	J			16-18	J4-6,7	
	H2			19,20	J4-1,2, J5-8	
	K			21-23	J5-1,2,3	
	L	A2, V1	A-F	24,25	J6-5,6,7	
	J	A2	A	16	J5-4,7, J6-8	
	D		A,B	26	J7-8	
	D		C-F	26-28	J7-8	
+5V	J	A2	A,B	27,28	J7-5,6	
+8V	TOP H742	B1	F	1	J2-8	SP-1
LTCL	TOP H742	R1	C	1	J2-7	
DCLOV	TOP H742				J6-1	SPIO
DCLO1	TOP H742	U1	C	12	J3-8	
ALLO1	TOP H742	S1	C	12	J3-7	
-15V	E	B2	E	2	J3-4	SP-3
+15V	TOP H742	A1	E	15	J3-2,3	SP-4
-15V	BOT. H742	B2	E	16	J3-1	SP-5
+3.8V L1	L2	V2	A,C,E	22-25	J6-3	
+19.7V L1	L2	U2	A,C,E	22-25	J6-2	
DCLOX	BOT. H742	U2	B	16	J4-3	
-15V	BOT. H742	B2	E	21	J5-5,6	SP-8
-5V	H1	C1	F	17-20,22-25	J6-4	SP-9
+2.3V H1	H1	V2	A,C,E	17-20	J4-8	SP-6
+19V H1	H1	U2	A,C,E	17-20	J4-6	SP-7
ACLOZ	BOT. H742	F1	B	28	J7-7	
DCLOZ	BOT. H742	F2	E	28	J7-4	

	PIN	ROW	SLOT	MATE N LOCK CONNECTION
GROUND	C2, N2, T1	A-F	2-25	J8-1, 8
	C2, T1	A-F	1,26-28	J9-1, 8
	B2, V2	A, B		J10-1, 8
	N1, P1, R1, S1	A		J11-1, 8
	D1, E1	B	1,26-28	

- NOTES:
- THIS LISTING IS FURNISHED TO SHOW COMBINATIONS OF MATE N LOCK CONNECTORS (J#) AND WIREWRAP PINS THAT ARE TO BE ETCHED TOGETHER. VOLTAGES WILL BE WIREWRAPPED BETWEEN 5409910 AND 5409912
 - ALL MATE N LOCK CONNECTORS AND PINS LISTED ARE CONNECTED TOGETHER BY THE GROUND PLANE (5409910-4 HIGH BOARD). WIREWRAP CONNECTIONS WILL BE MADE BETWEEN DXXT1 (5409910) AND EXXC2 (5409912)-XX= SLOT NUMBERS 01-28



REGULATOR LOCATIONS



FIN CONFIGURATION (ONE ROW-ONE SLOT SECTION)

REV	DATE	BY	CHK
A	11/45-00061	V. BOLEN	

FIRST USED ON OPTION/MODEL	QTY	DESCRIPTION	PART NO.	ITEM NO.
PDP11/45				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES				
DECIMALS	ANGLES	TITLE		
.XXX - .005	±0° 30'	11/45 BACK PANEL		
.XX - .02		PC BOARD		
.X - .1		SIZE CODE NUMBER REV.		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		D.I.C. 11/45-0-2 A		
MATERIAL	NEXT HIGHER ASSY.	SCALE		
FINISH		SHEET 1 OF 1		


REV A
11/45-0-2
D.I.C.

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DIGITAL EQUIPMENT CORPORATION

REV. C | NUMBER 7009540-0-2 | SIZE CODE K WL | 2

REVISIONS		REV.
CHK	CHANGE NO.	
JK	11/45-00057	A
	(4) 11/45-00057 3-21-74	
	V. BOAEN	
	<i>V. Boalen</i>	
JK	11/45-00060	B
	(2) 11/45-00060 3-16-74	
	V. BOAEN	
	<i>V. Boalen</i>	
JK	11/45-00061	C
	(1) 11/45-00061 4-2-75	
	V. BOAEN	
	<i>V. Boalen</i>	

FIRST USED ON OPTION MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
11/45		PARTS LIST		
DRN.	<i>3.3 Boalen</i>	DATE		
CHK'D.	<i>A. L. King</i>	DATE	MAYNARD, MASSACHUSETTS	
ENG.	<i>V. Boalen</i>	DATE	TITLE WIRE LIST	
PROJ. ENG.	<i>V. Boalen</i>	DATE		
PROD.	<i>V. Boalen</i>	DATE		
NEXT HIGHER ASSEMBLY	J-1A-7009540-0-0			
SCALE	1		SIZE CODE	REV.
SHEET	1	OF	K WL	C
			NUMBER	
			7009540-0-2	
			DIST.	

MIN NAME	NAME SORT	URL	Q	PKM	KV	RG	Y	X	Z	LEVEL	REMARKS	NEW RUN INDICATOR	RUN NUM
SEE NOTE 1											ALL WIRES 0.14 AUG UNLESS OTHERWISE NOTED	X	1
SEE NOTE 1												X	2
+15V1		P14-2								GRY		X	2
+15V2		P14-2								GRY		X	3
+15V3		P21-4								GRY		X	4
+15V4		P23-5								GRY		X	5
+15V5		P36-2								GRY		X	6
+19.7VH1		P1-3								GRY		X	7
+19.7VH2		P3-3								GRN		X	8
+19.7VH3		P26-4								GRN		X	9
+19.7VH4		P4-6								GRN		X	10
+20V1		P12-3								YEL		X	11
+20V2		P40-5								YEL		X	12
+23.2VH1		P26-5								RED		X	13
+23.2VH2		P4-6								RED		X	14
+23.2VH3		P30-5								RED		X	15
+23.2VH4		P6-3								RED		X	16
+5J1		P27-2								RED		X	17
+5J2		P4-5								RED		X	18
+5V1		P5-3								RED		X	19
+5V2		P5-4								RED		X	20
+5V3		P5-7								RED		X	21
+5V4		P5-8								RED		X	22
+5V5		P6-7								RED		X	23
+5V6		P6-8								RED		X	24
+5V7		P12-4								RED		X	25
+5V8		P7-6								RED		X	26
+5V9		P4-4								RED		X	27
+5V10		P7-5								RED		X	28
+5V11		P1-4								RED		X	29
+5V12		P3-4								RED		X	30

GRY-BLK TRIP(GND 12)

MIN NAME	NAME SORT	URL	Q	PKM	KV	RG	Y	X	Z	LEVEL	REMARKS	NEW RUN INDICATOR	RUN NUM
+5V13		P17-2								RED		X	31
+5V14		P2-6								RED		X	32
+5V15		P17-5								RED		X	33
+5V16		P2-5								RED		X	34
+5V17		P1A-5								RED		X	35
+5V18		P2-1								RED		X	36
+5V19		P14-2								RED		X	37
+5V20		P3-6								RED		X	38
+5V21		P14-5								RED		X	39
+5V22		P3-5								RED		X	40
+5V23		P20-2								RED		X	41
+5V24		P34-1								RED		X	42
+5V25		P12-1								RED		X	43
+5V26		P211-5								RED		X	44
+5V27		P18-2								RED		X	45
+5V28		P2-2								RED		X	46
+5V29		P31-2								RED		X	47
+5V30		P4-2								RED		X	48
+5V31		P31-5								RED		X	49
+5V32		P4-1								RED		X	50
+5V33		P27-5								RED		X	51
+5V34		P7-5								RED		X	52
+5V35		P24-2								RED		X	53
+5V36		P5-2								RED		X	54
+5V37		P28-5								RED		X	55
+5V38		P5-1								RED		X	56
+5V39		P24-2								RED		X	57
+5V40		P6-6								RED		X	58
+5V41		P24-5								RED		X	59
+5V42		P6-5								RED		X	60
+5V		P14-1								WHT		X	61
+5V		P2-4								WHT		X	62
-15V1		P22-4								BLU		X	63
-15V2		P5-6								BLU		X	64
-15V2		J45-4								BLU		X	65
-15V2		P12-13								BLU		X	66

MUN NAME	A/P	PIN LOCATION	BAY ORDER	DRAW RV KG Y	X	Z LEVEL	REMARKS	NEW RUN INDICATOR	RUN NUM
-15V3	P07-1	4"		BLU			010AWG	X	37
-15V3	P45-1	2"		BLU			010AWG		37
-15V3	P45-3								37
-15V4	P1-5	27.75"		BLU				X	38
-15V4	P7-3								38
-15V5	P3-1	14.5"		JLU				X	39
-15V5	P5-5								39
-15V6	P3-4	19.5"		BLU				X	40
-15V6	P7-2								40
-15VE1	P21-1	64"		BLU				X	41
-15VE1	P36-13								41
-15VF1	J45-1	65"		BLU				X	42
-15VF1	P25-1								42
-5VE1	P12-14			BRN				X	43
-5VE1	P40-3	62.75"		BRN					43
-5VH1	P26-3	72.5"		BRN				X	44
-5VH1	P6-4								44
AC L01	P14-8	70.5		YEL			010AWG	X	45
AC L01	P3-7								45
AC L01*	P14-8	47"		YEL			010AWG	X	46
AC L01*	P22-10								46
AC L02	P14-10			YEL			010AWG	X	47
AC L02	P7-7	60.25"							47
AC L02*	P14-10			YEL			010AWG	X	48
AC L02*	P22-8	46"							48
AC01	P16-1						RED-WHT TWP(AC2)	X	49
AC01	P18-1	11"							49
AC02	P16-2						WHT-RED TWP(AC1)	X	50
AC02	P18-7	11"							50
AC03	P16-7						RED-WHT TWP(AC4)	X	51
AC03	P19-6	15.75"							51
AC04	P16-8						WHT-RED TWP(AC3)	X	52
AC04	P19-7	15.75"							52
AC05	P15-1						RED-WHT TWP(AC6)	X	53
AC05	P17-6	13.5"							53
AC06	P15-2						WHT-RED TWP(AC5)	X	54
AC06	P17-7	11.5"							54

MUN NAME	A/P	PIN LOCATION	BAY ORDER	DRAW RV KG Y	X	Z LEVEL	REMARKS	NEW RUN INDICATOR	RUN NUM
AC07	P15-8						RED-WHT TWP(AC4)	X	55
AC07	P20-6	16"							55
AC08	P15-10						WHT-RED TWP(AC7)	X	56
AC08	P20-7	16"							56
AC09	P15-9						RED-WHT TWP(AC10)	X	57
AC09	P21-6	23.5"							57
AC10	P15-12						WHT-RED TWP(AC9)	X	58
AC10	P21-8	23.5"							58
AC11	P24-1						RED-WHT TWP(AC12)	X	59
AC11	P29-6	27"							59
AC12	P24-2						WHT-RED TWP(AC11)	X	60
AC12	P29-7	27"							60
AC13	P24-3						RED-WHT TWP(AC13)	X	61
AC13	P30-7	27"							61
AC14	P24-4						RED-WHT TWP(AC13)	X	62
AC14	P30-8	27"							62
AC15	P24-5						RED-WHT TWP(AC16)	X	63
AC15	P26-7	18"							63
AC16	P24-6						WHT-RED TWP(AC15)	X	64
AC16	P26-8	18"							64
AC17	P24-7	17.5"					RED-WHT TWP(AC18)	X	65
AC17	P31-6	17.5"							65
AC18	P24-8						WHT-RED TWP(AC17)	X	66
AC18	P31-7	17.5"							66
AC19	P23-1						RED-WHT TWP(AC20)	X	67
AC19	P27-6	21"							67
AC20	P23-2						WHT-RED TWP(AC19)	X	68
AC20	P27-7	21"							68
AC21	P23-8						RED-WHT TWP(AC22)	X	69
AC21	P28-6	17.5"							69
AC22	P23-10						WHT-RED TWP(AC21)	X	70
AC22	P28-7	17.5"							70
AC23	P23-9						RED-WHT TWP(AC24)	X	71
AC23	P25-6	9.5"							71
AC24	P23-12						WHT-RED TWP(AC23)	X	72
AC24	P25-8	9.5"							72

KUN NAME	A/P	PIN LOCATION	BAY ORDER	DRW RV	MG Y	X	Z LEVEL	REMARKS	NEW RUN INDICATOR	KUN NUM
AC25		P13-1			RED				X	73
AC25		P13-3	2.75"		RED			*10AWG		73
AC25'		P13-1			RED			CABLE 9107761	X	74
AC25'		P23-5	64.5"		RED					74
AC26		P13-2			WMT			CABLE 9107761	X	75
AC26		P23-6	64.5"		WMT					75
AC26'		P13-2			WMT			*10AWG	X	76
AC26'		P13-4	2.75"		WMT					76
AC27		P15-5	57"					RED-WMT TWP TO TIME MTM	X	77
AC27		P34								77
AC28		P15-6	57"					WMT-RED TWP TO TIME MTM	X	78
AC28		P35								78
DC L01		P14-12	69.5"		VIO			*10AWG	X	79
DC L01		P3-6			VIO					79
DC L02		P22-9	71"		VIO			*10AWG	X	80
DC L02		P7-4			VIO					80
DC L0X		P22-12			VIO			*10AWG	X	81
DC L0X		P4-3	76.5"		VIO					81
DC L0Y		P14-9	64"		VIO				X	82
PC L0Y		P6-1			VIO				X	82
GND D1		P14-4			BLK				X	83
GND D1		P21-3	26.5"		BLK				X	83
GND D2		P14-5	73"		BLK				X	84
GND D2		P8-8			BLK				X	84
GND D3		P14-6			BLK				X	85
GND D3		P25-3	43.5"		BLK				X	85
GND D4		P10-5	74"		BLK				X	86
GND D4		P22-3			BLK				X	86
GND D5		P3A	9"		BLK				X	87
GND D5		P9-6			BLK				X	87
GND D6		P11-7	9"		BLK				X	88
GND D6		P39			BLK				X	88
GND D9		P12-8	33.5"		BLK				X	89
GND D9		P9-3			BLK				X	89
GND D0		P11-8			BLK				X	90
GND D0		P36-6	25.5"		BLK				X	90

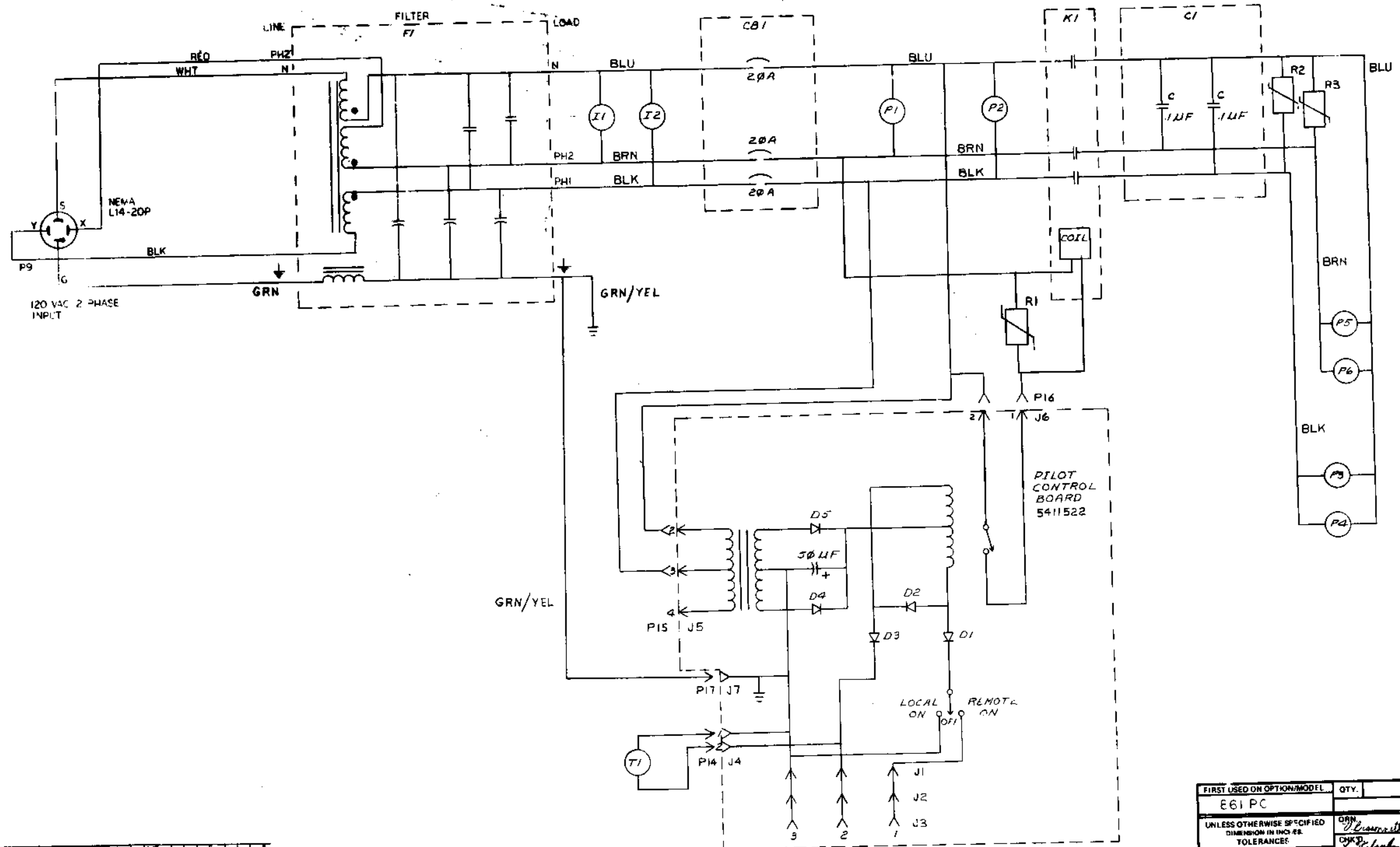
KUN NAME	A/P	PIN LOCATION	BAY ORDER	DRW RV	MG Y	X	Z LEVEL	REMARKS	NEW RUN INDICATOR	KUN NUM
GND J1		P1-2			BLK				X	91
GND J1		P10-3	24.5"		BLK					91
GND J2		P1-6			BLK				X	92
GND J2		P10-4	24.5"		BLK					92
GND J3		P41	9"		BLK				X	93
GND J3		P8-7			BLK					93
GND J4		P1-7	36		BLK				X	94
GND J4		P36-5			BLK					94
GND A1		P17-3	64.5"		BLK				X	95
GND A1		P8-2			BLK					95
GND A2		P17-4	66"		BLK				X	96
GND A2		P8-1			BLK					96
GND H1		P18-3	70.75"		BLK				X	97
GND H1		P6-4			BLK					97
GND B2		P18-4	71.5"		BLK				X	98
GND B2		P8-3			BLK					98
GND C1		P19-3	73.5"		BLK				X	99
GND C1		P8-6			BLK					99
GND C2		P19-4	73.5"		BLK				X	100
GND C2		P8-5			BLK					100
GND D1		P20-3	61"		BLK				X	101
GND D1		P36-7			BLK					101
GND D2		P12-7	61"		BLK				X	102
GND D2		P20-4			BLK					102
GND E1		P21-2			BLK				X	103
GND E1		P36-11	64"		BLK					103
GND E2		P42-5			BLK			BLK-ORN TWP(+2UVEL1)	X	104
GND E2		P40-2	1.3"		BLK					104
GND F1		P25-2	75.5"		BLK				X	105
GND F1		P9-4			BLK					105
GND H1		P11-1			BLK				X	106
GND H1		P31-3	74.5"		BLK					106
GND H2		P10-8			BLK				X	107
GND H2		P31-4	74"		BLK					107
GND H3		P11-2			BLK				X	108
GND H3		P26-2	73"		BLK					108

3145PH.UNL NAME SORT	URL100.SAV(10)	18-MAR-72	24-MAR-75	1515	PAGE 7			
RUN NAME	A/P	PIN LOCATION	BAY # ORDER	DRAW RV	RG Y X Z LEVEL	REMARKS	NEW RUN INDICATOR	RUN NUM
GND J1		P27-3 82.5"				BLK	X	109
GND J1		P9-2						109
GND J2		P27-4 82.5"				BLK	X	110
GND J2		P9-1						110
GND K1		P11-4					X	111
GND K1		P28-3 78"				BLK		111
GND K2		P11-3					X	112
GND K2		P28-4 78"				BLK		112
GND L1		P11-6					X	113
GND L1		P29-3 81"				BLK		113
GND L2		P11-5					X	114
GND L2		P29-4 81"				BLK		114
GND L3		P30-2					X	115
GND L3		P9-8 89"				BLK		115
LINE CLOCK		P14-11					X	116
LINE CLOCK		P37-2 58.5"				BRN #18AWG		116
LINE CLOCK'		P2-7					X	117
LINE CLOCK'		P37-2 33"				BRN #18AWG		117
LO GND 01		P10-1					X	118
LO GND 01		P14-7 66"				BLK #18AWG		118
LO GND 02		P10-2					X	119
LO GND 02		P22-7 78"				BLK #18AWG		119
LO GND 03		P37-1 30.5"				BLK #18AWG	X	120
LO GND 03		P9-5						120
PWR CONT COM1		P1-3					X	121
PWR CONT COM1		P32-3 98.25"				BLK-WHT TWP(P.C SW1)#18AWG		121
PWR CONT SW1		P1-4					X	122
PWR CONT SW1		P32-1 98.25"				WHT=BLK TWP(P.C COM1)#18AWG		122
SHIELD		P23-4					X	123
						CABLE 9107761		123
THERM 1		P13-6					X	124
THERM 1		P33-3 77.75"				BLK #18AWG		124
THERM 2		P13-5					X	125
THERM 2		P33-2 77.75"				RED #18AWG		125



NOTE:
 1. R1, R2, + R3 ARE MOV VARISTOR SPIKE SUPPRESSORS.

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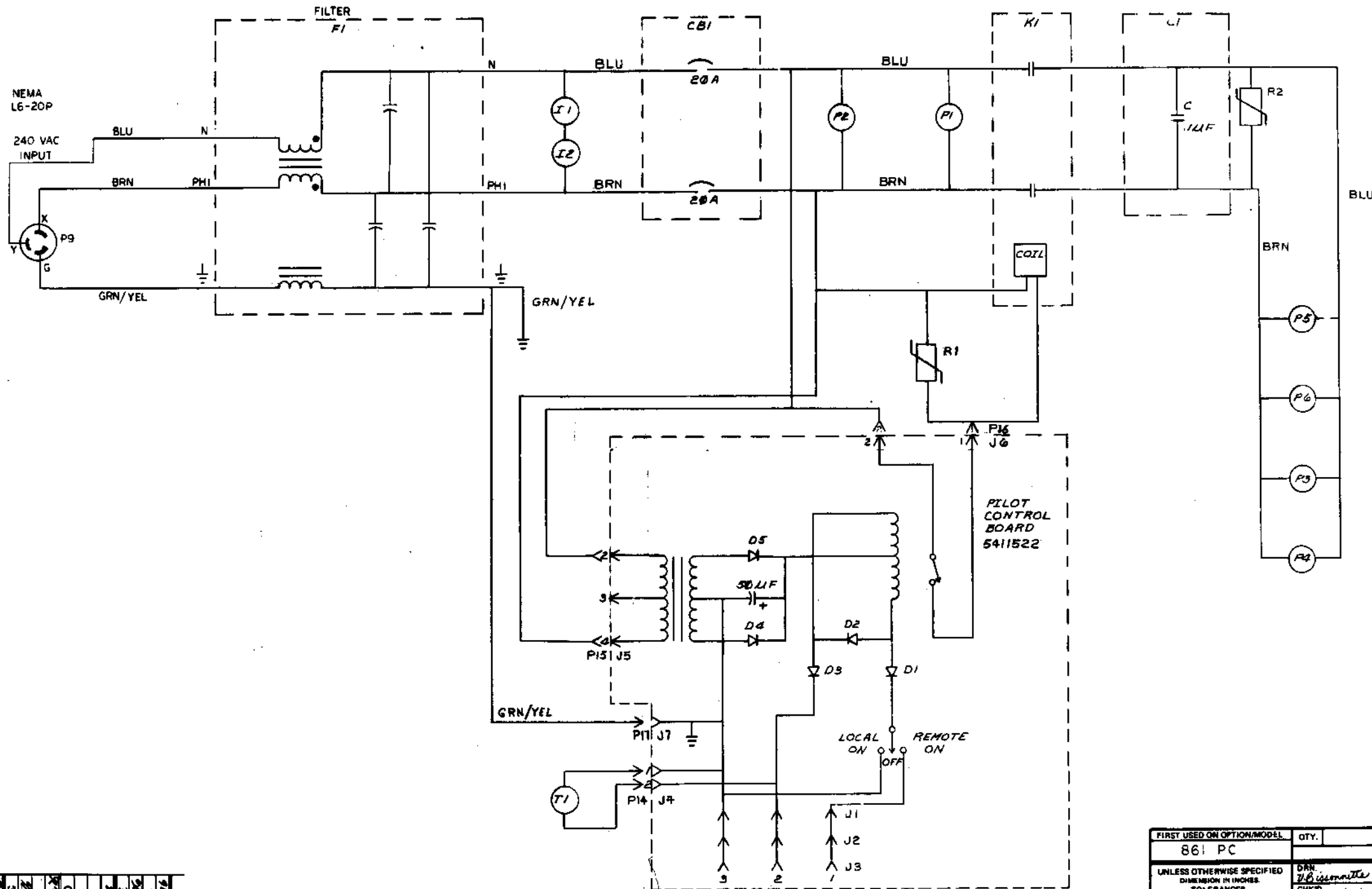
REV	CHANGE NO.	BY	DATE
1	1561-00005	A	12-17-76
2	1561-00009	B	12-17-76
3	1561-00010	C	12-17-76
4	1561-00011	D	12-17-76
5	1561-00012	E	12-17-76

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
E61 PC		PARTS LIST		
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES.	GRN	DATE 8-11-72	DIGITAL EQUIPMENT CORPORATION	
TOLERANCES:	CHK'D	DATE 12-17-76	MILWAUKEE WISCONSIN 53208-0001	
DECIMALS	ENG	DATE 12-17-76	TITLE	
XXX + .004	PROJ ENGR	DATE 12-17-76	CIRCUIT SCHEMATIC	
XX + .02	PROJ MGR	DATE 12-17-76	(861-A-PC)	
X + .1	PRDD	DATE 12-17-76	SIZE CODE	
REMOVE BURRS AT U BREAK SHAWP CORNERS SURF. 75 QUALITY	CHK'D	DATE 12-17-76	NUMBER	
MATERIAL	NEXT HIGHER ASSY.	B-DD-661-0	DCS 861-A-1	
FINISH	SCALE	1 OF 1	REV. E	
	SHEET		DST.	

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3 1-8-73 861-B-1 2

NOTE:
1. R1-R2 ARE MOV VARISTOR SPIKE SUPPRESSORS.

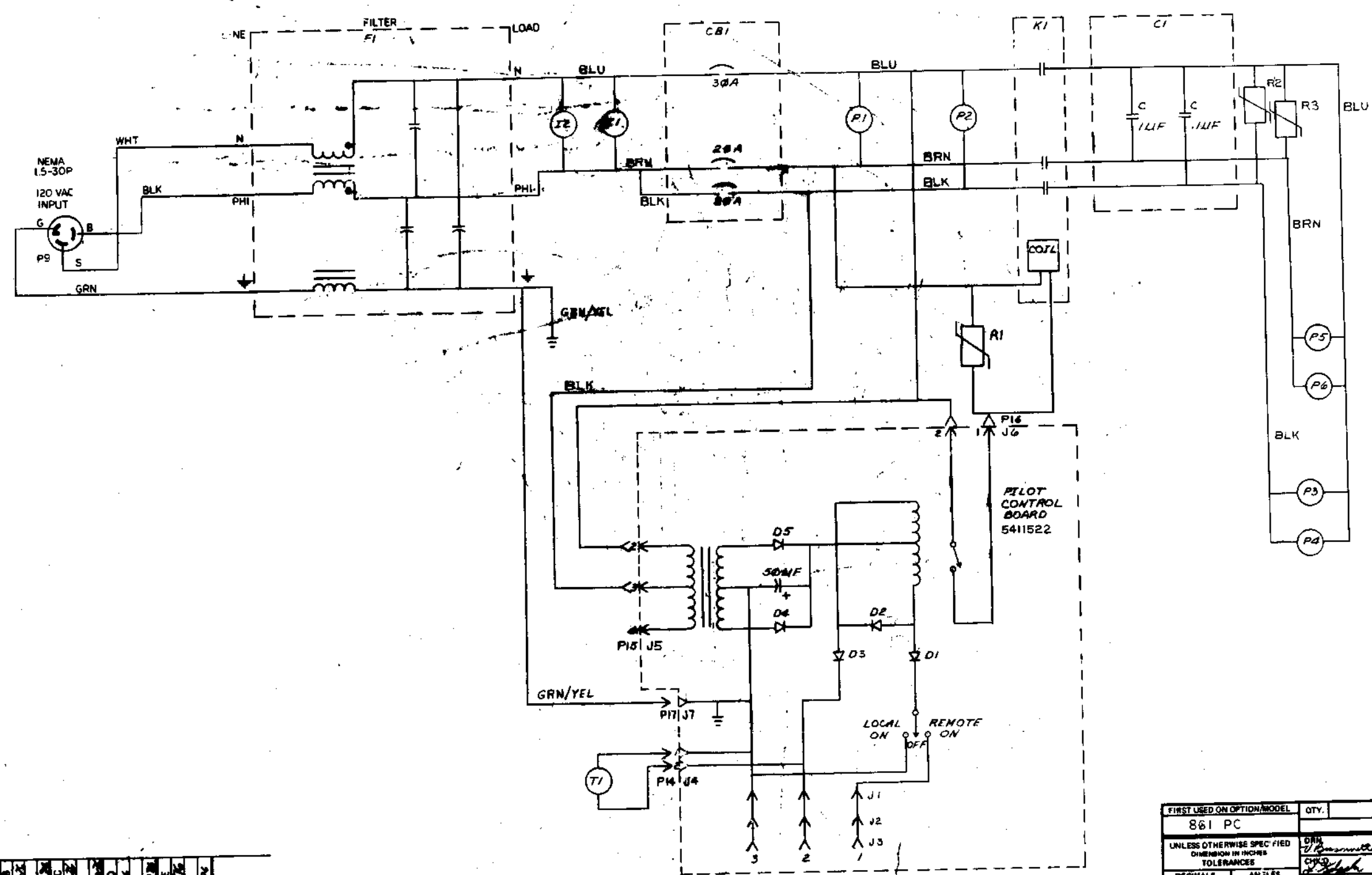


REV.	CHANGE NO.	DATE	BY	CHK'D
1				
2				
3				
4				
5				
6				
7				
8				
9				
10				
11				
12				
13				
14				
15				
16				
17				
18				
19				
20				

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
861 PC				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES		DRAWN: <i>J. Bismonte</i> DATE: 9-11-72	EQUIPMENT CORPORATION	
DECIMALS ANGLES		CHK'D: <i>J. Bismonte</i> DATE: 11-4-72	SOFTWARE MANUFACTURED TO	
XX - .008	± 0° 30'	ENG: <i>J. Bismonte</i> DATE: 10-4-72	TITLE: CIRCUIT SCHEMATIC (861-B-PC)	
XX - .02		DATE: 10-4-72		
X - .1		DATE: 10-4-72	MATERIAL: NEXT HIGHER ASSY.	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		DATE: 10-4-72	B-DD-861-0	
FINISH		DATE: 10-4-72	D CS 861-B-1	
SCALE		DATE: 10-4-72	NUMBER	
SHEET		DATE: 10-4-72	REV. E	

D CS 861-B-1

NOTE:
 1. R1, R2+R3 ARE MOV VARIATOR SPIKE SUPPRESSORS.



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REV.	CHANGE NO.	BY	DATE
1	0000	A	12-10-71
2	0001	B	12-10-71
3	0002	C	12-10-71
4	0003	D	12-10-71
5	0004	E	12-10-71
6	0005	F	12-10-71
7	0006	G	12-10-71
8	0007	H	12-10-71
9	0008	I	12-10-71
10	0009	J	12-10-71
11	0010	K	12-10-71
12	0011	L	12-10-71
13	0012	M	12-10-71
14	0013	N	12-10-71
15	0014	O	12-10-71
16	0015	P	12-10-71
17	0016	Q	12-10-71
18	0017	R	12-10-71
19	0018	S	12-10-71
20	0019	T	12-10-71
21	0020	U	12-10-71
22	0021	V	12-10-71
23	0022	W	12-10-71
24	0023	X	12-10-71
25	0024	Y	12-10-71
26	0025	Z	12-10-71

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	TITL
861 PC				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES	DATE	DATE	DATE	DATE
DECIMALS	AN 3LES	± 1/30"		
.XXX - .005				
.XX - .02				
.X - .1				
REMOVES BURRS AND BEAK SHARP CORNERS SURFACE QUALITY				
MATERIAL	NEXT HIGHER ASSY.			
FINISH	B-DD-861-C			
	SCALE			
	SHEET	OF		
	DIST.			

CIRCUIT
 SCHEMATIC
 861-C P.C.

DCS 861-C-1

DCS 861-C-1

REV E

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DIGITAL EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS			
PACKAGING INSTRUCTION		REV: <u>A</u>	DATE: <u>4-73</u>
TITLE 861 POWER CONTROL, INTERPLANT PACKAGE		_____	_____
MATERIAL REQUIREMENTS			
Quantity	Identification No.	Purchase Spec	Description
1	7-2008 1314 0500-0	9905229	Full overlap carton
2		9905228	Expanded polystyrene foam insert
A/R			3-inch wide Glasflex tape
PACKAGING INSTRUCTIONS			
Step	Procedure		
1	Set up the full over lap carton (9905229) using one strip of tape across the bottom and extending up the sides approximately three inches. See Figure 1.		
2	Place one expanded polystyrene foam insert (9905228) in each end of the carton with the slots in the foam facing inward.		
3	Place the 861 power control in the carton with the ears of the upright sliding into the slots in the foam.		
4	Coil the power cord so that it fits into the cavity in the rear of the power control.		
5	Close and seal the carton with one piece of tape across the top of the carton and extending down the sides approximately three inches.		
ENG. <i>[Signature]</i>		APPD. <i>[Signature]</i>	REV A
DATE 4/13/73	SIZE A	CODE PI	NUMBER 3700083-0-0

D&C 8-(551)-1031-1-R471
DRA - 129

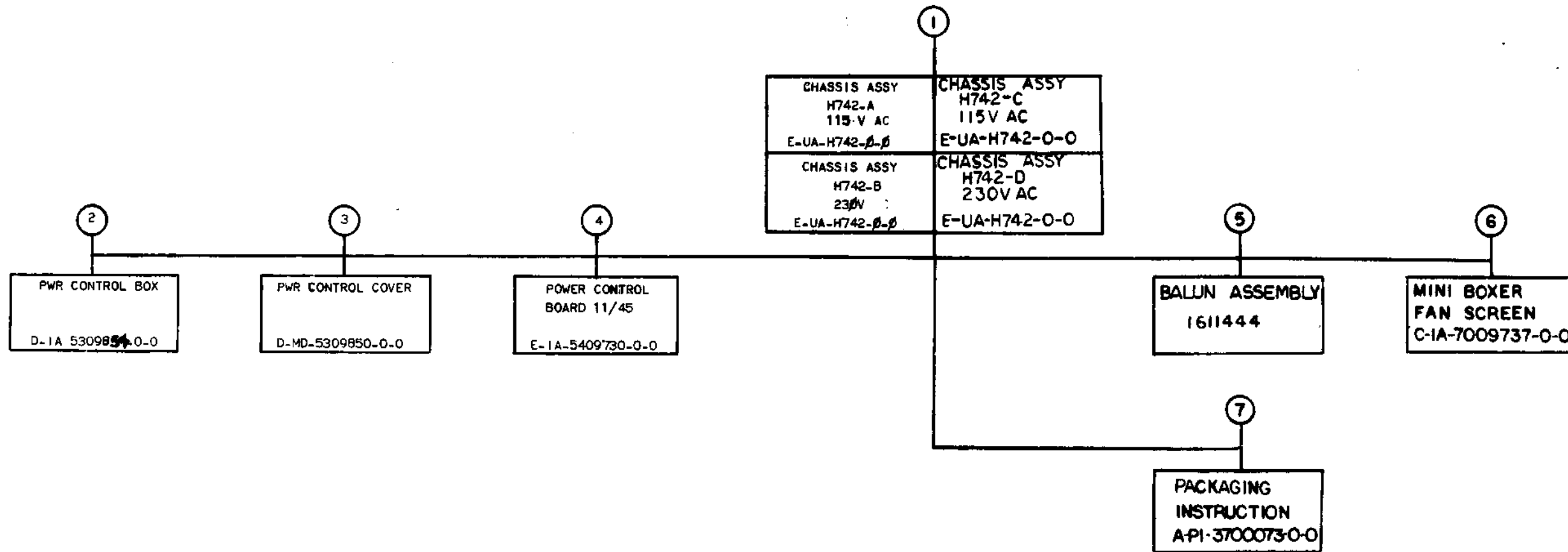
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PACKAGING INSTRUCTION		REV: <u>A</u>	DATE: <u>4/73</u>
TITLE 861 POWER CONTROL, INTERPLANT PACKAGE		_____	_____
FIGURE 1			
<p>NOTE Make changes to the "C" size original only and rephotograph.</p>			
ENG. <i>[Signature]</i>		APPD. <i>[Signature]</i>	REV A
DATE 4/13/73	SIZE A	CODE PI	NUMBER 3700083-0-0

D&C-107



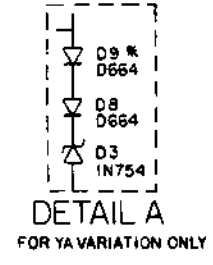
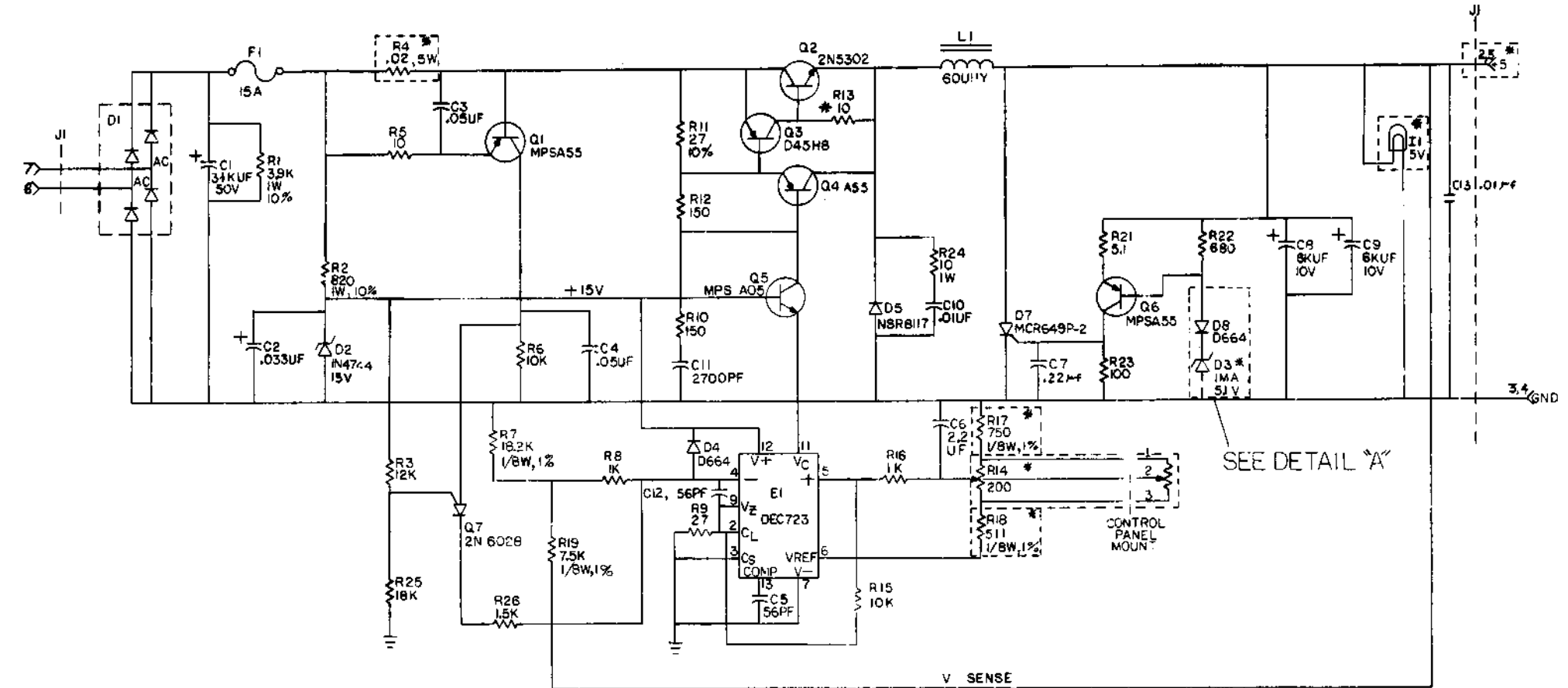




TITLE	SHEET	SIZE	CODE	NUMBER	REV
CHASSIS ASSY H742	2 OF 3	B	DD	H742- β	Y



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* FUSIBLE RESISTOR

FOR YA VARIATION COMPONENT VALUES ARE AS FOLLOWS:
 R4 - .06 5W
 R4 - 1K 10 TURN
 R7 - 300 1/8W 1%
 R19 - 150 1/4W 5%
 D3 - IN754
 I1 - 15V
 J1-2,3 - 20-80V

* D9 - D664 ADDED FOR YA VARIATION ONLY

UNLESS OTHERWISE INDICATED:
 RESISTORS ARE 1/4W, 5%

1	D. MARTEL
2	H744-00010
3	A. BARON 5-30-76
4	H. BARON 5-30-76
5	H. BARON 5-30-76
6	H. BARON 5-30-76
7	H. BARON 5-30-76
8	H. BARON 5-30-76
9	H. BARON 5-30-76
10	H. BARON 5-30-76
11	H. BARON 5-30-76
12	H. BARON 5-30-76
13	H. BARON 5-30-76
14	H. BARON 5-30-76
15	H. BARON 5-30-76
16	H. BARON 5-30-76
17	H. BARON 5-30-76
18	H. BARON 5-30-76
19	H. BARON 5-30-76
20	H. BARON 5-30-76
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23	H. BARON 5-30-76
24	H. BARON 5-30-76
25	H. BARON 5-30-76
26	H. BARON 5-30-76
27	H. BARON 5-30-76
28	H. BARON 5-30-76
29	H. BARON 5-30-76
30	H. BARON 5-30-76
31	H. BARON 5-30-76
32	H. BARON 5-30-76
33	H. BARON 5-30-76
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35	H. BARON 5-30-76
36	H. BARON 5-30-76
37	H. BARON 5-30-76
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41	H. BARON 5-30-76
42	H. BARON 5-30-76
43	H. BARON 5-30-76
44	H. BARON 5-30-76
45	H. BARON 5-30-76
46	H. BARON 5-30-76
47	H. BARON 5-30-76
48	H. BARON 5-30-76
49	H. BARON 5-30-76
50	H. BARON 5-30-76

QTY	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
	ETCH BOARD REV	F H		
	IN964A	SAME	MPSA55	
	D004	IN3605	2N5302	
	H744-00010		D45H8	
	H744-00010		MPSA55	
	IN4744	SAME		
	NSR8117			
	2N665			
	DEC NO.	EIA NO.	DEC NO.	EIA NO.
SEMICONDUCTOR CONVERSION CHART				
SCALE	D CS		H744-0-1	REV. 7 Y
SHEET	OF		DIST.	

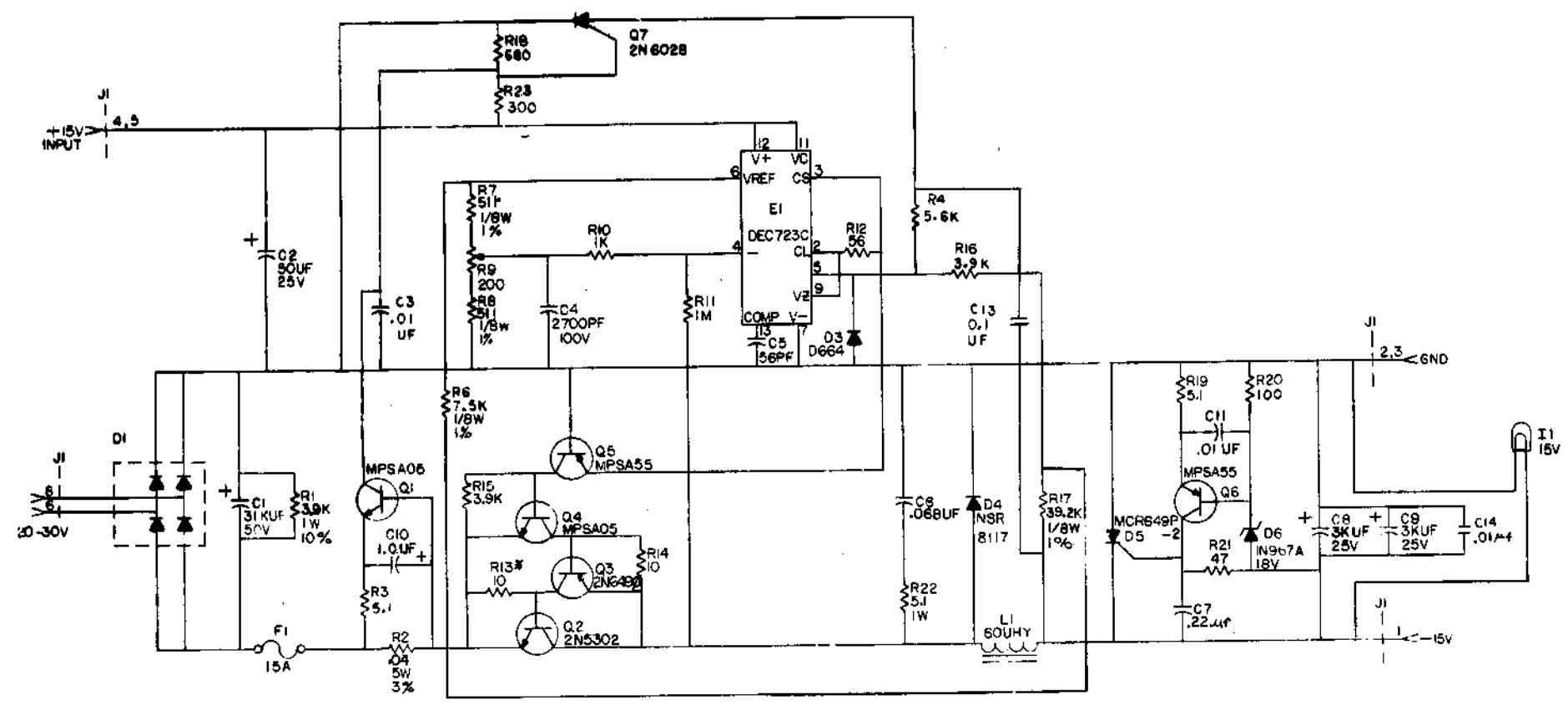
5V REGULATOR

D CS H744-0-1 7 Y

H744-0-1



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* FUSIBLE
 UNLESS OTHERWISE INDICATED:
 RESISTORS = 1/4W, 5%

QTY	REF DESIGNATION	DESCRIPTION	PART NO	ITEM NO.
PARTS LIST				
	ETCH BOARD REV	E		
	D664	IN 3606	D45H8	
	NSR 8117		2N 6028	
	MCR649P - 2			
	IN 967A	SAME		
	2N 5302			
	MPS A05			
	MPS A55			
	DEC NO.	EIA NO.	DEC NO.	EIA NO.
SEMICONDUCTOR CONVERSION CHART				
SCALE		DISTRIBUTION		
SHEET		OF		

EQUIPMENT CORPORATION
 MANAGED BY: H745-0-1

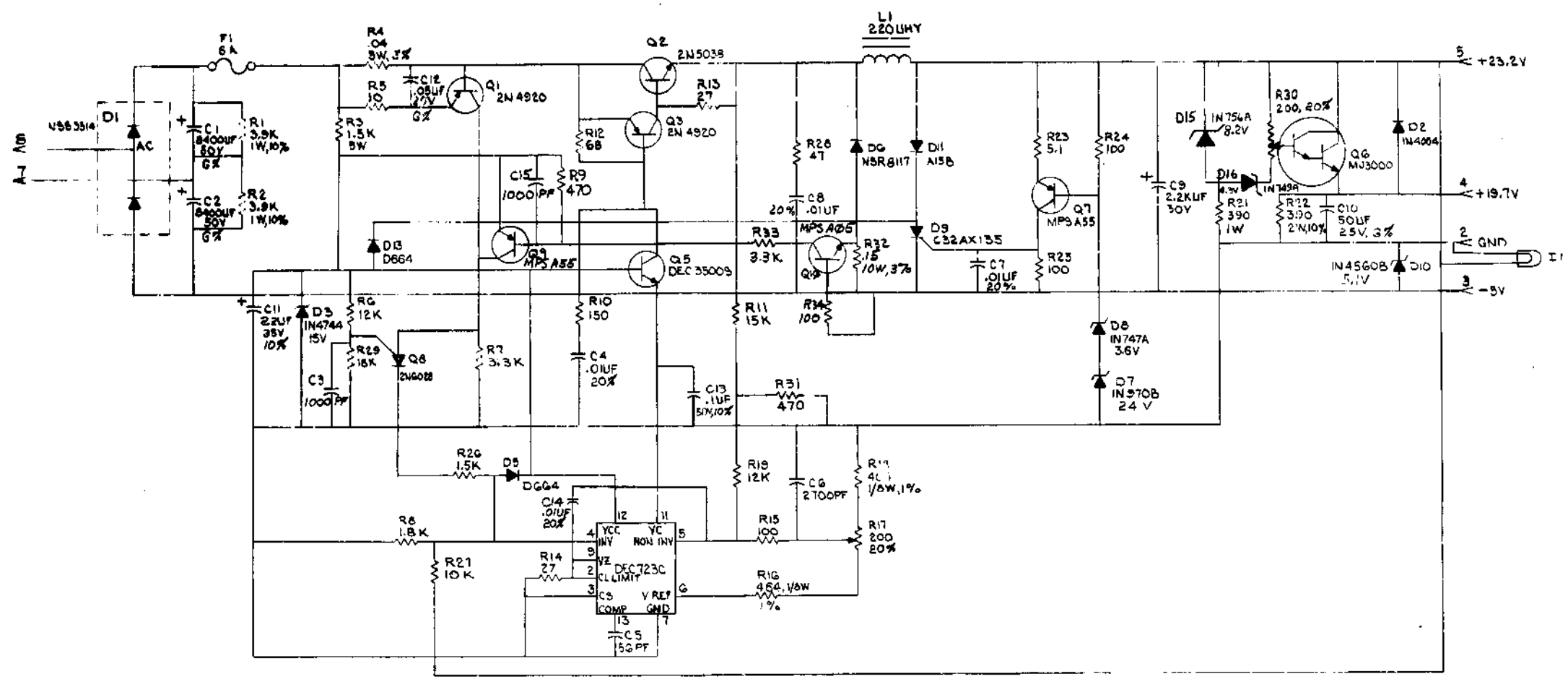
-15V REG.

DCS H745-0-1 2



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1-0-972H 2



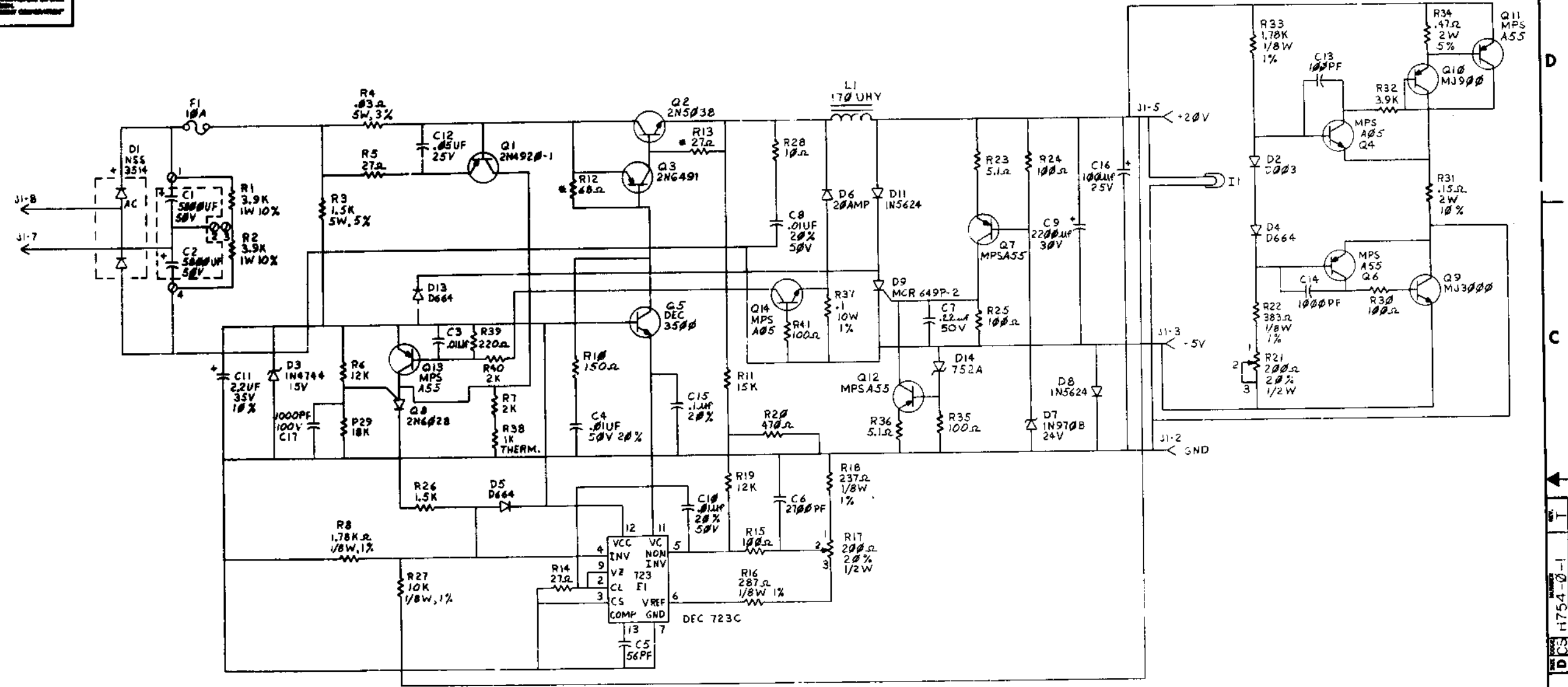
UNLESS OTHERWISE INDICATED:
 CAPACITORS - 100V, 5%
 RESISTORS - 1/4W, 5%
 C% = -10% + 15%

REV	DATE	BY	CHKD	DESCRIPTION
1	12-2-72	G. POTTER		INITIAL DESIGN
2	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
3	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
4	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
5	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
6	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
7	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
8	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
9	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
10	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
11	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
12	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
13	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
14	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
15	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
16	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
17	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
18	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
19	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
20	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
21	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
22	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
23	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
24	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
25	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
26	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
27	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
28	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
29	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
30	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
31	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
32	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
33	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
34	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
35	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
36	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
37	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
38	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
39	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
40	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
41	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
42	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
43	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
44	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
45	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
46	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
47	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
48	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
49	12-2-72	G. POTTER		REVISED FOR MANUFACTURE
50	12-2-72	G. POTTER		REVISED FOR MANUFACTURE

QTY	REF DESIGNATION	DESCRIPTION	PART NO.	TEM NO.
PARTS LIST				
ETCH BOARD REV H				
DIGITAL EQUIPMENT CORPORATION				
MOS REGULATOR				
SCALE: DCS H746-0-1				
SEMICONDUCTOR CONVERSION CHART				

H746-0-1 P

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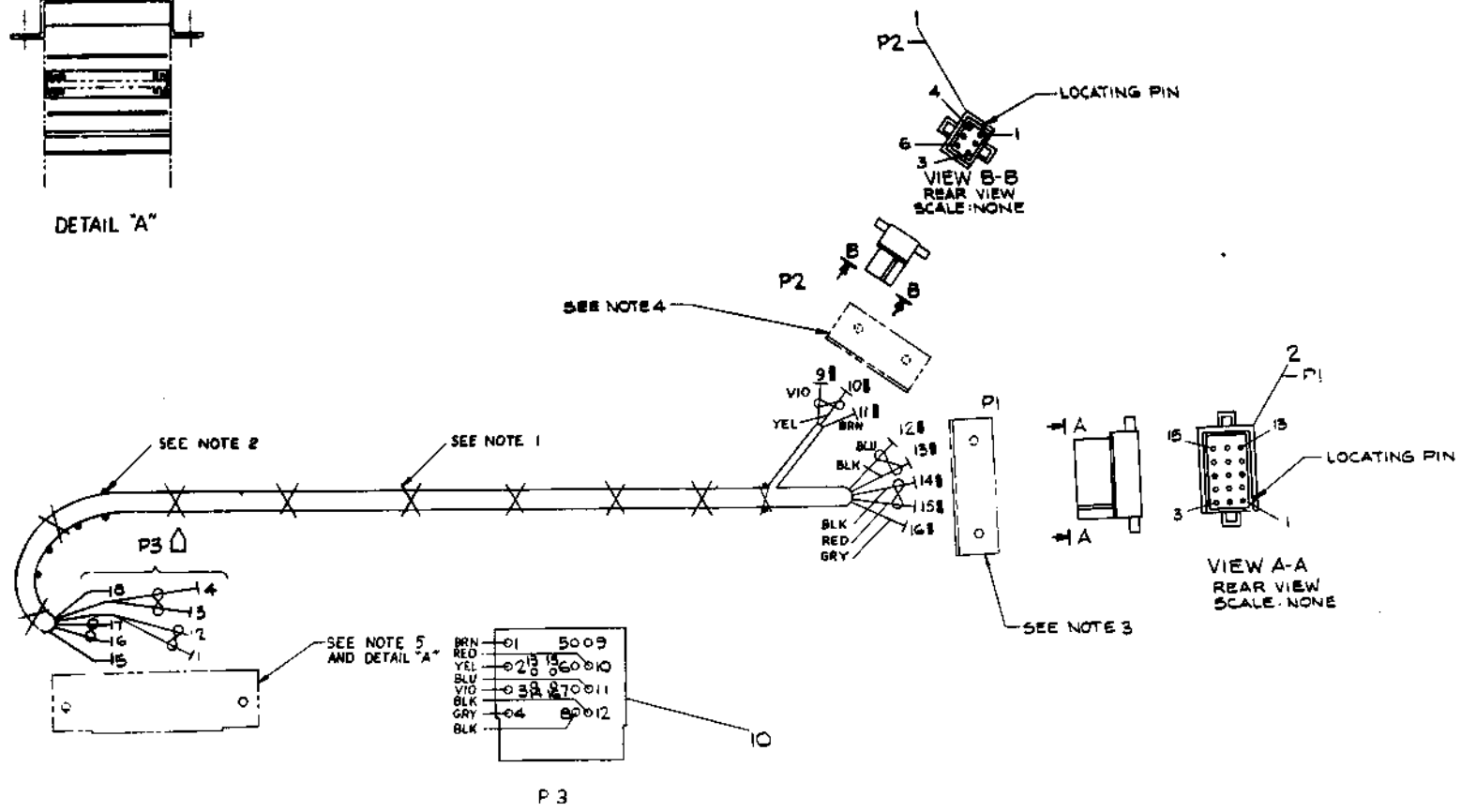
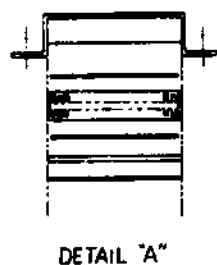


* FUSIBLE RESISTOR

DRN: BESSONNETTE 8-2-72	FIRST ORDER ON	1-0-0-0-0
CHK'D: V. WYLES 12-17-72	H754	
ENG. L. BURTON 5-7-73	TITLE	
PROJ. ENG. R. BAPTISTE 5-7-73	+20 VOLT REGULATOR	
PROD. L. BAPTISTE 5-7-73		
NEXT HIGHER ASSY.	SIZE CODE	NUMBER
	D CS	H754-0-1
SCALE		REV. T
SHEET 2 OF 2	DIST.	

WIRE TABLE									
ITEM NO.	DESCRIP.	COLOR	POINT	CONNECTION	TERM.	POINT	CONNECTION	TERM.	SIGNAL
5	14	RED	3	P2-10	SOLD	18	P1-1	3	+5V
5	14	BLK	4	P2-9	SOLD	18	P1-7	3	GND
5	14	GRY	5	P2-8	SOLD	18	P1-2	3	+5V
6	14	BLK	1	P2-12	SOLD	13	P1-9	3	GND
6	14	BLU	2	P2-11	SOLD	13	P1-18	3	-15V
6	19	BRN	8	P2-1	SOLD	11	P2-2	3	LTC
7	16	VIO	6	P2-3	SOLD	9	P2-3	3	Dcke
7	17	YEL	7	P2-2	SOLD	10	P2-4	3	Asks
11	22	-	-	P2-13	SOLD	-	P2-14	SOLD	Dcke
11	22	-	-	P2-15	SOLD	-	P2-15	SOLD	Asks

- NOTES:**
- USE TIE WRAPS (X) ITEM #4 APPROXIMATELY EVERY THREE (3) INCHES WHEN NECESSARY AND AT EVERY BREAKOUT POINT.
 - DOT (•) INDICATES NAIL LOCATIONS FOR ASSEMBLY USE ONLY. COVER NAILS WITH SHRINK TUBING TO PREVENT CUTTING HARNESS.
 - USE CONN BRKT G-MD-980576-H6-0 MOUNT WITH WOOD SCREWS. USE MATING CONN 1209550-5.
 - USE CONN BRKT G-MD-980576-H6-0 MOUNT WITH WOOD SCREWS. USE MATING CONN 1209550-06.
 - USE CONN. HOLD DOWN B-MD-980576-00 WITH PLATE B-MD-980576-0-1. USE TAPE DEC #9008734 (CONN H607 DEC #1209123) REMOVE PINS / FLANGES AS SHOWN IN DETAIL "A". MOUNT WITH WOOD SCREWS.



0 IN. 6 IN. 12 IN.

SCALE

DO NOT REDUCE
DO NOT BUILD FROM REDUCED PRINT

CAUTION - DRAWING SIZE AND SCALE REDUCED FOR MICROFILM NOT TO BE USED FOR PRODUCTION

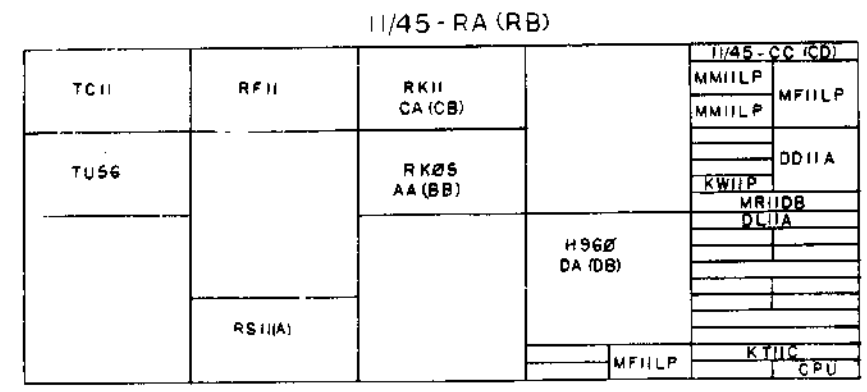
WR BUSS WIRE #22AWG	9107560-01	11
1 POWER CONN	6-72	10
A/W WIRE #22AWG GRN	907350-08	9
A/W WIRE #22AWG BRN	907350-11	8
A/W WIRE #22AWG YEL/VIO	907350-47	7
A/W WIRE #22AWG BLK/BLU	907350-08	6
A/W WIRE #22AWG BLK/RED	907350-08	5
A/W WRAP TIE	500710-1	4
2 DIN NAIL	120918-02	3
1 HO-SING CONN. 3PIN	120918-15	2
1 HO-SING CONN. 6PIN	120918-16	1

UNLESS OTHERWISE SPECIFIED	USE THE FOLLOWING	EQUIPMENT CORPORATION
WIRE GAUGE	AWG	
WIRE COLOR		
WIRE TYPE		
WIRE PARTS - ST		
G772 SYSTEM UNIT HARNESS		
81A 7009562 0 0		

DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS			LEGEND		QUANTITY / VARIATION											
ACCESSORY LIST			D	DOCUMENT	ALL VARIATIONS						KIT CHECK	BY	DATE	INSTALLATION CHECK	BY	DATE
			DN	DOCUMENT CHANGE NOTICE												
MADE BY <i>J. Horner</i>		CHECKED <i>J. Horner</i>	SECTION		PA	PAPER TAPE ASCII										
DATE <i>6-15-72</i>		DATE <i>6-15-72</i>			PB	PAPER TAPE BINARY										
ENG <i>J. Horner</i>		PROD <i>Jack Horner</i>	ISSUED SECT.		PM	PAPER TAPE READ-IN-MODE										
DATE <i>6/20/72</i>		DATE <i>6-20-72</i>														
ITEM NO.	DWG NO. / PART NO.	DESCRIPTION														
	Lib Kit 11-Bas 45-B-K	System Software Kit														
	Dec 11-H45B-D	11/45 Systems and installation Manual														
	112-01071-1854-D-09-25	11 Peripherals and interfacing Handbook														
	112-01071-1876-D-09	11/45 Processor Handbook														
	Dec 11-HKBA-D	KB11 Maintenance Manual														
	B-DD-11/45-0	PDP-11/45 Systems Eng. Drawings														
		Dec Supplies List														
		Log Book														
		PDP-11 Instruction Card														
		H960C Cab Filter														
	9007221	AGC 5 A Fuse														
	9007226	AGC 15A Fuse														
	9009039	Fuse 2/10A SB														
		Key on off Switch														
	7008855	POWER CABLE, SYSTEM UNIT														
TITLE		ASSY. NO.			SIZE	CODE	NUMBER			REV.	ECO NO					
Central Processor					A	AL	11/45 - 00065			B	11/45-00065					
SHEET		OF			DIST.											

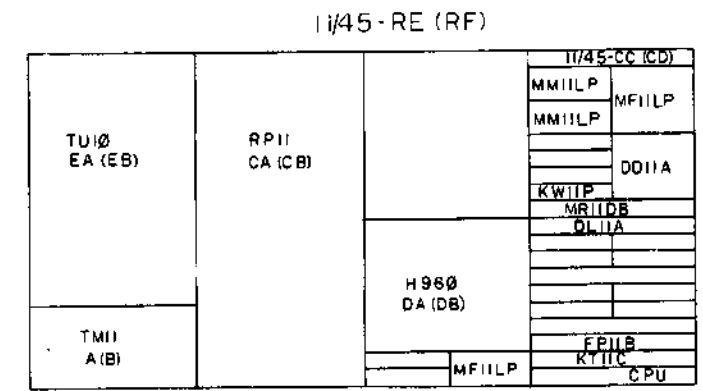
- NOTES:**
1. SYSTEMS IN () ARE 230V 50 HZ VERSIONS.
 2. DLIIA SPEED GROUP 3 (300 BAUD ONE STOP BITS, NO PARITY, 8 BITS)
 3. DLIIA MUST BE SET FOR 150 BAUD TO RUN DIAGNOSTICS.

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SOFTWARE QR430-AC

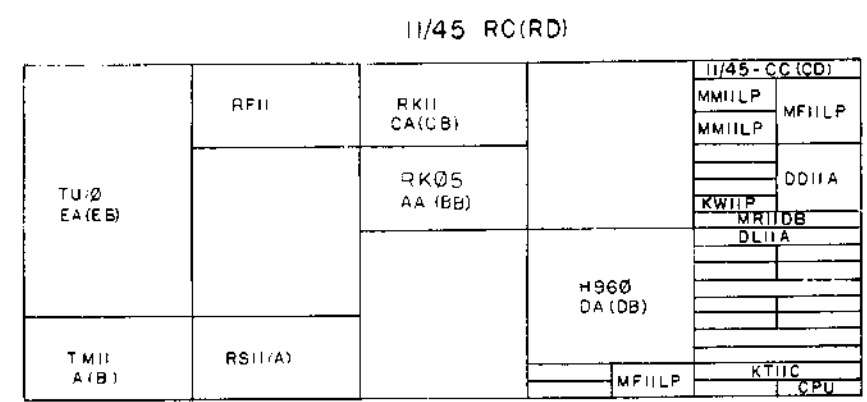
LA30 CA (CD)



SOFTWARE QR430-AD

RP03 AS (BS)

LA30 CA (CD)



SOFTWARE QR430-AD

LA30 CA (CD)

REV	CHG	NO	BY	DATE	REASON
A					REVISED & REDRAWN
B					BY T. ZSERALD
C					BY LIAF. GREGG
					BY V. BUAEN

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	REV.																										
PDP 11/45																														
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES		<table border="1"> <tr> <td>DECIMALS</td> <td>ANGLES</td> </tr> <tr> <td>XX + .006</td> <td>10° 30'</td> </tr> <tr> <td>X - .1</td> <td></td> </tr> </table>	DECIMALS	ANGLES	XX + .006	10° 30'	X - .1		<table border="1"> <tr> <td>DRAWN</td> <td>DATE</td> </tr> <tr> <td>B. CRAMM</td> <td>11-6-72</td> </tr> <tr> <td>CHK'D</td> <td>DATE</td> </tr> <tr> <td>J. BAILEY</td> <td>11-10-72</td> </tr> <tr> <td>ENG.</td> <td>DATE</td> </tr> <tr> <td>SWANSON</td> <td>11-3-72</td> </tr> <tr> <td>PROJ. ENG.</td> <td>DATE</td> </tr> <tr> <td>SWANSON</td> <td>11-3-72</td> </tr> <tr> <td>PROD.</td> <td>DATE</td> </tr> <tr> <td>HILGENDORF</td> <td></td> </tr> </table>		DRAWN	DATE	B. CRAMM	11-6-72	CHK'D	DATE	J. BAILEY	11-10-72	ENG.	DATE	SWANSON	11-3-72	PROJ. ENG.	DATE	SWANSON	11-3-72	PROD.	DATE	HILGENDORF	
DECIMALS	ANGLES																													
XX + .006	10° 30'																													
X - .1																														
DRAWN	DATE																													
B. CRAMM	11-6-72																													
CHK'D	DATE																													
J. BAILEY	11-10-72																													
ENG.	DATE																													
SWANSON	11-3-72																													
PROJ. ENG.	DATE																													
SWANSON	11-3-72																													
PROD.	DATE																													
HILGENDORF																														
MATERIAL		NEXT HIGHER ASSY.	SIZE CODE	NUMBER																										
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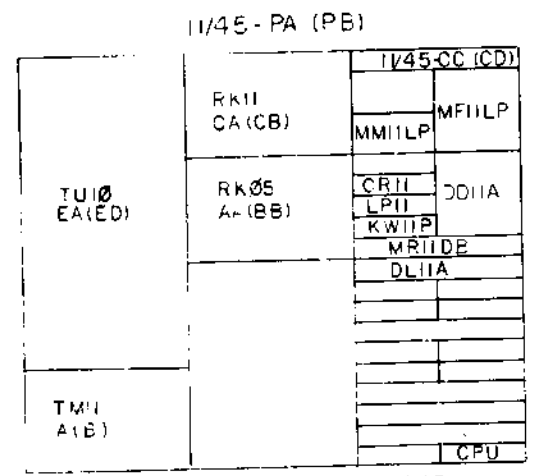
DIGITAL EQUIPMENT CORPORATION

TIME SHARING SYSTEMS

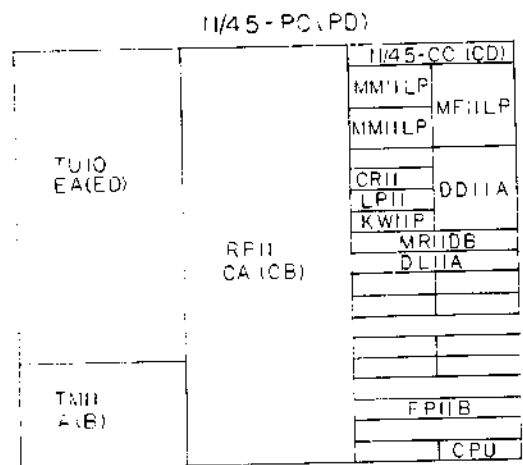
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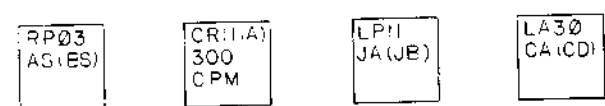
373



SOFTWARE QJ250-AD



SOFTWARE QJ250-AD

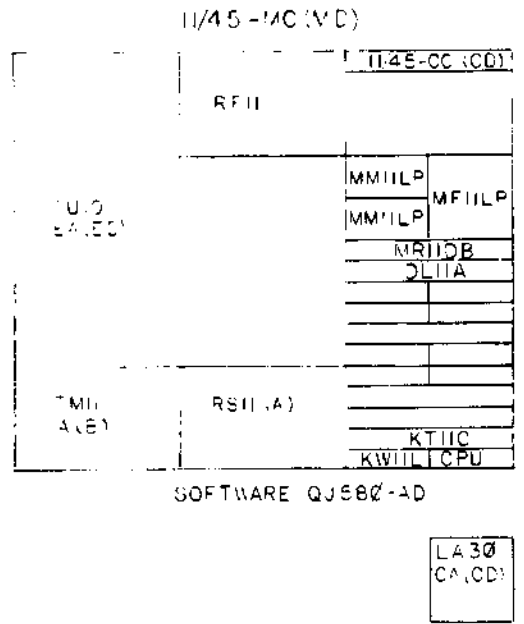
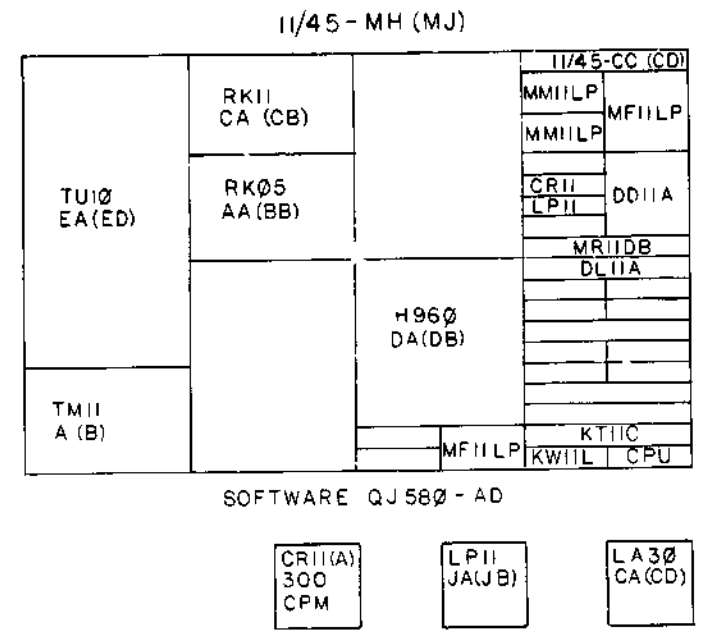
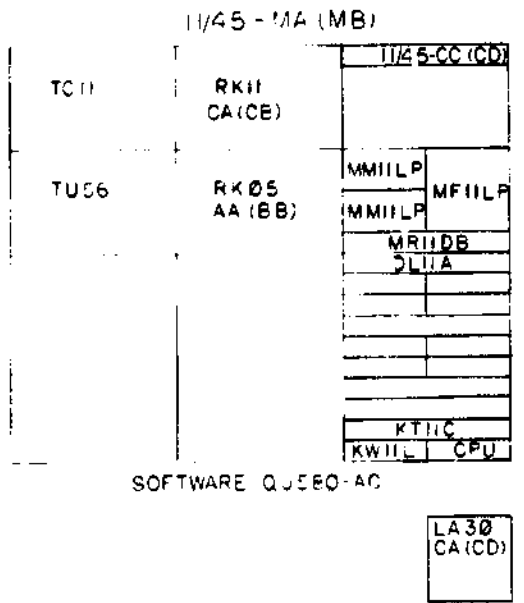


FIRST USED ON OPTION/MODEL	QTY	DESCRIPTION	PART NO	ITEM NO
PDP11/45				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES	DRN 3 CRAMM	DATE 1-6-72		
DECIMALS	CHK'D J BAILEY	DATE 11-10-72		
ANGLES	ENG SWANSON	DATE 11-13-72	TITLE BATCH PROCESSING SYSTEMS	
REMOVE BURRS AND BREAK SHARP CORNERS SURF. QUALITY	PROJ. ENG SWANSON	DATE 11-13-72		
MATERIAL	PROD R. HILGENCORP	DATE	REV DAR 11/45-0-4 C	
FINISH	NEXT HIGHER ASSY			
	B DD-11/45-0		SCALE SHEET 2 OF 3	

REVISIONS
 CHANGE NO
 REV
 CPM

PART NO
 B AR 11/45-0-4
 REV
 C

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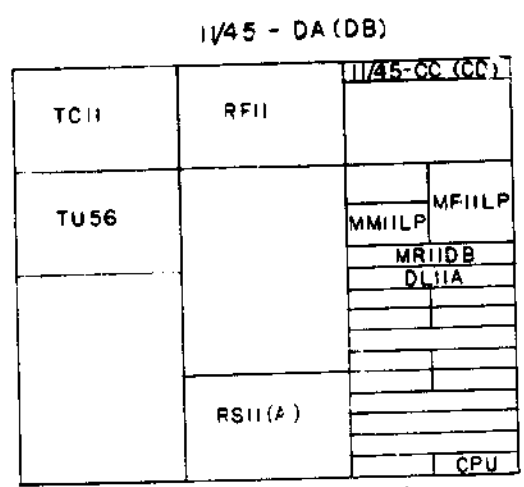


FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	REV.
PDP11/45				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES	ORH B. CRAMM	DATE 11-8-72	DIGITAL EQUIPMENT CORPORATION <small>MEMPHIS, MISSISSIPPI</small>	
DECIMALS	CHK'D. J. BALLEW	DATE 11-10-72		
ANGLES	ENG SWANSON	DATE 11-13-72		
XXX - 000 XX - 02 X - 1	PROJ. ENG. SWANSON	DATE 11-13-72		
REMOVE BURRS AND BREAK SHARP CORNERS. SURFACE QUALITY	PROD. MILGENDORF	DATE	REAL TIME SYSTEMS	
MATERIAL	NEXT HIGHER ASSEMBLY			
FINISH				
	B DD-1145-0	SIZE CODE	NUMBER	REV.
	SCALE	DAR	11/45-0-4	C
	SHEET 3 OF 5	DIST		

DRAWING NO. 11/45-0-4
 REV. C
 DATE 11/13/72
 BY J. SWANSON
 CHECKED BY J. BALLEW

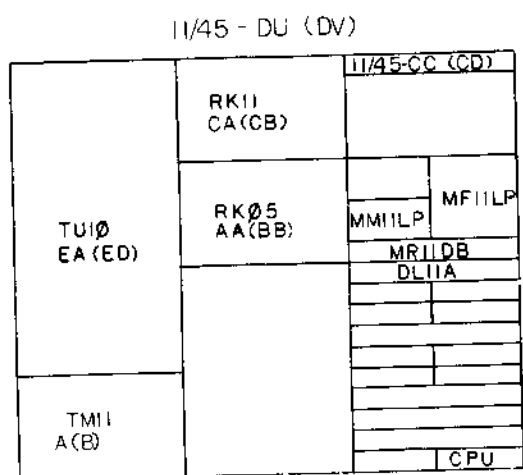
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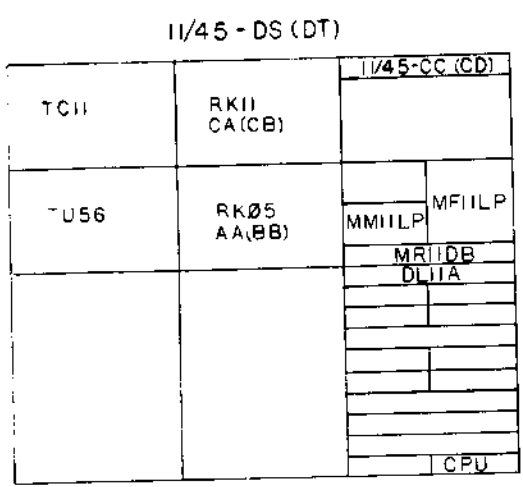
SOFTWARE QJ220-AC

LA 30
CA (CD)



SOFTWARE QJ220-AD

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SOFTWARE QJ220-AC

LA 30
CA (CD)

REVISIONS	REV
CHANGE NO	
CHK	

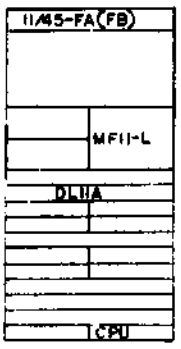
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D
C
B
A
11/45-0-4 C

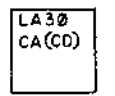
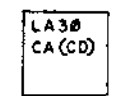
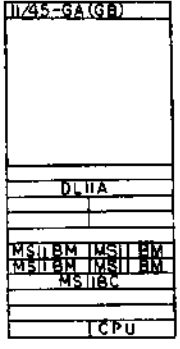
FIRST USED ON OPTION/MODEL PDP 11/45	QTY	DESCRIPTION	PART NO.
PARTS LIST			
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES	DRN BCRAMM	DATE 11-6-72	 DIGITAL EQUIPMENT CORPORATION <small>MEMPHIS, MISSISSIPPI</small>
DECIMALS	CHK'D J. BAILEY	DATE 1-10-73	
ANGLES	ENG. SWANSON	DATE 1-13-72	INTERACTIVE DISK SYSTEMS
XX - 005 XX - 02 X - 1	PROJ. ENG. SWANSON	DATE 11-13-72	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	PROD. HILGENDORF	DATE	
MATERIAL	NEXT HIGHER ASSY.	SIZE CODE	NUMBER
FINISH	B-DD-11/45-0	DAR	11/45-0-4
	SCALE	DIST	REV
	SHEET 4 OF 5		C

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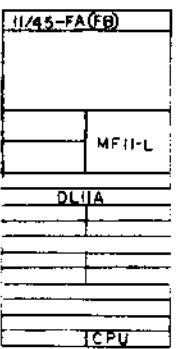
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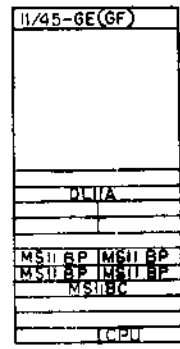
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11/45-FE (FF)



11/45-GP (GR)



DRAWING NO 107 19848
 REV. 1
 DATE 11/22/73
 BY B. C. RAMM
 CHECKED BY [Signature]
 ENG. DATE 11-22-73
 PROJ. ENG. DATE
 PROD. DATE
 MATERIAL
 FINISH

FIRST USED ON OPTION/MODEL	QTY	DESCRIPTION	PART NO.	REV.	
POP-11					
PARTS LIST					
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES	DRN. B. C. RAMM	DATE 11-22-73	EQUIPMENT CORPORATION CORPORATION		
DECIMALS	CHK'D [Signature]	DATE 11-22-73			TITLE OEM SYSTEMS
ANGLES	ENG.	DATE			
XX - .005 XX - .01 X - .1	PROJ. ENG.	DATE			
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	PROD.	DATE			
MATERIAL	NEXT HIGHER ASBY	SIZE CODE	NUMBER	REV.	
---	B-DD-11/45-0	DAR	11/45-0-4	C	
FINISH	SCALE	SHEET	OF	DIST.	
---	1:1	5	OF 5		



