

CB12

IDENTIFICATION

PRODUCT CODE: MAINDEC 12-D1DA-D(D)
PRODUCT NAME: PDP-12 CHECKERBOARD
DATE CREATED: OCTOBER 20, 1969
MAINTAINER: DIAGNOSTIC GROUP
AUTHOR: HAROLD LONG

RSW = 0007 for 2K runs
8MODE
START 20
RSW 4-1 for pass control
REPC OUT. RIM & BIN

COPYRIGHT © 1969
DIGITAL EQUIPMENT CORPORATION

1. ABSTRACT

PDP-12 checkerboard is designed to test the operation of the PDP-12 memory, from 4 to 32K. It accomplishes this by using the L mode instructions LAM (link & AC added to memory, sum in AC and memory), ADM (AC added to memory, sum in AC and memory), and SAE (skip if accumulator is equal to designated memory register). The algorithm used for testing is to first set the memory cell under test to 5252; second, set the AC to 6525, and rotate it into the link one place, resulting in the AC = (1) 5252 (the (1) indicating the link is set); third, a LAM is performed into the test cell and two comparisons made; once for the AC = 2525, and once for memory equal to the AC. Fourth, an ADM is performed; since the AC is now equal to 2525, and memory equal to 2525, the sum will be 5252. Another test of the AC and memory is made. If any of the comparisons fail, an error routine is entered; otherwise, the memory target address is incremented and testing continued.

This test will cycle throughout all available memory, as determined by the right switches.

2. REQUIREMENTS

2.1 Equipment

- a) Any PDP-12 computer, (with or without EXT. memory)
- b) An ASR-33 teletype or equivalent.

2.2 Preliminary Programs

- a) Insure that the binary loader is operating properly.
- b) If this test will not run as indicated, verify processor operation with CP Test 1 (INSTST). No other programs are necessary.

3. LOADING PROCEDURES

3.1 Method

This program must be loaded with the binary loader. If you are unfamiliar with the proper binary loading procedures refer to "Appendix A" of this program, otherwise proceed with the following:

- a) Set the teletype reader switch to FREE.
- b) Open the teletype reader and insert the program tape so that the arrows on the tape are visible to and pointing toward the operator.
- c) Close the reader and set the reader switch to START.
- d) Set the teletype front panel switch to ON LINE
- e) Set the LEFT switches to 7777.
- f) Set the RIGHT switches to 4000.
- g) Set the MODE switch to 8 mode.
- h) Depress I/O preset.
- i) Depress START LS.

- j) When the program tape has been read the ACCUMULATOR must be 0000 if it is not, a read-in error has occurred and one might try reloading the binary loader.
- k) Remove the program tape from the reader.

4. STARTING PROCEDURES

- a) Set the RIGHT Switches SR7 thru 11 to the amount of memory available, in 1K segments, within the range 0 to 37. (In a 4K machine this would be 0003).
- b) Set the MODE switch to 8 mode.
- c) Depress I/O preset.
- d) Depress START 20.
- e) The program, when properly running, will cause the data field lights to appear to be counting up, and the teletype bell to ring at intervals dependent upon the amount of memory being tested.
- f) Attempting to test non-existent memory may result in program destruction or false error printouts.

5. ERROR ROUTINE

5.1 Switch Settings

In general, SR0-3 allows selection of the error mode. With all switches equal to zero, the sequence would be:

(HLT) - OPERATOR SELECTS ANY ADDITIONAL ERROR OPTIONS AND
DEPRESSES THE CONTINUE SWITCH -

(ERROR PRINTOUT) - (NEXT CELL TESTED)

SR0 = 1 SUPPRESS HALT

SR1 = 1 SUPPRESS PRINTOUT

SR2 = 1 SCOPE LOOP ON FAILING CELL

SR3 = 1 LOOP ON SELECTED FIELD

With SR3 = 1, the right switches 07-11 must contain the field you wish to test, within the range 0 to 37. The diagnostic will cycle within this field, stopping only in the event of an error.

SR4 = 1 DUMP PASS COUNTER

Setting this switch to a one causes a type out of the contents of the pass counter. A start 20 will set the counter to 0000.

SR5 = 1 INHIBIT BELL RING AT END OF PASS.

5.2 Error Printout

The error printout has the following general form:

```
LINC  CHKB
FIELD  LOCN  CONT  ACUM
0007  0400  2524  2525
0007  0400  5202  5202
```

The message is interpreted as follows:

- FIELD - The data field being tested, within the range 0 to 37.
- LOCN - The 10 bit address within that field.
- CONT - The contents of that location; this should equal the AC.
- ACUM - The contents of the AC. This should equal either 2525 or 5252.

5.3 Error Analysis

Compare the memory contents against the contents of the AC. In the first example, it is apparent that the AC is correct, indicating proper data acquisition, but that memory is bad, indicating poor write response in memory - this could be either inhibit current, memory timing, or bad cores.

In the second example, both memory and the AC are the same, but the data is bad, indicating poor read response. This could be marginal sense amps, memory timing, or bad cores.

These are examples only, and are not to be taken as a hard & fast rule.

OK

```

0000 *20
0001 /PDP-12 CHECKERBOARD, MAINDEC 12-DIDA-L
0002 /COPYRIGHT DIGITAL EQUIPMENT CORP., MAYNARD, MASS.
0003 /
0004 /AUTHOR: HAROLD LONG
0005 /
0006 /THIS TEST IS DESIGNED TO CHECK THE OPERATION
0007 /OF THE PDP-12 INSTRUCTION "LAM".
0008 /IT MAKES USE OF THE "READ -MODIFY -WRITE
0009 /MEMORY CYCLE TO TEST MEMORY RELIABILITY.
0010 /THE PROGRAM OCCUPIES CELLS 0000 TO 0300
0011 /IN BANK 0. IT WILL CHECK ALL OTHER MEMORY
0012 /AVAILABLE.
0013 /THE ALGORITHM USED FOR TESTING IS TO SET
0014 /THE MEMORY CELL UNDER TEST TO 5252, AND THE AC TO 6525.
0015 /THE AC IS ROTATED INTO THE LINC ONE PLACE
0016 /AND A LAM TO THE TEST CELL EXECUTED
0017 /A SAE IS EXECUTED FOR TESTING PURPOSES AND THE
0018 /ROUTINE CONTINUED
0019 /AN ERROR WILL CAUSE ENTRY INTO THE
0020 /ERROR ROUTINE
0021 /
0022 /
0023 /
0024 /
0025 /
0026 /
0027 /
0028 /
0029 /
0030 /
0031 /SWITCH SETTINGS:
0032 /
0033 /RSW 00=1, INHIBIT ERROR HALT
0034 /RSW 01=1, INHIBIT ERROR TYPEOUT
0035 /RSW 02=1, SCOPE LOOP ON FAILING CELL
0036 /RSW 03=1, SCOPE LOOP ON SELECTED BANK
0037 /RSW 04=1, DUMP PASS COUNTER
0038 /RSW 05=1, INHIBIT BELL
0039 /
0040 /RSW 07 TO 11 ARE SET TO THE HIGHEST MEMORY BANK AVAILABLE,
0041 /WITHIN THE RANGE 0 TO 37. IN A 4K MACHINE, THIS WOULD BE
0042 /0003; WITH RSW 03=1, THE DESIRED BANK MUST BE IN THE SWITCHES.
0043 /
0044 /
0045 /
0046 /I/O PRESET TO 8 MODE, START 20
0047 /
0048 /
0049 /
0050 /

```

EJECT

0051 / TAGS AND CONSTANTS

0052 /
 0053 /
 0054 /
 0055 PMODE
 *0001

0056	2000	AUTO1,	0000
0057	2351	AUTO2,	2351
0058	0000	RSWB,	0000
0059	0000	AUTO4,	0000
0060	0000	AUTO5,	0000
0061	0000	ERROR1,	0000
0062	0000	MESSA,	K215-1
0063	0000	AUTO10,	0000
0064	0000	TEMP,	0000
0065	1026	K1026,	1026
0066	7774	K7774,	7774
0067	0000	REGB,	0000
0068	0003	MASK,	0003
0069	0240	K0240,	0240
0070	0000	BANK,	0000

0071 /
 0072 /
 0073 /
 0074 /
 0075 EJECT
 0076


```

0077  /
0100  / PMODE
0101  /
0102  / MAJOR START & MODE
0103  /
0104  / *0020
0105  / START,
0106  / LMODE
0220  5141
0221  0020
0222  0021
0223  0022
0224  0023
0225  0024
0226  0025
0227  0026
0228  0027
0229  0028
0230  0029
0231  0030
0232  0031
0233  0032
0234  0033
0235  0034
0236  0035
0237  0036
0238  0037
0239  0038
0240  0039
0241  0040
0242  0041
0243  0042
0244  0043
0245  0044
0246  0045
0247  0046
0248  0047
0249  0048
0250  0049
0251  0050
0252  0051
0253  0052
0254  0053
0255  0054
0256  0055

SET I    AUTO4
0000     0000
SET I    AUTO0
0000     0000
JMP      6112
RSW      0516
BCL I    1560
7740     0030
STC      4003
RSW      0516
BCL I    1560
7377     0034
AZE      0450
JMP      6133
SET      0041
AUT02    0002
RSW      0516
BCL I    1560
7577     0043
AZE      0450
JMP      6333
LDA I    1020
LOF      0640
ADD      2017
STA      1040
CHANGE   0200
STA      1040
CHANG2   0207
STC      4056

```

```

/GO TO LINC MODE
/RESET PASS COUNTER
/RESET PASS MULTIPLIER
/START WITH BANK 0
/READ THE SWITCHES
/SAVE BITS 07-11
/SAVE FOR ITERATION
/FIXED FIELD?
/CHECK FOR SWITCH 03
/WAS IT THERE?
/SET FOR FIXED FIELD
/SET LOWER LIMIT
/READ THE SWITCHES
/SAVE SW 04
/IS IT SET?
/YES, TYPE PASS COUNTER
/PICK UP LDF
/ADD NEW BANK NUMBER
/RESET INTERNAL LDF
/STORE FOR EXECUTION

```

GC,

EJECT

0144

0145
0146
0147
0150
0151
0152
0153
0154
0155
0156
0157
0160
0161
0162
0163
0164
0165
0166
0167
0170
0171
0172
0173
0174
0175
0176
0177
0200
0201
0202
0203
0204

/TEST MEMORY WITH LAM, ADM, AND SAE
/SETB, 0000
BACK, CLR
LDA I
6525
ROL I
1
STA AUT01
LAM AUT01
SAE I
2525
JMP ERROR
SAE AUT01
JMP ERROR
ADM AUT01
SAE I
5252
JMP ERROR
SAE AUT01
JMP ERROR
XSK I AUT01
JMP BACK
XSK I BANK
LDA BANK
COM
COM RSWB
ADD K0001
ADD
APO I
JMP GO
EJECT

0056 0000 /EXECUTE LDF
0057 0011 /CLEAR LINK
0262 1020 /PICK UP CONSTANT
0761 6525 /SET LINK, JUSTIFY
0262 0261 /INDIRECT TO DF
0263 1041 /ADD (AC&N&LINK)=2525
0264 1221 /AC OK?
0265 1460 /NO, GO TYPE MESSAGE
0066 2525 /MEMORY OK?
0067 6151 /NO, GO TYPE MESSAGE
0070 1441 /ADD THEM TOGETHER AGAIN
0071 6151 /TEST
0072 1141 /TEST MEMORY
0073 1460 /INCREMENT TARGET
0074 5252 /TRY ANOTHER CELL
0075 6151 /WILL NEVER SKIP
0076 1441 /PICK UP BANK
0077 6151 /COMPLEMENT
0100 0221 /COMPARE WITH RSW
0101 0057 /COMPARE WITH RSW
0102 0237 /LAST BANK?
0103 1000 /NEW BANK
0104 0017
0105 0017
0106 2003
0107 2252
0110 0471
0111 6046

0205	0112	0077	RESET,	SET I	BANK	/RESE, BANK
0206	0113	0000		0000	AUT05	/TO ZEROS
0207	0114	0225		XSK I		/PASS MULTIPLIER
0210	0115	0016		NOP		/NO SKIP WANTED
0211	0116	0516		RSW		/READ THE SWITCHES
0212	0117	1560		BCL I		/SAVE SW 05
0213	0120	7577		7677		
0214	0121	0450		AZE		/IS IT SET?
0215	0122	0726		JMP	START+6	/YES, INHIBIT BELL
0216	0123	1020		LDA I		/PICK UP CONSTANT
0217	0124	0207		0207		/BELL CODE
0220	0125	0500		IOB		
0221			PMODE	TLS		/RING IT
0222	0126	6046				
0223			LMODE	IOB		
0224	0127	2522				
0225			PMODE	TSF		/WAIT
0226	0130	5041				
0227			LMODE	JMP		
0230	0131	6127		JMP	-2	/NEXT PASS
0231	0132	6326		JMP	START+6	
0232			EJECT			

0233	0133	0516	FILED1,	RSW	/READ SWITCHES AGAIN
0234	0134	1560		BCL I	
0235	0135	7740		7740	/IN FIELD 0?
0236	0136	0261		SET I	/TRY FOR WHOLE FIELD
0237	0137	3777		3777	
0240	0140	2470		AZE I	/NOW SEE IF FIELD 0
0241	0141	6146		JMP	/IT WAS
0242	0142	1040		STA	
0243	0143	0017		BANK	/SET UP LIMIT
0244	0144	4003		STC	/INTC BUFFER
0245	0145	5246		JMP	/BACK TO MAINLINE
0246	0146	2041	SET2,	SET	/FIELD 0
0247	0147	0022		AUTO2	
0250	0150	6000		JMP	/BACK TO FIELD
0251				EJECT	

0252	0151	4006	ERROR,	STC	ERROR1	/SAVE AC
0253	0152	0516		RSW		/READ THE SWITCHES
0254	0153	0471		AP0 I		/SWITCH 0 SET?
0255	0154	0000		HLT		/NO, STOP
0256	0155	0241		ROL	1	/ROTATE
0257	0156	0471		AP0 I	TYPE	/SWITCH 1 SET?
0260	0157	6164		JMP	1	/NO, TYPE ERROR
0261	0160	0241	CHECK,	ROL		/ROTATE
0262	0161	0471		AP0 I		/SWITCH 2 SET?
0263	0162	6100		JMP	INCRN	/NO, TRY NEW CELL
0264	0163	6057		JMP	BACK	/YES, USE SAME CELL
0265	0164	1000	TYPE,	LDA		/CHECK FOR HEADING
0266	0165	0010		AUTO10		/MESSAGE POINTER
0267	0166	0470		AZE I		/WAS IT TYPED?
0270	0167	6213		JMP	HEAD	/NO, GO TYPE IT
0271	0170	1000		LDA		/GET BANK UNDER TEST
0272	0171	0017		BANK		
0273	0172	6226		JMP	OUTYP	/TYPE IT
0274	0173	1000		LDA		
0275	0174	0001		AUTO1		
0276	0175	1560		BCL I		
0277	0176	6000		6000		
0300	0177	6226		JMP	OUTYP	/10 BIT ADDR
0301	0200	0000	CHANGE,	0000		/TYPE CELL LOCATION
0302	0201	1001		LDA	AUTO1	/CHANGE DATA FIELD
0303	0202	6226		JMP	OUTYP	/GET CELL CONTENTS
0304	0203	1000		LDA		/TYPE IT
0305	0204	0006		ERROR1		
0306	0205	6226		JMP	OUTYP	/TYPE CONTENTS OF AC
0307	0206	6316		JMP	CRLF	/RETURN AND LINEFEED
0310	0207	0000	CHANG2,	0000		/CHANGE DATA FIELD
0311	0210	0516		ROL		/READ SWITCHES AGAIN
0312	0211	0241		RSW	1	/JUSTIFY
0313	0212	6160		JMP	CHECK	/CHECK WITH MONITOR
0314			/			
0315			EJECT			

```

0316 /TYPEOUT ROUTINES
0317 /
0320 HEAD, PDP
0321 PMODE
0322
0323 CDF 00
0324 TAD MESSA
0325 DCA AUTO10
0326 TAD I AUTO10
0327 SNA
0330 JMP .+3
0331 JMS PRINT
0332 JMP .-4
0333 LINC
0334 LMODE
0335 JMP 0
0336 OUTYP, PDP
0337 PMODE
0340 DCA TEMP
0341 TAD K7774
0342 DCA REGB
0343 TAD K1026
0344 DCA AUTO10
0345 TAD TEMP
0346 RAL TEMP
0347 DCA AUTO10
0350 RAL
0351 TAD 7004
0352 SNL 7420
0353 JMP 5233
0354 JMS 4325
0355 ISZ 2014
0356 JMP 5232
0357 TAD 1016
0360 JMS K0240
0361 LINC PRINT
0362 LMODE
0363 JMP 0
0364 PMODE
0365 K0001, 0001
0366 EJECT

```

```

/GO TO PMODE
/SAVE DATA
/SET REGISTER
/TO -4
/GET CONSTANT
/SAVE
/GET DATA
/ROTATE
/SAVE IT
/GET CONSTANT
/ROTATE
/OK TO PRINT?
/NO, ROTATE SOME MORE
/YES, TYPE IT
/DONE?
/NO
/PICK UP SPACE CODE
/TYPE IT
/BACK TO LMODE
/RETURN
/SAVE DATA
/SET REGISTER
/TO -4
/GET CONSTANT
/SAVE
/GET DATA
/ROTATE
/SAVE IT
/GET CONSTANT
/ROTATE
/OK TO PRINT?
/NO, ROTATE SOME MORE
/YES, TYPE IT
/DONE?
/NO
/PICK UP SPACE CODE
/TYPE IT
/BACK TO LMODE
/RETURN

```

/MESSAGE TABLE

0367
0370
0371
0372
0373
0374
0375
0376
0377
0400
0401
0402
0403
0404
0405
0406
0407
0410
0411
0412
0413
0414
0415
0416
0417
0420
0421
0422
0423
0424
0425
0426
0427
0430
0431
0432
0433
0434
0435

0253 0215
0254 0212
0255 0314
0256 0311
0257 0316
0260 0303
0261 0240
0262 0303
0263 0310
0264 0313
0265 0302
0266 0215
0267 0212
0270 0302
0271 0301
0272 0316
0273 0313
0274 0240
0275 0314
0276 0317
0277 0303
0300 0316
0301 0240
0302 0303
0303 0317
0304 0316
0305 0324
0306 0240
0307 0301
0310 0303
0311 0325
0312 0315
0313 0215
0314 0212
0315 0000

/LINC CHKB
/BANK LOCN CONT ACUM

/END

EJECT

```

0436 /TYPE CARRIAGE RETURN AND LINE FEED
0437 /
0440 LMODE
0441 CRLF, /BACK TO PMODE
0442 PMODE
0443
0444 TAD K215
0445 JMS PRINT
0446 TAD K212
0447 JMS PRINT
0450 LINC
0451 JMP 0 /RETURN
0452
0453 /SINGLE CHARACTER PRINT ROUTINE
0454 /
0455 PMODE
0456 PRINT, 0000 /PRINT CHARACTER
0457 TLS /WAIT
0460 TSF
0461 JMP --1
0462 CLA CLL
0463 JMP I PRINT
0464
0465 /PASS COUNTER DUMP ROUTINE
0466 /
0467 LMODE
0470 DUMP, SET AUTO4 /SAVE RETURN
0471 0 /GET PASS COUNTER
0472 LDA AUT05
0473 JMP OUTYP /GO TYPE PASS COUNTER
0474 JMP CRLF /GO CR-LF
0475 SET I AUTO10 /CLEAR AUTO10 TO ALLOW HEADER TYPEOUT
0476 0000
0477 JMP AUTO4 /RETURN TO MAINLINE
0500
0501 /LINCK80 MODEL C
0502 /
0503
0504

```


0000 ERRORS

AUTO1 0001
AUTO10 0010
AUTO2 0002
AUTO4 0004
AUTO5 0005
BACK 0057
BANK 0017
CHANGE 0200
CHANGE2 0207
CHECK 0160
CRLF 0316
DUMP 0333
ERROR 0151
ERROR1 0006
FILD1 0133
GO 0046
HEAD 0213
HERE 0232
INCRN 0100
K0001 0252
K0240 0016

K1026 0012
K212 0254
K215 0253
K7774 0013
MASK 0015
MESSA 0007
OUTYP 0226
PRINT 0325
REDU 0233
REDB 0014
COPDC 0012
GRVA 0992
ADRA 9940
SETZ 0146
START 0020
TEMP 0011
TYPE 0164

