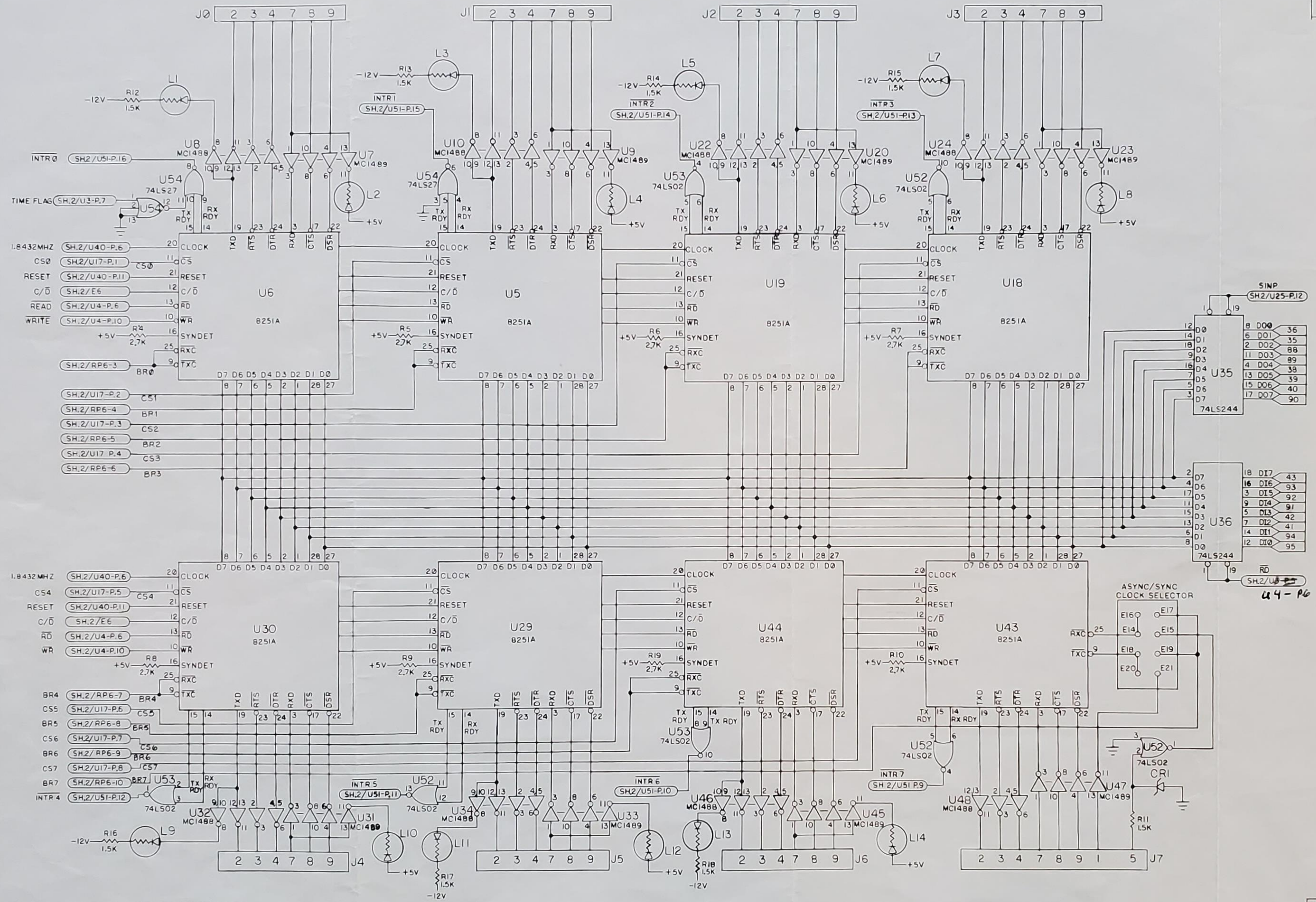


REV	DESCRIPTION	DATE
A	PRODUCTION RELEASE	12-16-81



- UNLESS OTHERWISE NOTED:
1. ALL RESISTORS ARE IN OHMS.
 2. ALL CAPACITORS ARE IN MICROFARADS.
 3. : SINGLE IN LINE RESISTOR PULLED UP TO +5V.
 4. : LED WITH BUILT-IN RESISTOR.

SINP		
12	8 D00	36
14	6 D01	35
18	2 D02	88
9	11 D03	89
16	4 D04	38
7	13 D05	39
5	15 D06	40
3	17 D07	90

U36		
18	D17	43
16	D16	93
17	D15	92
9	D14	91
15	D13	42
7	D12	41
14	D11	94
12	D10	95

© 1981 SSM MICROCOMPUTER PRODUCTS, INC. / ALL RIGHTS RESERVED

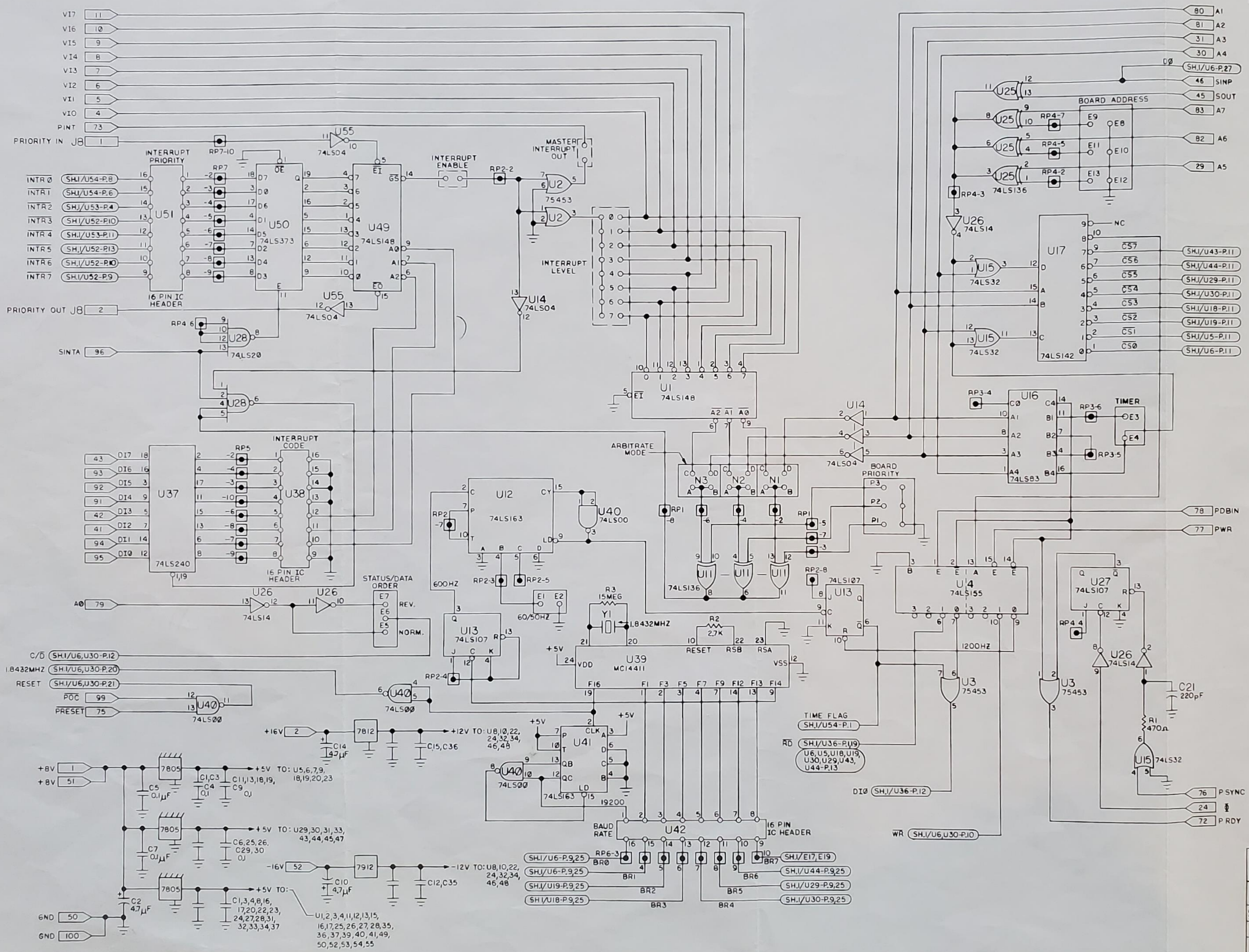
TITLE 108 SERIAL INTERFACE

SCALE: NONE SHEET: 1 OF 2 DATE: 1-15-82

DRAWN BY: B. BASTIAN APPROVED BY: *M. J. Night*

DWG. # 1027-003

REV	DESCRIPTION	DATE
A		



© 1981 SSM MICROCOMPUTER PRODUCTS, INC./ ALL RIGHTS RESERVED

IO8 SERIAL INTERFACE

SCALE: NONE SHEET: 2 OF 2 DATE: 1-15-82

DRAWN BY: B. BASTIAN APPROVED BY: *[Signature]*

DWG. #: 1027-003