

FSU RAM-S100 MEMORY CARD  
FUNCTIONAL DESCRIPTION  
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The FSU RAM-S100 memory board will provide up to 16K bytes of static random access memory for a microprocessor system designed around the popular S-100 buss.

This card requires eight FSU RAM modules. Any one of eight different FSU RAM part numbers may be used as long as all eight modules are of the same part number. This allows the user to configure the card for a capacity of 4, 8, or 16K bytes of either the normal speed FSU or the slower 520 sort FSU. A user option may be jumpered to provide one processor wait state cycle if the slow FSU is used or if the faster 4 MHZ Z-80 microprocessor is used.

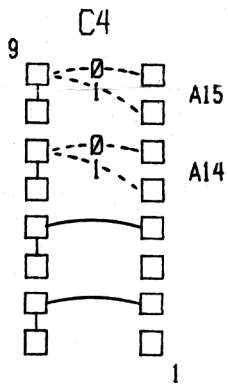
Another user option may be jumpered to provide the proper memory write strobe depending on whether or not a front panel deposit function is to be used.

The high order address bits are jumpered to establish which block (page) of memory space the card is to respond. For example, the two high order address bits (A14 and A15) are used if cards are configured for 16K byte capacity, thus allowing up to four such cards to exist in the system.

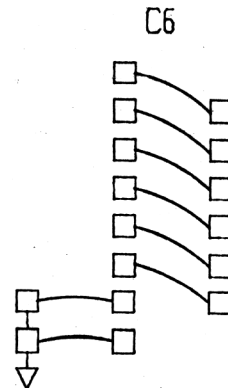
All voltage requirements are provided by on board regulators which draw their power from the S-100 buss. All input lines to the memory card are buffered so as to present no more than one normal TTL load to the S-100 buss.

FSU RAM S-100

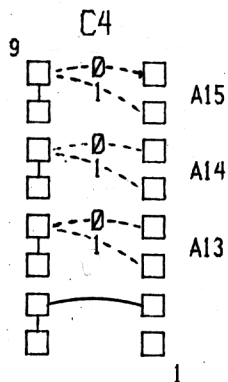
ALL GOOD 2K X 9



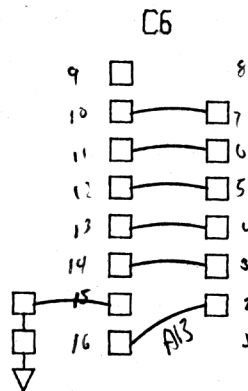
16K



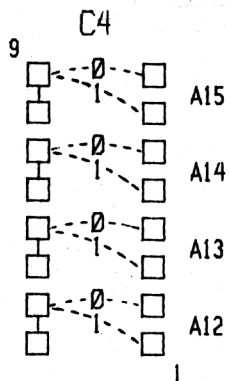
ALL GOOD 1K X 9  
OR  
1/2 GOOD 2K X 9



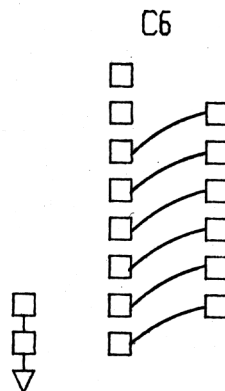
8K



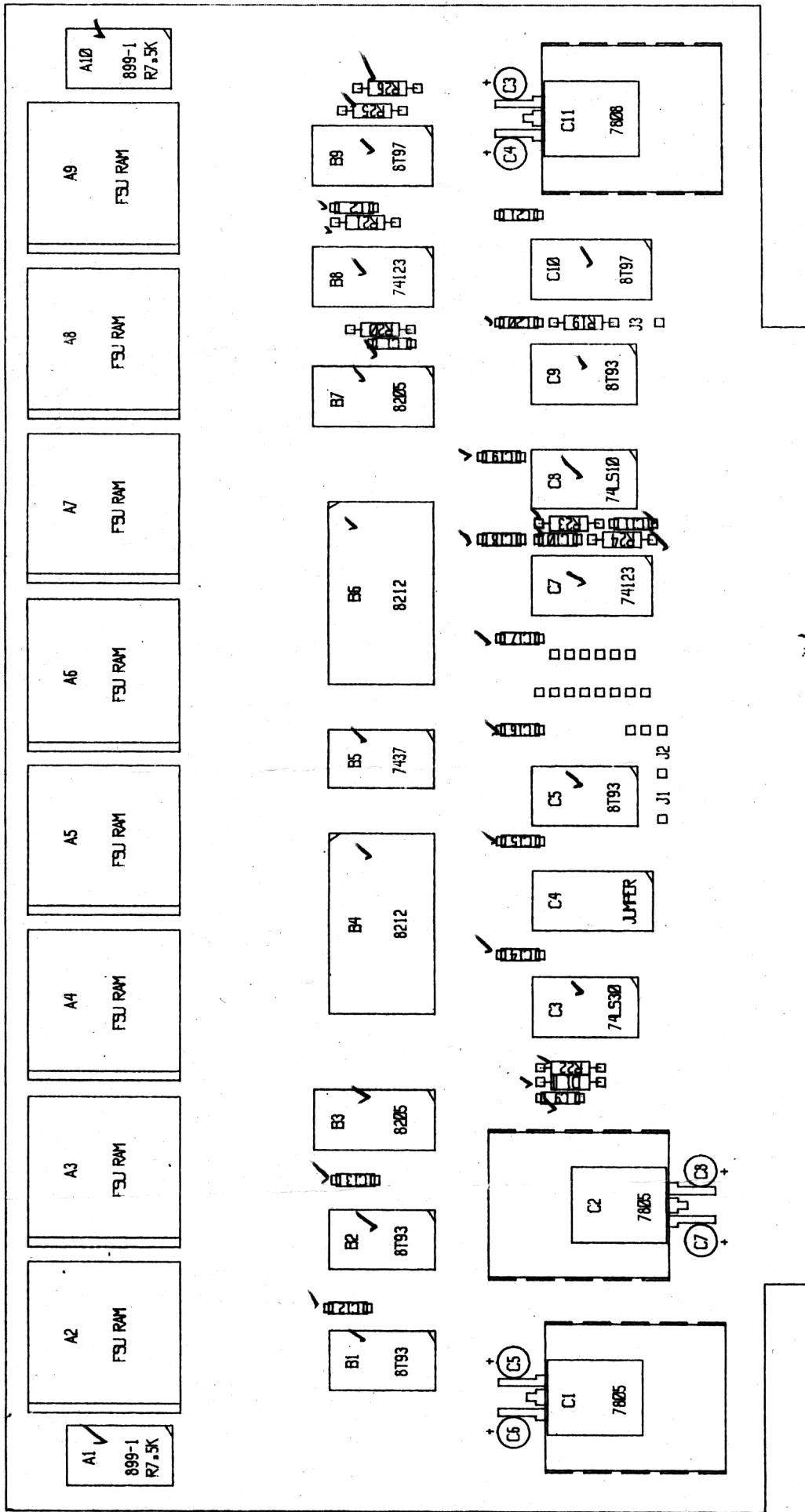
1/2 GOOD 1K X 9  
OR  
1/4 GOOD 2K X 9



4K



FSU RAM S-100 REV 1



COMPONENT LOCATION CHART