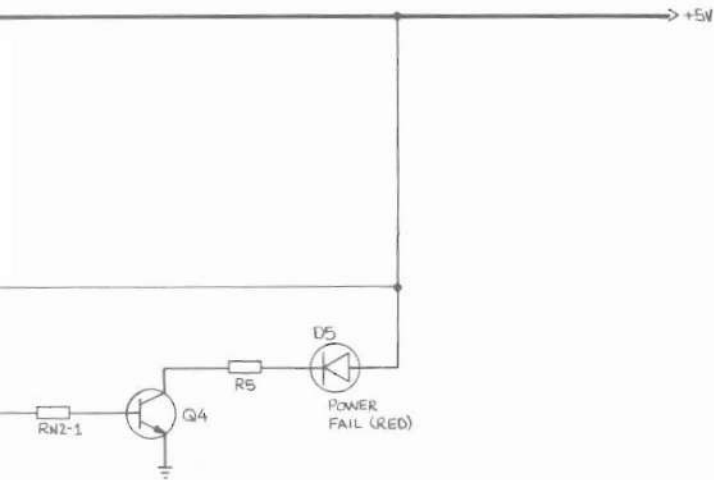
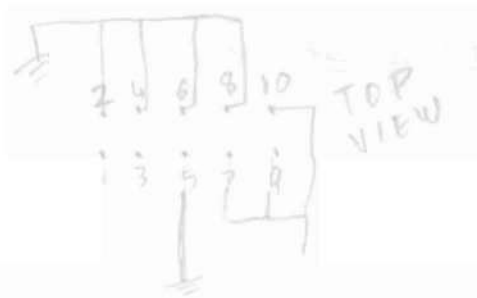
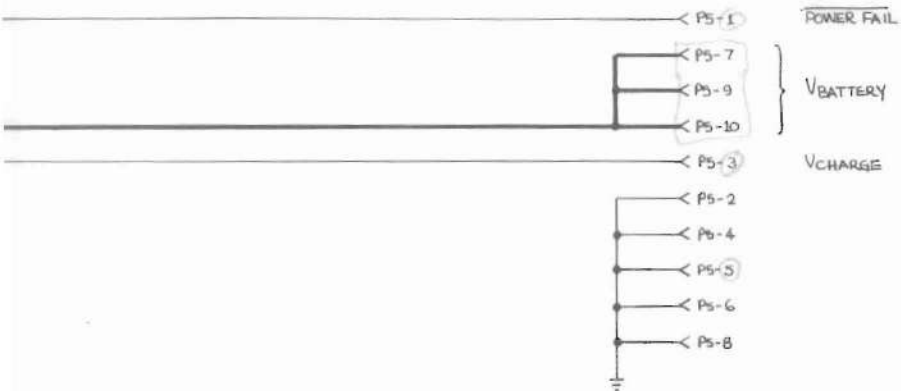


NOTE 1: SOME SYSTEMS (eg. CROMEMCO, NORTHSTAR) USE PIN 53 AS DATA DISABLE FOR THESE SYSTEMS, CUT TRACE FROM PIN 53.



→ LATCHED PWR FAIL
2/AD
3/3H

→ POWER FAIL
1/GF



DO NOT SCALE TO DRAWING

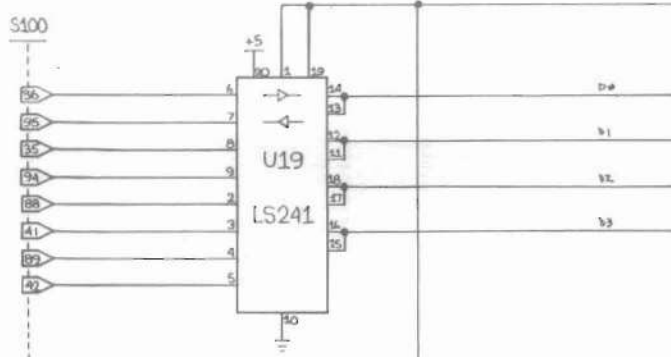
REV	ZONE	DESCRIPTION	DATE	APP
		QUASI-DISK	DE-900006-	
MODEL		DWG		
DESC		SEMICONDUCTOR DISK		REV-CO
SCALE	NONE	SHT 5 OF 5	DWN BPZ	
CKD	APP RPH	DATE 11/07/83		



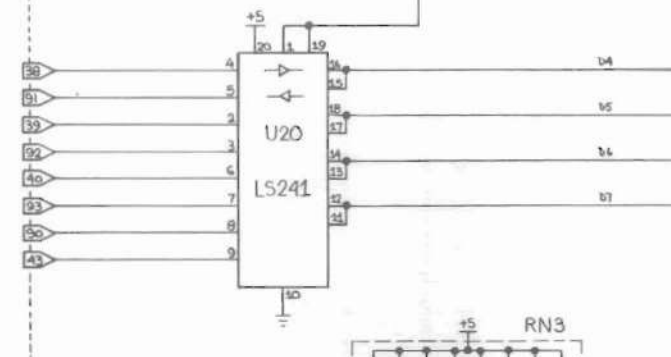
F E D C B A

L | K | J | H | G | F

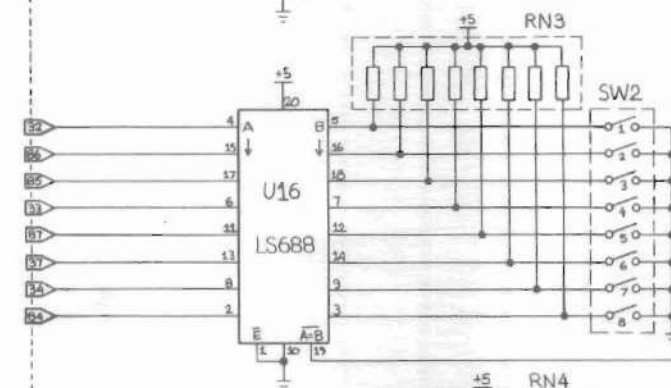
DO0
DI0
DO1
DI1
DO2
DI2
DO3
DI3



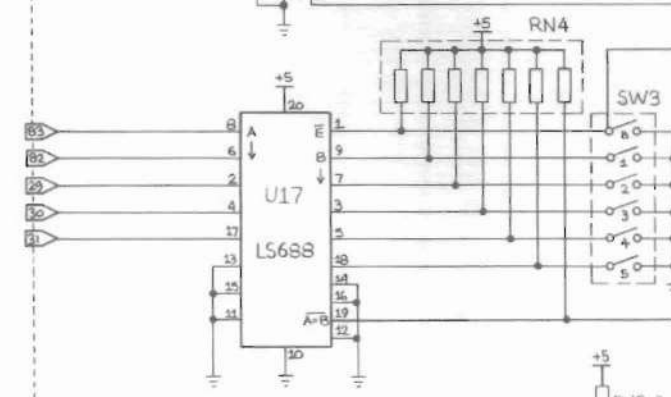
DO4
DI4
DO5
DI5
DO6
DI6
DO7
DI7



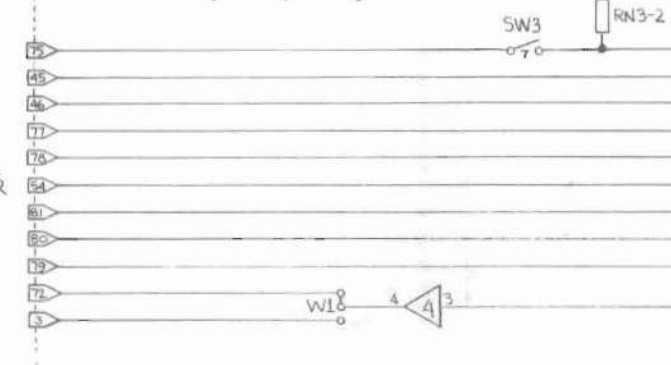
A15
A14
A13
A12
A11
A10
A9
A8



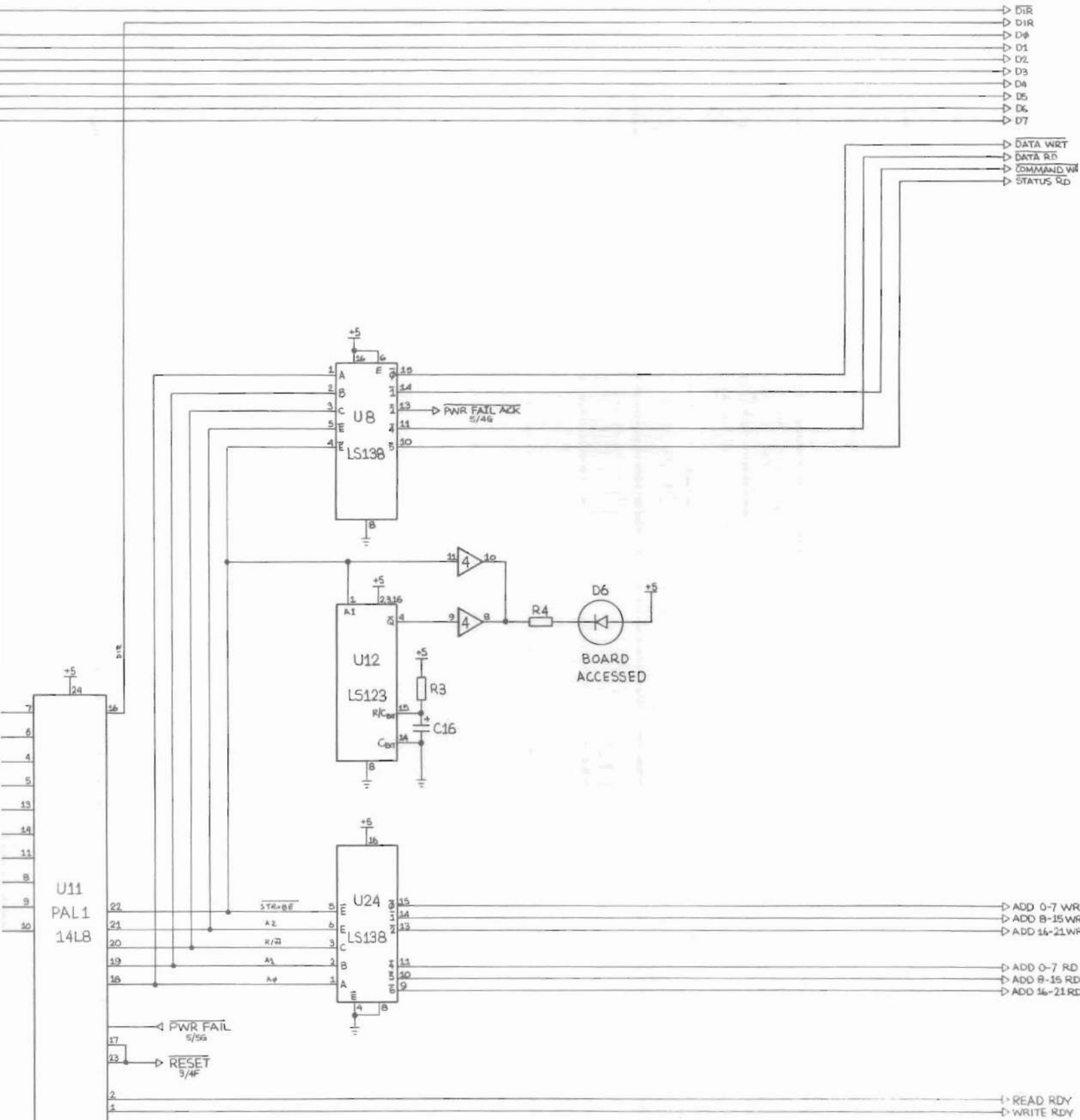
A7
A6
A5
A4
A3



RESET
SOUT
SINP
PWR
PDBIN
SLAVE CLR
A2
A1
A0
RDY
XRDY

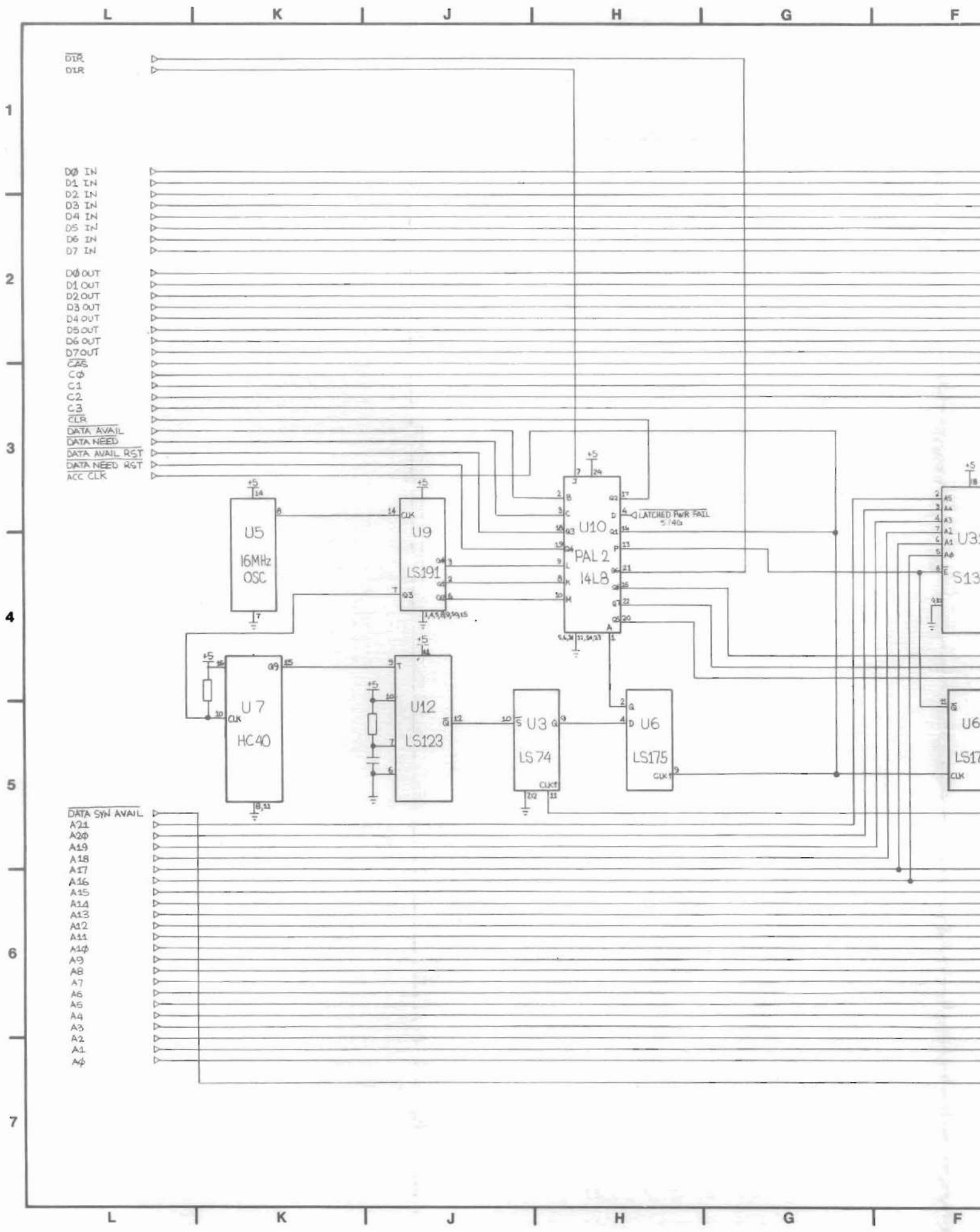


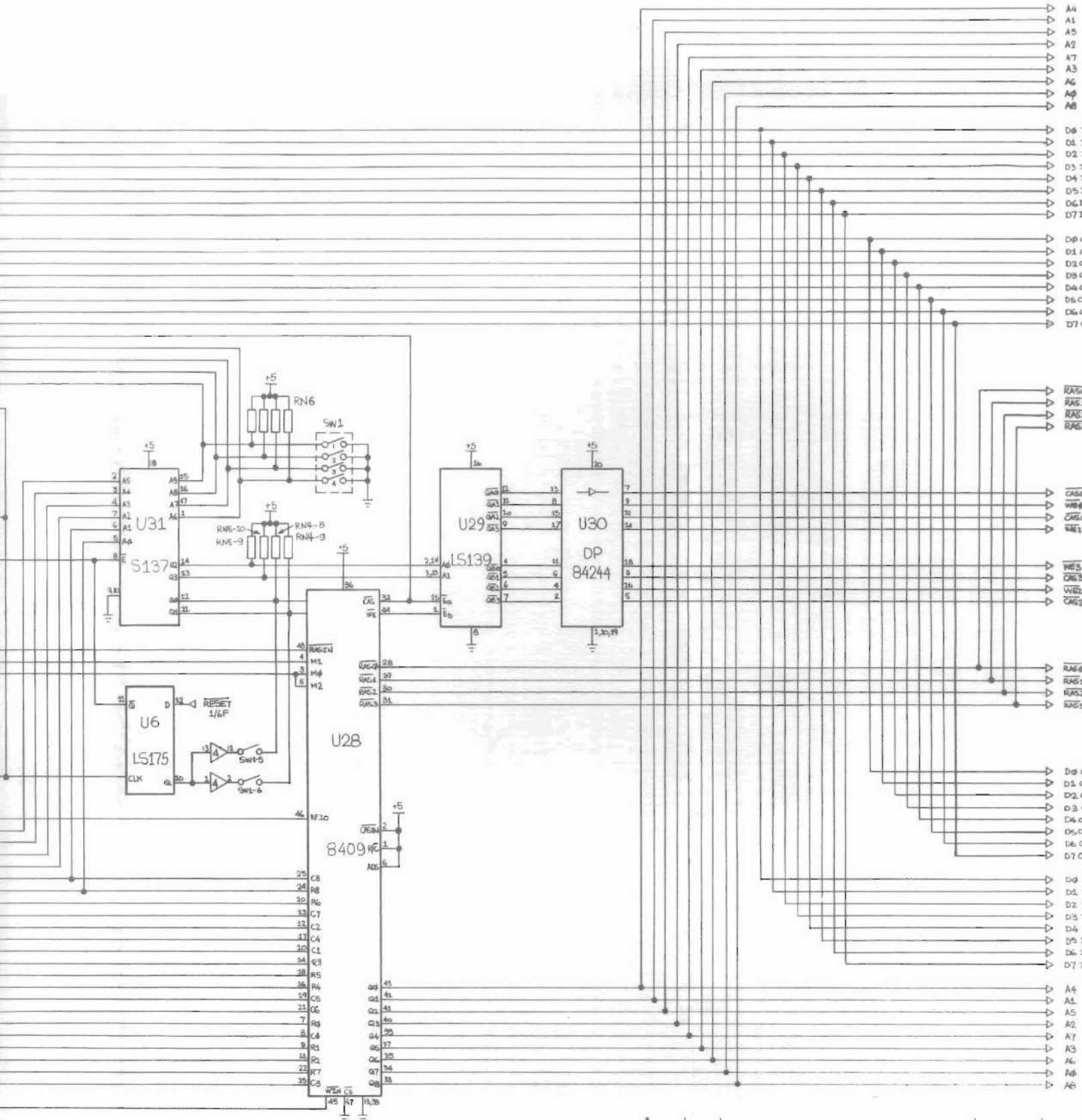
L | K | J | H | G | F



REV	ZONE	DESCRIPTION	DATE	APPROVED
MODEL	QUASI-DISK		DWG DE-900006-00	
DESC	SEMICONDUCTOR DISK		REV-C00	
SCALE	NONE	SHT 1 OF 5	DWN BPZ	
CRD	APP RPH	DATE 11/07/83		

DO NOT SCALE TO DRAWING





DO NOT SCALE TO DRAWING

REV	ZONE	DESCRIPTION	DATE	APPRO
MODEL		QUASI-DISK	DE-900006-0	
DESC		SEMICONDUCTOR DISK	REV-C00	
SCALE	NONE	SHT 3 OF 5	DWN BPZ	
CKD		APP RPH	DATE 11/07/83	



L | K | J | H | G | F

1

A4
A1
A5
A2
A7
A3
A6
A4
A8

2

D0 IN
D1 IN
D2 IN
D3 IN
D4 IN
D5 IN
D6 IN
D7 IN

D0 OUT
D1 OUT
D2 OUT
D3 OUT
D4 OUT
D5 OUT
D6 OUT
D7 OUT

3

RAS0
RAS1
RAS2
RAS3

CAS0
WE0
CAS1
WE1

4

WE2
CAS3
WE2
CAS2

RAS0
RAS1
RAS2
RAS3

5

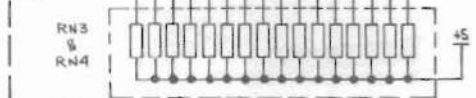
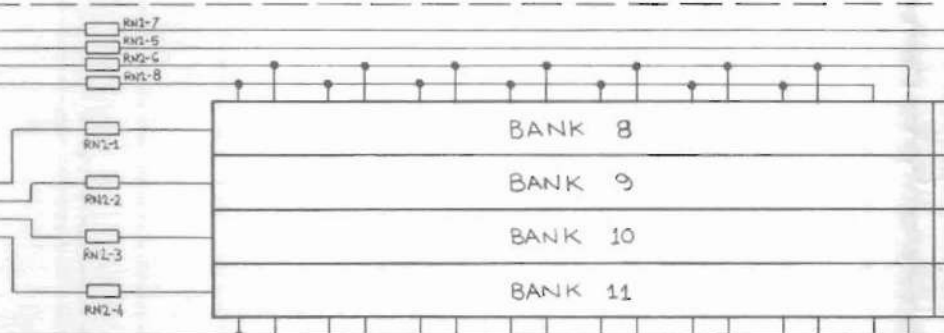
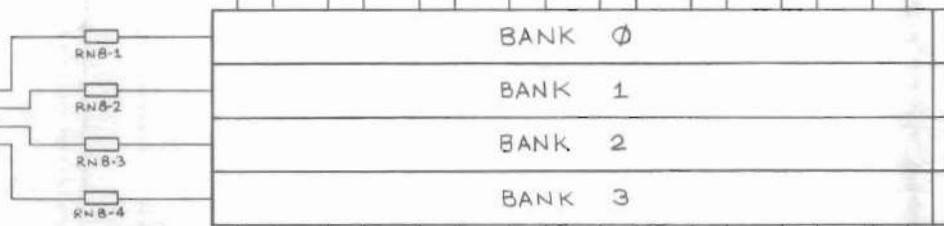
D0 OUT
D1 OUT
D2 OUT
D3 OUT
D4 OUT
D5 OUT
D6 OUT
D7 OUT

6

D0 IN
D1 IN
D2 IN
D3 IN
D4 IN
D5 IN
D6 IN
D7 IN

A4
A1
A5
A2
A7
A3
A6
A4
A8

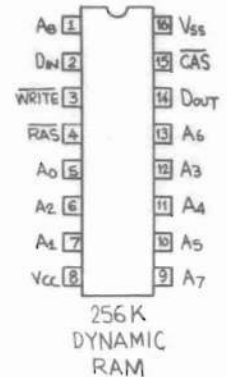
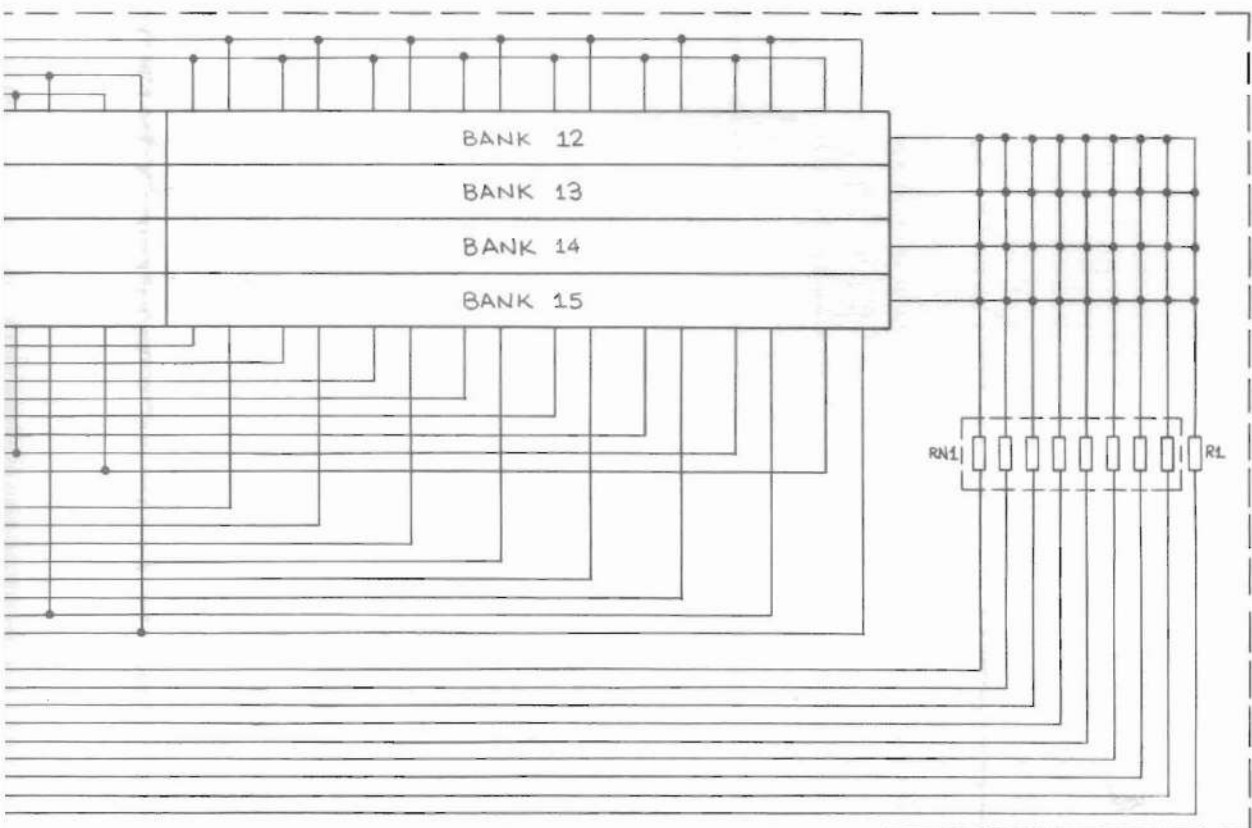
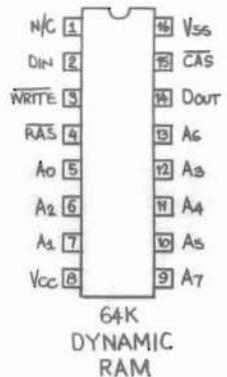
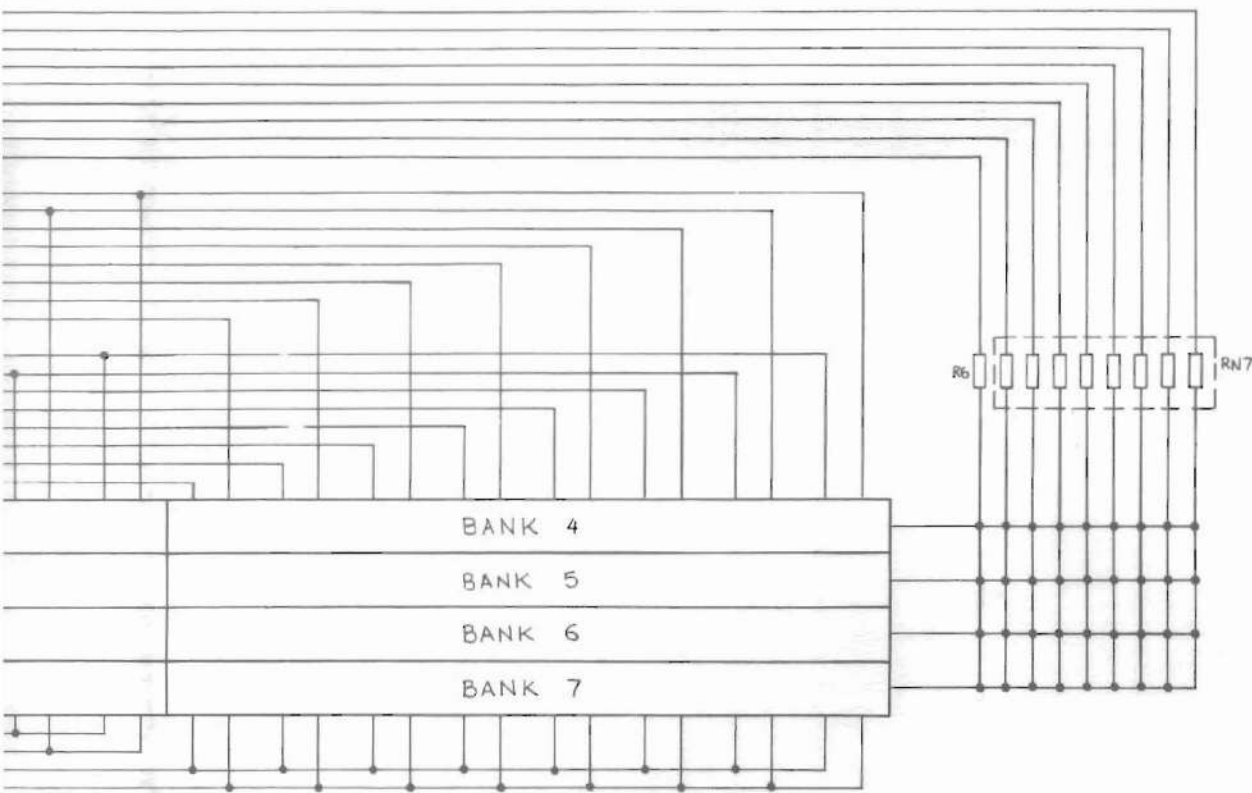
7



NOTE: P1-1, P2-1, P3-1 = +5V
P1-10, P2-10, P3-6 = GND.

Expansion Module

L | K | J | H | G | F



1 Module

DO NOT SCALE TO DRAWING

REV	ZONE	DESCRIPTION	DATE	APPROVED
		QUASI-DISK		
MODEL		SEMICONDUCTOR DISK	DWG DE-900006-00	REV-C00
SCALE	NONE	SHT 4 OF 5	DWN BPZ	
CKD		APP RPH	DATE 11/07/83	



L | K | J | H | G | F

DIR
D1R
D0
D1
D2
D3
D4
D5
D6
D7

DATA WRT
DATA RD
COMMAND WRT
STATUS RD

1

2

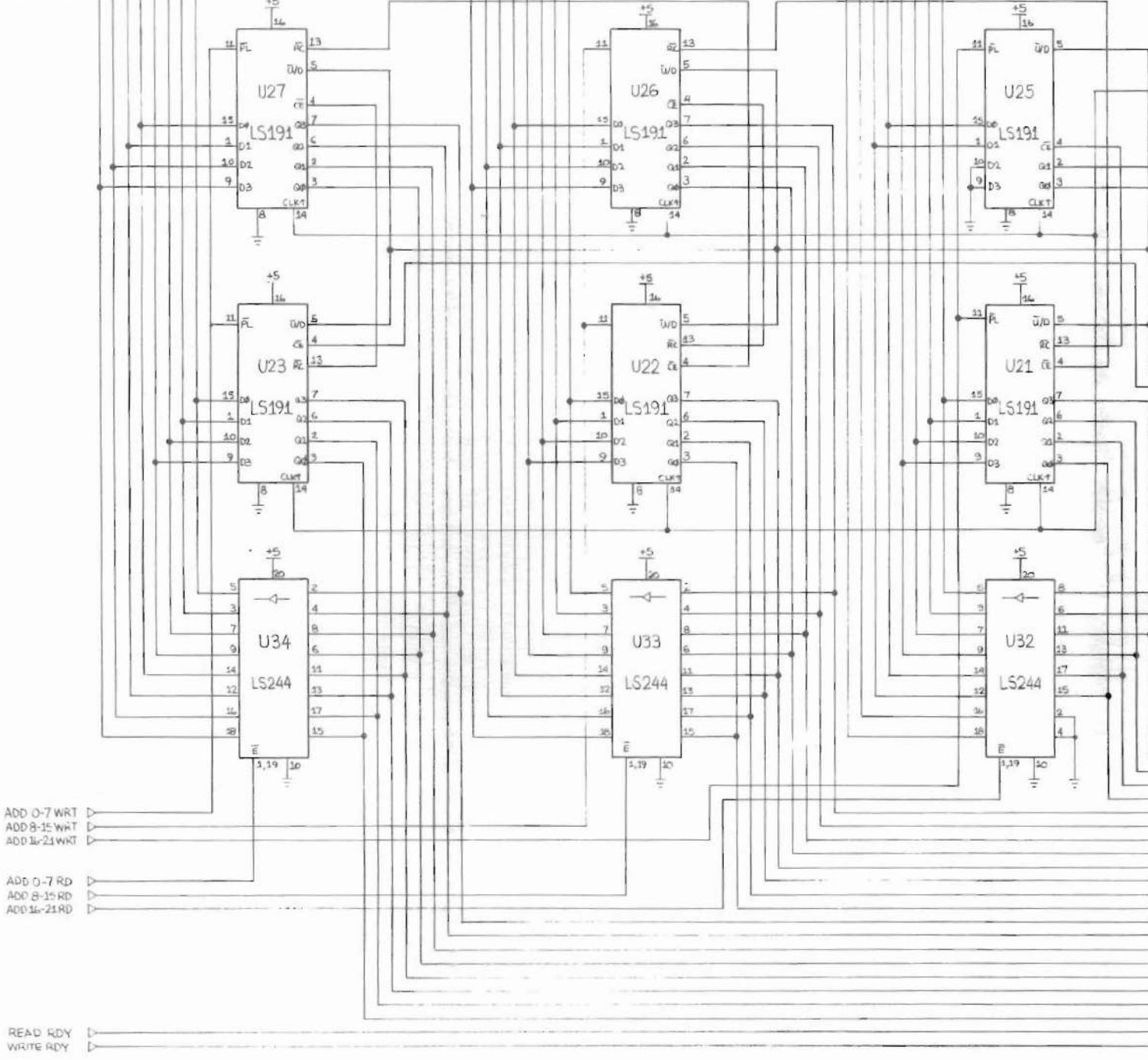
3

4

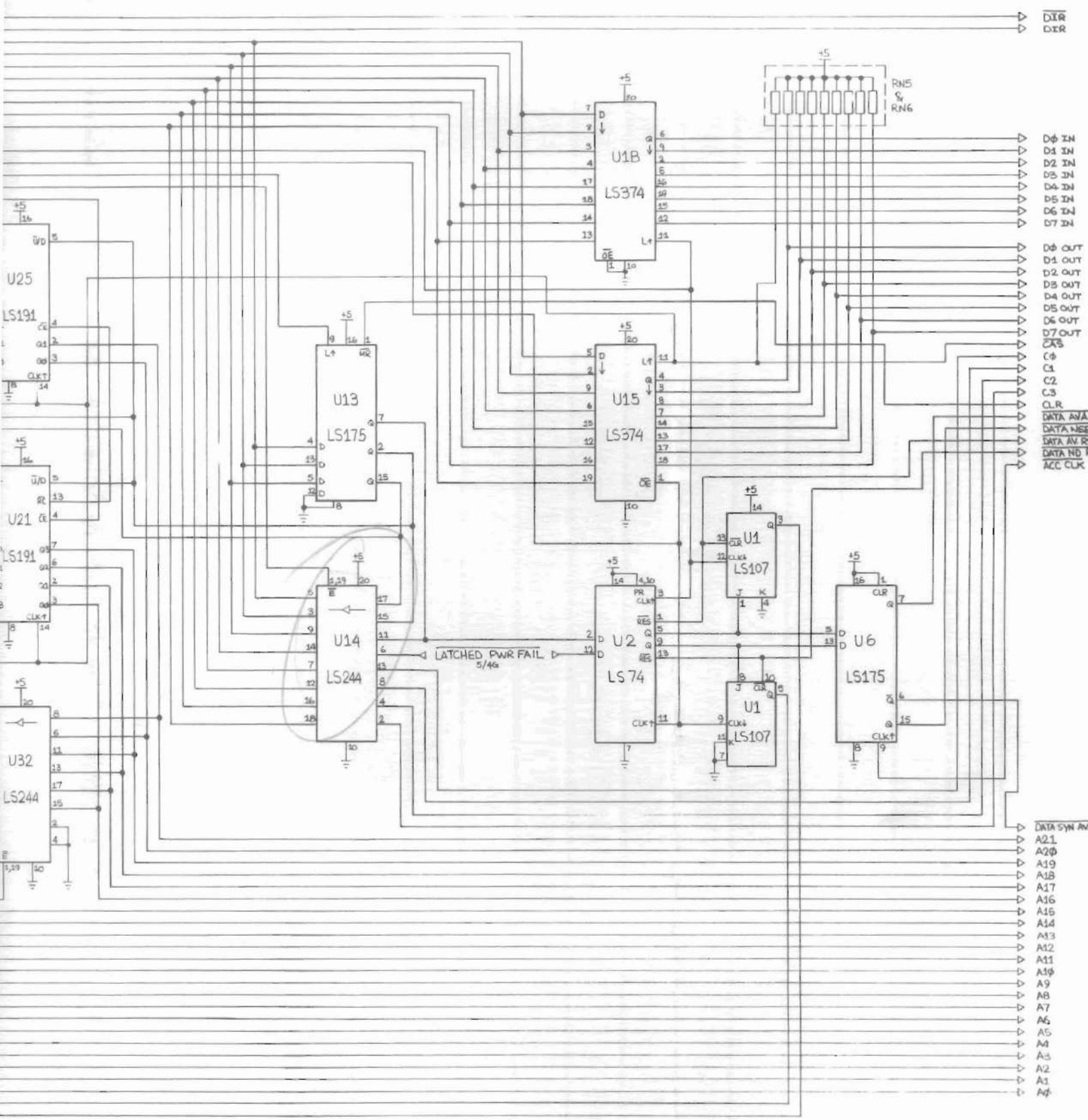
5

6

7



L | K | J | H | G | F



DO NOT SCALE TO DRAWING

REV	ZONE	DESCRIPTION	DATE	APPROVED
MODEL		QUASI-DISK	DWG DE-900006-00	
DESC		SEMICONDUCTOR DISK	REV-C00	
SCALE	NONE	SHT 2 OF 5	OWN BPZ	
CKD	APP	RPH	DATE 11/07/83	

