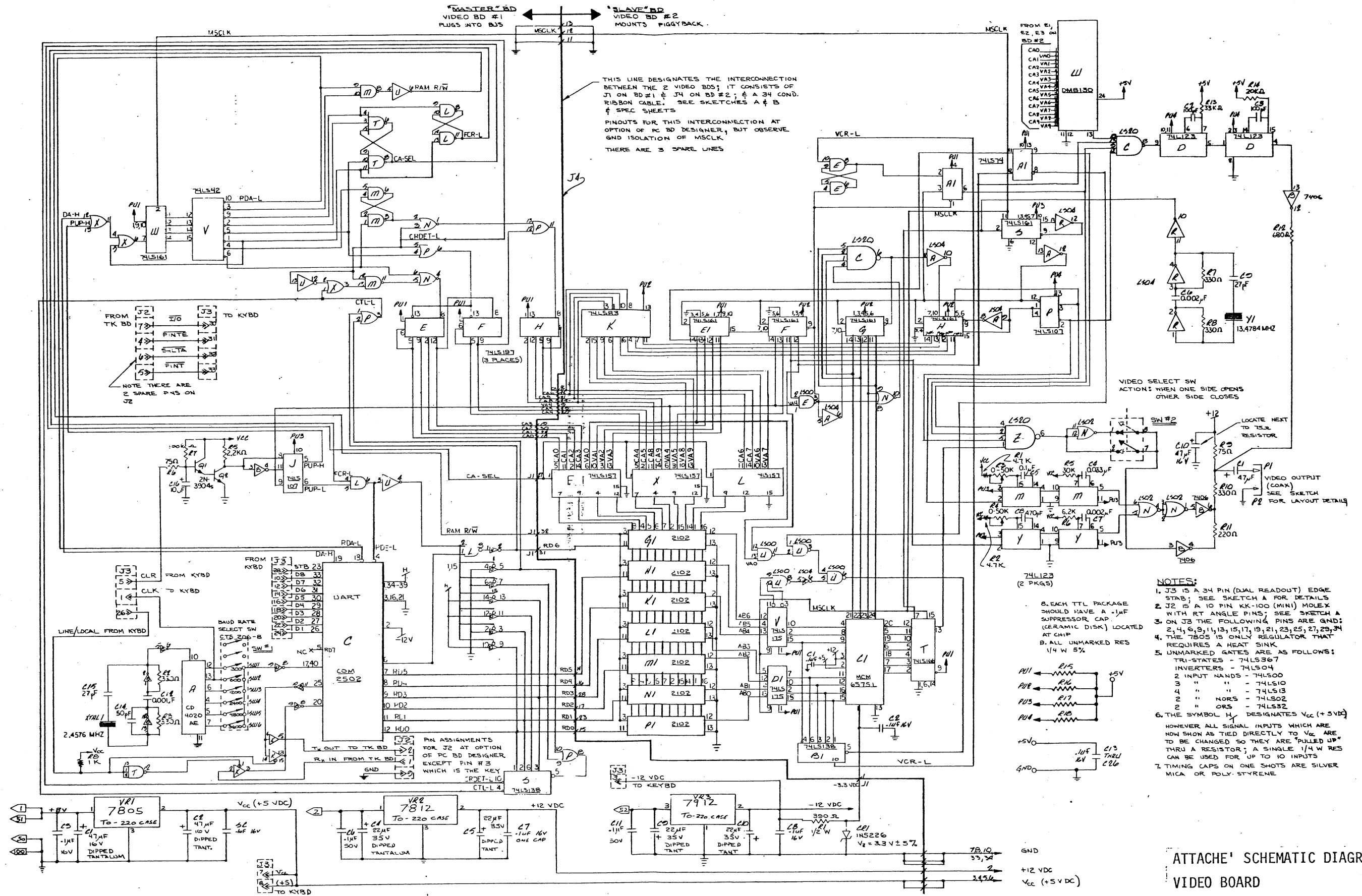


ATTACHE' SCHEMATIC DIAGRAMS
KEYBOARD

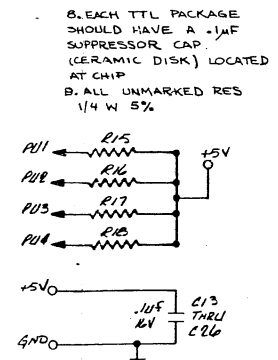


THIS LINE DESIGNATES THE INTERCONNECTION BETWEEN THE 2 VIDEO BDS; IT CONSISTS OF J1 ON BD #1 & J4 ON BD #2, & A 34 COND. RIBBON CABLE. SEE SKETCHES A & B & SPEC SHEETS
PINOUTS FOR THIS INTERCONNECTION AT OPTION OF PC BD DESIGNER, BUT OBSERVE GND ISOLATION OF MSCLK
THERE ARE 3 SPARE LINES

NOTE THERE ARE 2 SPARE PINS ON J2

VIDEO SELECT SW ACTION: WHEN ONE SIDE OPENS OTHER SIDE CLOSSES

- NOTES:
- J3 IS A 34 PIN (DUAL READOUT) EDGE STAB; SEE SKETCH A FOR DETAILS
 - J2 IS A 10 PIN KK-100 (MINI) MOLEX WITH RT ANGLE PINS; SEE SKETCH A
 - ON J3 THE FOLLOWING PINS ARE GND: 2, 4, 6, 8, 11, 13, 15, 17, 19, 21, 23, 25, 27, 29, 31
 - THE 7805 IS ONLY REGULATOR THAT REQUIRES A HEAT SINK
 - UNMARKED GATES ARE AS FOLLOWS:
TRI-STATES - 74LS367
INVERTERS - 74LS04
2 INPUT NANDS - 74LS00
3 " " - 74LS10
4 " " - 74LS13
2 " NORS - 74LS02
2 " ORS - 74LS32
 - THE SYMBOL V_{CC} DESIGNATES $V_{CC} (+5VDC)$
- HOWEVER ALL SIGNAL INPUTS WHICH ARE NOW SHOWN AS TIED DIRECTLY TO V_{CC} ARE TO BE CHANGED SO THEY ARE "PULLED UP" THRU A RESISTOR; A SINGLE 1/4 W RES CAN BE USED FOR UP TO 10 INPUTS
7. TIMING CAPS ON ONE SHOTS ARE SILVER MICA OR POLY. STYRENE



ATTACHE' SCHEMATIC DIAGRAMS
VIDEO BOARD