MURE -MAS 1 AS CE

MAC 3 A2 0 
MAC 4 A3 02 6 MAB / 2 MAG 3 MAG 4 MAS 4 MAS 5 MA3 /6 MA2 77 MAI 18 MAR 79 2 OF CLE 3 Y7 - 5 OL DL 7 Y5 -SWITCHING POWER SUPPLY 15472 15472 1051 502-0101 -+ + V TO 60m SG3524 IC101 ( JEE SHEET B "C" 5/28 )  $\begin{pmatrix} (Ma1) & 22 & M_1 & \frac{1}{\sqrt{10}} \\ (Ma1) & 22 & M_2 & \frac{1}{\sqrt{10}} \\ (Ma1) & 22 & M_3 & \frac{1}{\sqrt{10}} \\ (Ma1) & 23 & M_4 & \frac{1}{\sqrt{10}} \\ (Ma1) & 24 & M_4 & \frac{1}{\sqrt{10}} \\ (Ma1) & 25 & M_4 & \frac{1}{\sqrt{10$ PHASE - LOCKED LOOP CIRCUIT 1550 CE M9 2 M8 3 M87 4 Q 220 .5 1073 TYP 2.2K MC1658 470 2 VCE Q DE SENSE S III / mmeo/7 1077 1052 Q2 2N330Y L MINE IL/Z3\_ 2 ANI 520a C1 C1 Thuches CS WE CS WE JA 12 9 / MRCS MRCS MRWR 17 911 4.7K Tool CB THURE Q1 2H3506 ₹ 15 0E RN2 MAS | 1 | 2 | MAC | 3 | MAS | 4 | MAS | 4 | MAS | 1/2 1995 4 1995 4 1995 4 1995 16 1982 17 1982 17 1981 18 1988 19 823147 '5472 TYP 74/374 TYP 1056 19 3012 11 10 10 (ram) | 29 MRID | 1 MRID | 2 MRID | 2 MRID | 4 MRID | 4 MRID | 5 MRID | 7 MRID 153 A 1072 7415 395 2 mv7
5 Mv6
6 Mv5
7 mv9
12 Mv3
15 Mv2
15 Mv2
16 TrN1
7 tents 2 MSI 5 M30 6 M29 9 M21 12 M21 12 M21 15 M26 16 M25 17 M24 \_ MA3\_ MAS 3
MAS 3
MAS 4
MAS 5
MAS 6
MAS 6
MAS 7
MAS 8
MAS 7
MAS 7 2 CLK © 8° CB QA LO 9
2 CLK © 8° CB QA LO 9
2 EF 7445665
1C45
1C45
400 DD Q D6 QA \_Ma\_\_\_5 147314 TAP 1670 MRS 6
MRS 7
MRS 8
MRS 8
MRS 16
MRS 17 747 747 1057 2/64/3 TYP - MC2 -MC1 -MC0 -K74. 1055 11 1 Mare 24 Bu \_ mGd \_ /8 †3000 R2 \_mag\_ 18 C3 WE NRCS Ca WE 79 7/1 9 // MRCS. (3) 2 (3) 2 (1) 3 3 (10 A) 13 12 62) 1B5

1 OND

2 START

3 M42

1 DEN ABLE

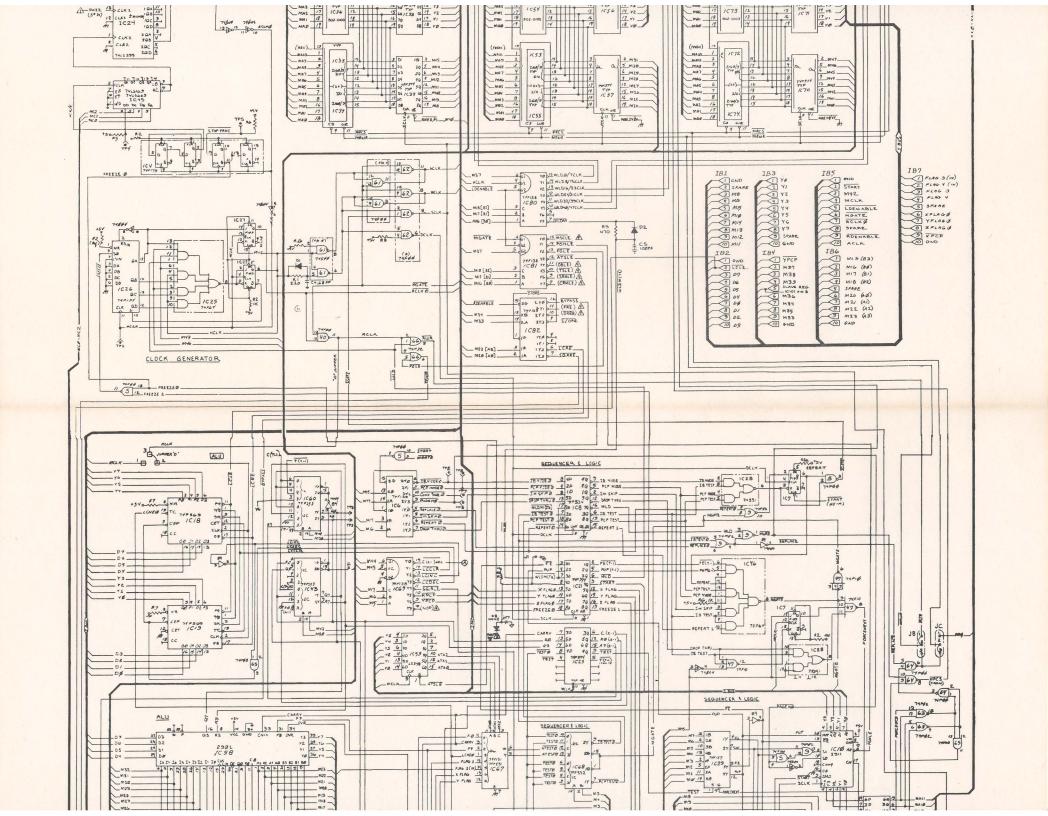
4 MOATE

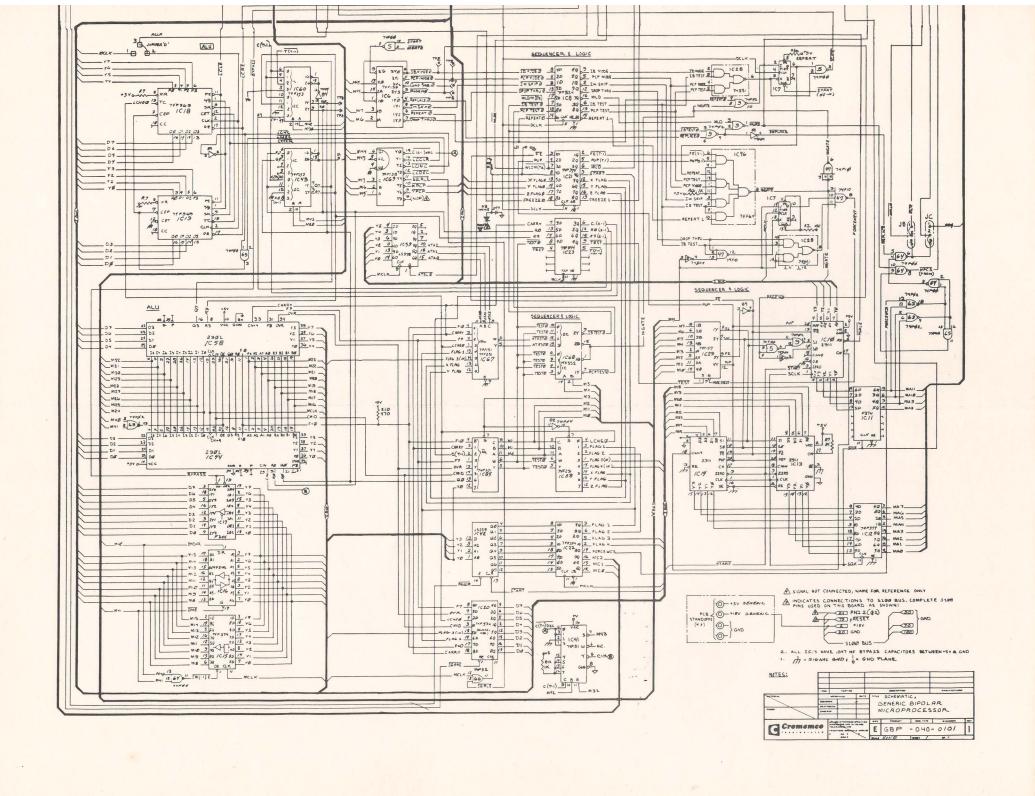
7 RCLKS

3 SPARE 183 1 FLAG 3 (IN)
2 FLAG 4 (IN)
3 FLAG 3
4 FLAG 4 183
1 YI
2 YI
3 Y2
4 Y3
5 Y4
6 Y5
7 Y6
8 Y7
9 SPARE 1060 11 19618 5 SPARE G XFLAGS T YFLAGS \_ MI7 [81] 74500 \_MIG [BUS] ₩ D2 8 XPLAG & 3 SPARE

3 RDENABLE

ACLK 14 62 6 DELA TO 15 MSCLE A 86 2 (PAR) 97 1 6)0 DI 74588 -O mil I CS IB6 IB2 74566 72 ATSLE
7/68/1 79 // (BBLE) A
1 50 // (TSLE) A
1 76 // (TSLE) A
1 76 // (TSLE) A
1 77 // (ZRMLE) A 74569 5 6 7 220 T CV, LB NO 182 1 6ND 2 LCLE. 3 D7 7 D6 5 D5 D7 D9 0 D1 0 D2 0 D9 MIS (83) 2 MID (88)
2 MID (88)
3 MIT (81)
4 MIB (87)
5 SPARE
6 M20 (40)
7 M21 (A1)
8 M22 (A2)
9 M23 (A3) 3 C 2 B \_ MIT [BU]\_ \_\_ MIG [BO) MOATE KCLK D. T/ORE-77/13 71 (FRE) A 77/13 71 (FRE) A (II. 1082 18 40 2 RWR 1 GG 8 4 74/32 5 GG 6 CLOCK GENERATOR S IL FASEZE 1





designation C BUS - SIDO BUS 10 74 M W/M W/M DI4 (II) DIS (II) AESET D +8V H A16 16 A17 5050 A1 (75) AIN ST A21 (62) PWR (22) A2 (77) A/5 (31) AIS OF SMEMA P 34 M. C. N. W. WID NO DO A22 GD SHLTA TE AG CAD DNA CHAPINEL GND CID 011 010 GND B GND TO AH DO AS DE GND (NO MAEG CEL DO1 (38) 6ND 30 SINTA SWO 01 20 RESH CAC DO# 000 A2 (77) 1989 AG (B) A4 B MEEG 32 AIS DE -IBV CED ADSS II PHI 2 PSTVAL DE 004 TE 005 TE 006 TE 00 SLAVECUA CETY MWAT COLD MWAT ZONG TO THE SET OF THE SET O 9888888 D3 3 APEN (39) A3 (13) A10 (13) A11 (13) A-3 (ED DY 20 EN MALABLE 8 AII OZ 06 00 D:3 (E) HALT (TA) PWRFAIL DMA3 RFU (EZ) (37) GND TO DIT TO 002 000 AS DE 5x-42 000 002 129 GND TT A12 (37) AIS SIXTN (33) A+ 020 SOUT TO SIDE REGISTERS IN DECODING 1029 60 15 D7 --I OLE -ACLK -MIBCB27 -SICLK -55LCLK \_\_\_ SDBQ/7CIK \_SDBQ/7RE I/O OUTPUT REG. LOAD CLOCKS 77 70 70 70 /4 A15 (\$P\$1) / (\$ Y0 015 SDCCLK-Y1 014 SU/RCLK-Y2 08 Y3 012 CAB0/7CLK-5 00 9 77 //75 76 /8 90 9 75 /9 12 12 12 13 13 15 74 /9 10 70 51 1 73 /9 10 70 51 1 72 /7 50 3 71 /9 60 60 50 1 72 /7 50 3 START I/O INPUT REG. READ ENABLES \_ SD875 -77.70 70 70 14 A23 (\$0.00)
10.88 80 80 (\$1.50)
11.50 10.50 1 T/ORE 5 612 -M22 [A2] -M2/CALT MZØ CAD INO DECODING / REG. CONTROL M35. SCOCLK \_ CP(77) SCIBE 50 15 D7 -50 12 D6 -30 6 D5 --PURPAIL (SOIS) 14 60 1 200 L DENABLE N39-\_M38-15 E 18 (2 (5 PAS) ) A (1 NF 17 RED) 1 (1 NF 1 INPUT READ ENABLE I ONE BAO - left +5V ANY ENABLES 77 0 78 AS (C92)

76 0 85 F G F AN (C93)

77 0 9 10 F AN (C93)

78 3 3 6 6 7 10 F AN (C93)

72 73 3 6 6 7 10 F AN (C93)

72 71 6 6 6 7 10 F AN (C93)

73 70 6 7 10 F AN (C93)

74 71 6 6 6 7 10 F AN (C93)

75 70 70 AN (C93)

76 70 AN (C93)

77 70 AN (C93)

78 70 AN (C93) 30 6 IORG (#38) 30 5 MI (\*36) 50 12 BUS AVAILABLE (\*\*4) ABB/ISCLK TSLE 10 45 COBOY TRE - 77 77 70 70 16.
- 76 9 9 9 90 17
- 75 9 18 20 5
- 74 9 15 20 5
- 73 3 10 636 0 2
- 73 7 86 63 0 6 HALT (C+C) Du((=12)\_ D2 (C=8)\_

