

September 2015

2N6520 PNP Epitaxial Silicon Transistor

Features

- · High Voltage Transistor
- Collector-Emitter Voltage: V_{CBO} = -350 V
- Collector Dissipation: P_C (max) = 625 mW
- · Complement to 2N6517



Ordering Information

Part Number	Top Mark	Package	Packing Method
2N6520TA	2N6520	TO-92 3L	Ammo

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-Base Voltage	-350	V
V _{CEO}	Collector-Emitter Voltage	-350	V
V _{EBO}	Emitter-Base Voltage	-5	V
I _C	Collector Current	-500	mA
I _B	Base Current	-250	mA
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 to 150	°C

Thermal Characteristics(1)

Values are at T_A = 25°C unless otherwise noted.

Symbol	Parameter	Max.	Unit
P _C	Collector Power Dissipation	625	mW
L.C.	Derate Above 25°C	5.0	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	200	°C/W

Note:

1. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

Electrical Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
BV _{CBO}	Collector-Base Breakdown Voltage	$I_C = -100 \mu A, I_E = 0$	-350		V
BV _{CEO}	Collector-Emitter Breakdown Voltage ⁽²⁾	$I_C = -1 \text{ mA}, I_B = 0$	-350		V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = -10 \mu A, I_C = 0$	-5		V
I _{CBO}	Collector Cut-Off Current	V _{CB} = -250 V, I _E = 0		-50	nA
I _{EBO}	Emitter Cut-Off Current	$V_{EB} = -4 \text{ V}, I_{C} = 0$		-50	nA
		$V_{CE} = -10 \text{ V}, I_{C} = -1 \text{ mA}$	20		
		V_{CE} = -10 V, I_{C} = -10 mA	30		
h _{FE}	DC Current Gain ⁽²⁾	V_{CE} = -10 V, I_{C} = -30 mA	30	200	
		V_{CE} = -10 V, I_{C} = -50 mA	20	200	
		$V_{CE} = -10 \text{ V}, I_{C} = -100 \text{ mA}$	15		
		I _C = -10 mA, I _B = -1 mA		-0.30	
\/ (aat)	Collector-Emitter Saturation Voltage	I _C = -20 mA, I _B = -2 mA		-0.35	V
V _{CE} (sat)		$I_C = -30 \text{ mA}, I_B = -3 \text{ mA}$		-0.50	
		I _C = -50 mA, I _B = -5 mA		-1.00	
		I _C = -10 mA, I _B = -1 mA		-0.75	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = -20 mA, I _B = -2 mA		-0.85	
		I _C = -30 mA, I _B = -3 mA		-0.90	
V _{BE} (on)	Base-Emitter On Voltage	$V_{CE} = -10 \text{ V}, I_{C} = -100 \text{ mA}$		-2	V
f _T	Current Gain Bandwidth Product ⁽²⁾	V _{CE} = -20 V, I _C = -10 mA, f = 20 MHz	40	200	MHz
C _{ob}	Output Capacitance	V _{CB} = -20 V, I _E = 0, f = 1 MHz		6	pF
C _{EB}	Emitter-Base Capacitance	V _{EB} = -0.5 V, I _C = 0, f = 1 MHz		100	pF
t _{ON}	Turn-On Time	V _{BE} (off) = -2 V, V _{CC} = -100 V, I _C = -50 mA, I _{B1} = -10 mA		200	ns
t _{OFF}	Turn-Off Time	V_{CC} = -100 V, I_{C} = -50 mA, I_{B1} = I_{B2} = -10 mA		3.5	ns

Note:

2. Pulse test: pulse width \leq 300 μ s, duty cycle \leq 2%

Typical Performance Characteristics

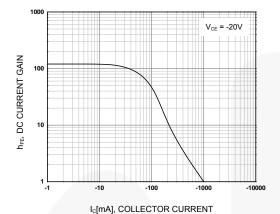


Figure 1. DC Current Gain

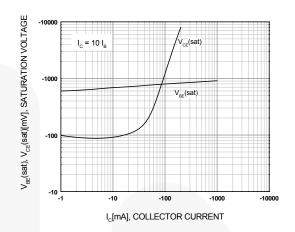


Figure 2. Base-Emitter Saturation Voltage and Collector-Emitter Saturation Voltage

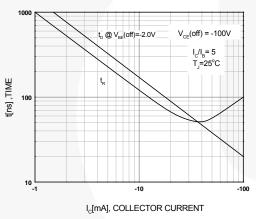


Figure 3. Turn-On Time

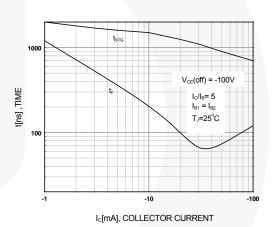


Figure 4. Turn-Off Time

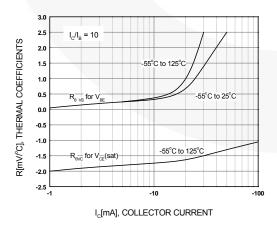


Figure 5. Temperature Coefficients

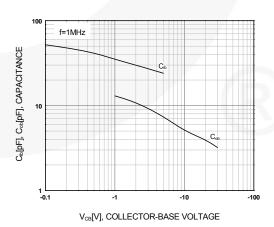


Figure 6. Capacitance

Typical Performance Characteristics (Continued)

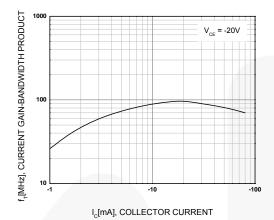
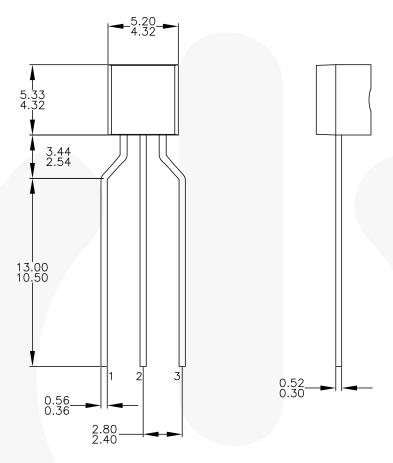
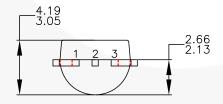


Figure 7. Current Gain Bandwidth Product

Physical Dimensions





NOTES: UNLESS OTHERWISE SPECIFIED

- DRAWING CONFORMS TO JEDEC MS-013, VARIATION AC. ALL DIMENSIONS ARE IN MILLIMETERS. DRAWING CONFORMS TO ASME Y14.5M-2009. DRAWING FILENAME: MKT-ZAO3FREV3. FAIRCHILD SEMICONDUCTOR.

Figure 8. 3-Lead, TO-92, Molded, 0.2 In Line Spacing Lead Form, Ammo Type





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Definition of Terms			
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