



NOTE: 1.-I/O-IV7 BUS LINES ARE ACTIVE LOW  
 2.-RAMS Z, A1-A7 ENABLED ON RB SELECT, REQUIRE ALL 256 I/O ADDRESSES ON RIGHT BANK

REF DESIG	PART NO.	VCC	GRD
A9, A9A, A11, A12	BT32	24	12
A13, X	74LS00	14	7
V	74LS04	14	7
U	74LS377	20	10
Y	99425	16	8
Z, A1, A2, A3, A4, A5, A6, A7			

FIGURE 4-9, SHEET 2 OF 2  
 88-HDSK PROCESSOR CARD