

Subject: SYSTEM TIMING MODIFICATION FOR ALTAIR 88-DCDD FLOPPY DISK

Date: SEPTEMBER 2, 1977

From: MITS ENGINEERING DEPARTMENT

To: FLOPPY DISK USERS

To increase diskette interchangeability from drive to drive and minimize Disk I/O errors, we have re-evaluated two time constants on the 88-DCDD Controller Board #1. The effect of the timing change is to center the data within the sector to allow a greater tolerance of Disk Drive misalignment.

Note that a diskette written with the new write delay should be marked with "NWD" for identification purposes. All Basic and DOS diskettes shipped from MITS after August 31, 1977 are written with the new write delay, and are marked "NWD". All diskettes written with the new write delay are compatible with the unmodified systems. To take advantage of the new write delay, one must also change the Read Clear Timing. Please follow the procedure below if you are making the modification. If your system does not require diskette interchangeability and you have not had difficulty with Disk I/O errors, the complete modification is not mandatory, although, we do recommend changing the write delay as in step IIA. The modification is strongly recommended for multiple drive systems or single drive systems where diskette interchange is required.

A modification kit is available at no charge to owners of Altair 88-DCDD Floppy Disk Systems. If you do not have the facilities for performing the modification, return your Controller Board #1 for special modification at no charge. The board will be modified except for replacement of R5, the Read Clear one shot timing resistor. The correct resistor for R5 will be returned with your board and should be installed after completion of re-writing or copying your diskettes as indicated in Section III of the modification procedure.

An important part of the modification includes changing the timing I.C. to 74LS221. This was done because 74LS221 is more stable and predictable than the 74123, and eliminates the need for trimming or adjusting the timing resistors.

I. PARTS REQUIRED (Included in the FDSK Mod. Kit.)

2 each	74LS221 I.C.	MITS PART # 101466	(F1, F4)
1 "	6.65K 1% resistor	" " # 102225	(R5)
1 "	12.1K 1% "	" " # 102226	(R12)
1 "	4.3K 5% "	" " # 101995	(R11)
1 "	8.2K 5% "	" " # 102090	(R6)

SYSTEM TIMING MODIFICATION FOR ALTAIR 88-DCDD FLOPPY DISK (Cont'd)

II MODIFICATION PROCEDURE (Controller Board #1 Only)

- A. Change the Write Clear one shot timing from 280us to 389us
 1. Remove R11 and R12
 2. Install a 4.3K, 5% resistor in the R11 position, and a 12.1K, 1% in the R12 position.
 3. Remove I.C. F4, install a 74LS221 in its place*
 4. If available, use an oscilloscope to measure the positive pulse width at TP8 (I.C. F4, pin 5). This step is not mandatory due to the timing predictability of 74LS221. The pulse width should be in the range of 355us to 425us (389us NOM \pm 10%) when the drive is enabled and a diskette installed.

- B. Copy all diskettes using the procedure listed in Section III. If you are not changing the Read Timing, it is not necessary to copy your diskettes.

- C. Change the Read Clear one shot timing from 140us to 214us.
 1. Remove R5 and R6
 2. Install a 6.65K, 1% resistor in the R5 position and an 8.2K, 5% resistor in the R6 position.
 3. Remove I.C. F1, install a 74LS221 in its place*
 4. If available, use an oscilloscope to measure the positive pulse width at TP5 (I.C. F1, pin 13). This step is not mandatory due to the timing predictability of the 74LS221. The pulse width should be in the range of 195us to 230us (214us NOM \pm 10%) when the drive is enabled and a diskette is installed.

- D. Change Schematic notation to coincide with the modification.

* If I.C.'s F1 and F4 are not socketed, remove the soldered in I.C.'s by cutting all the pins, then carefully removing each pin one by one. Clean the holes by using solder wick or a solder removing tool, being careful not to remove the plated portion of the hole. When soldering the new I.C.'s in place, solder each pin on both sides of the P.C. board to insure proper feed-through connection.

III COPY/REWRITE PROCEDURE

The Following procedures are recommended for copying disk software for the new Disk Read/Write modification:

CAUTION - All disk software copying should be done AFTER the Write modification has been made, but BEFORE the Read modification is made.

A. With a two or more disk drive system:

METHOD I - Using Disk BASIC "PIP" Utility Program. If you have Disk BASIC, versions 3.3, 3.4, 4.0, or 4.1, use the PIP utility program provided on the system diskette to copy onto a new diskette. A listing of PIP and instructions on its use are included at the end of this article.

- STEP 1:** Load Disk BASIC. Initialize the system for at least two disk drives, i.e. HIGHEST DISK NUMBER? should be answered with 1 or higher.
- STEP 2:** MOUNT the diskette with BASIC and PIP on it. Do not attempt to MOUNT a diskette that is brand new (never add BASIC or files on it).
- STEP 3:** LOAD PIP and type RUN.
- STEP 4:** Use the PIP Copy command to copy the old diskette (the one with BASIC and your files) onto the new diskette. COP takes approximately 30 minutes, so have patience.
- STEP 5:** Check the new diskette by re-loading BASIC (from the new diskette), MOUNTing, printing a directory of files to make sure everything was copied okay.
- STEP 6:** Now make the disk Read modification.

METHOD II - Using Disk BASIC "PIP" and DOS. If you have DOS (Disk Operating System) and Disk BASIC, you can use Disk BASIC and PIP to copy your DOS diskette. Follow the procedure in Method I, except:

- STEP 3:** LOAD "PIP" but UNLOAD the diskette with BASIC on it before RUNing PIP. Place the DOS diskette in the drive where BASIC was. You don't have to MOUNT to copy with PIP. Now RUN PIP, and proceed with STEP 4 of Method I.
- STEP 5:** Check the new diskette by loading DOS, MOUNTing, issuing a DIR command and possibly run a couple of the programs, and proceed with STEP 6 of Method I.

SYSTEM TIMING MODIFICATION FOR ALTAIR FLOPPY DISK

IMPORTANT

If assembling a new unit, disregard the component removal instructions and install new components in place of the components provided with the 88-DCDD kit.

I. Introduction

To increase Diskette interchangeability from Drive to Drive and minimize Disk I/O errors, two time constants on the 88-DCDD Controller Board #1 have been reevaluated. The effect of the timing change is to center the data within the Sector, allowing a greater tolerance of Disk Drive misalignment.

A Diskette written with the new write delay should be marked "NWD" for identification purposes. All BASIC and DOS Diskettes shipped from MITS after August 31, 1977, are written with the new write delay and are marked "NWD." All Diskettes written with the new write delay are compatible with unmodified systems. The Read Clear Timing must be changed to take advantage of the new write delay modification. When Diskette interchangeability is not required and there is no difficulty with Disk I/O Errors, the complete modification is not mandatory; however, changing the Write Delay as in Section II, Step A is recommended. The modification is strongly recommended for multiple Drive systems or single Drive systems where Diskette interchange is required.

If facilities for performing the modification are not available, return Controller Board #1 for special modification at no extra charge. The board will be modified except for replacement of the Read Clear One Shot timing resistor, R5. The correct resistor for R5 will be returned with the board and should be installed after all Disk software copying or rewriting is completed as indicated in Section IV of the modification procedure.

An important modification includes changing the timing IC to 74LS221. This is done because the 74LS221 is more stable and predictable than the 74123, and eliminates the need for trimming or adjusting the timing resistors.

The modification procedure is outlined in Section II and IC installation and identification instructions are provided in Section III. The recommended procedure for copying Disk software for single and multiple Drive systems is explained in Section IV.

The following is a list of the parts included in the modification kit:

<u>Quantity</u>	<u>Value</u>	<u>MTS Part #</u>	<u>Component Location</u>
2	74LS221 IC	101466	F1, F4
1	6.65K \pm 1% resistor	102225	R5
1	12.1K \pm 1% resistor	102226	R12
1	4.32K \pm 1% resistor	102227	R11
1	8.45K \pm 1% resistor	102228	R6

II. Modification Procedure

- A. Change the Write Clear One Shot timing from 280 μ s to 389 μ s.
1. Remove resistors R11 and R12. Be careful not to damage the plated portion around the holes in the PC board.
 2. Install a 4.32K ohm ($\pm 1\%$ tolerance) resistor in the R11 position and a 12.1K ohm ($\pm 1\%$ tolerance) resistor in the R12 location, according to the following instructions.
 - a. Using needle-nose pliers, bend the leads of the resistors at right angles to match their respective holes in the PC board.
 - b. Install the resistors into the correct holes in the silkscreened side of the board.
 - c. Holding the resistor in place with one hand, turn the board over and bend the two leads slightly outward.
 - d. Solder the leads to the foil pattern on the back side of the board. Then clip off any excess lead lengths.
 3. Remove IC F4 from its socket and install the 74LS221 IC in its place according to the installation instructions in Section III, Part C. If IC F4 is installed directly into the board, follow the removal and installation instructions in Section III, Part B.
 4. If available, use an oscilloscope to measure the positive pulse width at TP-8 (IC F4 pin 5). This step is not mandatory due to the timing predictability of the 74LS221 IC. The pulse width should be in the range of 355 μ s to 425 μ s (389 μ s nominal $\pm 10\%$) when the Drive is enabled and a Diskette installed.
- B. Copy all Diskettes using the procedures and instructions listed in Section IV. If the Read Timing is not to be changed, it is not necessary to copy the Diskettes.

- C. Change the Read Clear One Shot timing from $140\mu\text{s}$ to $214\mu\text{s}$.
1. Remove R5 and R6 being careful not to damage the plated portion around the holes in the PC board.
 2. Install a 6.65K ($\pm 1\%$ tolerance) resistor in the R5 position and an 8.25K ($\pm 1\%$ tolerance) resistor in the R6 position according to the resistor installation procedures followed in Part A, Step 2 of this section.
 3. Remove IC F1 from its socket and install the 74LS221 IC in its place according to the installation instructions in Section III, Part C. If IC F1 is installed directly into the board, follow the removal and installation instructions in Section III, Part B.
 4. If available, use an oscilloscope to measure the positive pulse width at TP-5 (IC F1 pin 13). This step is not mandatory due to the timing predictability of the 74LS221 IC. The pulse width should be in the range of $195\mu\text{s}$ to $230\mu\text{s}$ ($214\mu\text{s}$ nominal $\pm 10\%$) when the Drive is enabled and a Diskette is installed.
- D. Change the schematic to coincide with the modification.

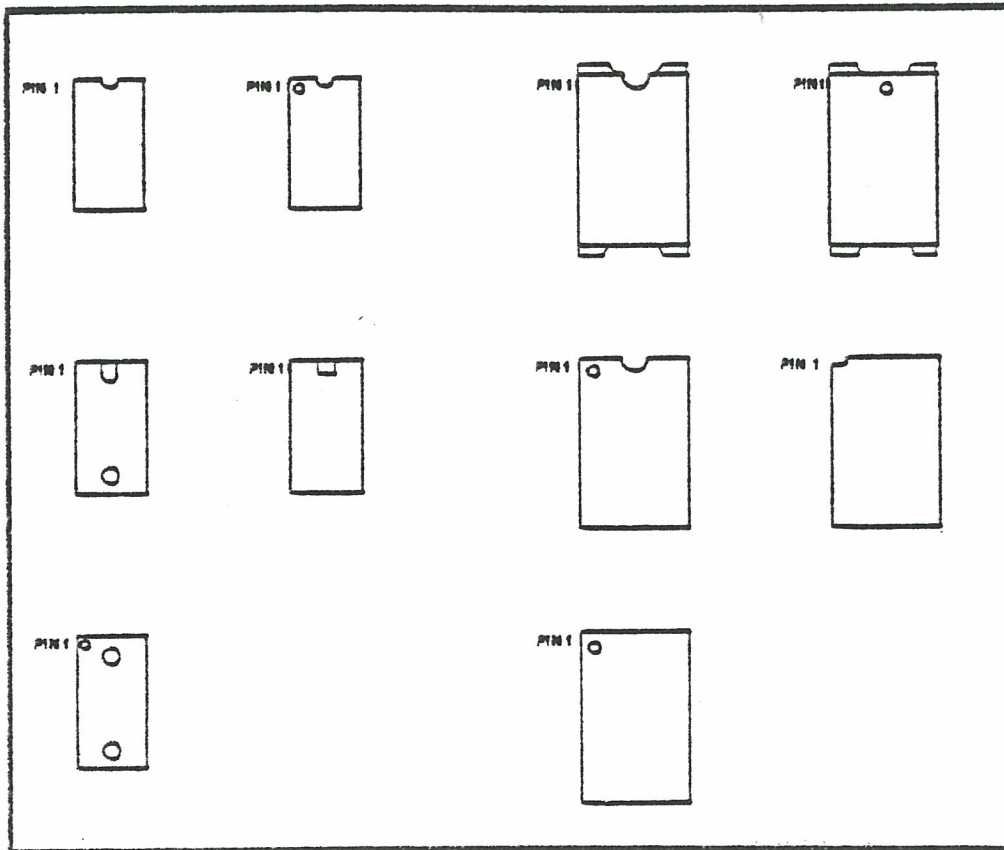
III. IC Identification and Installation

A. IC Identification Instructions.

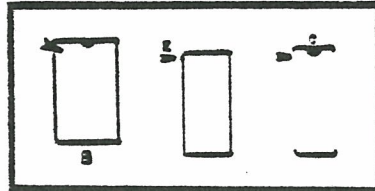
All integrated circuits (ICs) must be oriented so that the notched end is toward the arrowhead printed on the PC board. Pin 1 of the IC should correspond with the pad marked with the arrowhead. If the IC does not have a notch on one end, refer to the IC Identification Chart to identify pin 1.

ICs may have any one, or a combination, of several different markings which are very important in determining correct orientation when the ICs are placed on the printed circuit boards. Incorrect orientation of ICs may cause permanent damage.

IC Identification Chart



The chart below indicates the various methods used to show the position of ICs on the printed circuit boards. These outlines are silkscreened directly on the board. The arrowhead indicates the pin 1 position.



All ICs can be easily damaged and should be handled carefully. Always try to hold the IC by the ends, touching the pins as little as possible. When removing the IC from its holder, carefully straighten any bent pins with needle-nose pliers. All pins should be evenly spaced and should be aligned in a straight line, perpendicular to the body of the IC itself.

B. Removal and Installation of ICs Without Sockets.

1. Cut all the pins and carefully remove each pin one by one.
2. Clean the holes by using solder wick or a solder removing tool. Be careful not to remove the plated portion of the hole.
3. To install the new IC, orient the IC so that pin 1 coincides with the arrowhead on the PC board.
4. Align the pins on one side of the IC so that just the tips are inserted into the proper holes on the board.