19.2K, 38.4K and 76.8K Baud for the 88-2SIO Serial Interface Board

The MITS 88-2SIO board for the Altair has a baud rate jumper area in which common baud rates from 110 to 9600 can be independently connected to the two 6850 UARTs. With short jumper wires you can add higher baud rate clocks at 19.2K, 38.4K and 76.8K baud. This comes in very handy when transferring large files such as disk images. 38.4K baud is the maximum rate you can used with the 6850. To use 76.8K baud, install a 68B50.

The picture below shows how to pick up the clock for 19.2K baud from IC-G pin 3. The 38.4K baud clock is on IC-G pin 2, and 76.8K baud is on pin 1. Shown in the rear view is a wire running from IC-G pin 3 to an unused position in the baud rate jumper area. On the front view, a jumper can be seen connecting the new 19.2K baud clock to CK1 (baud rate clock for the 2nd 6850). Note: The angle of the camera makes CK0 and CK1 look like they're one hole higher than they really are in this photo. You may want to install a socket or socket strips into the baud rate jumper holes as shown below. This allows easy reconfiguration of baud rates using spare part leads as jumpers.

Unrelated to the mod for high baud rates, the rear-view of the board also shows a bare-wire jumper from the 9600 baud hole to an unused hole in the baud rate jumper area. This provides two separate sources for connecting to 9600 baud since this rate is such a common requirement. On the front view, the jumper for CK0 is shown connected to the extra 9600 baud hole. This leaves the original 9600 baud hole available for connecting to CK1 when needed.